

PATENT ASSIGNMENT

Electronic Version v1.1
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SUBMISSION TYPE:	NEW ASSIGNMENT																														
NATURE OF CONVEYANCE:	ASSIGNMENT																														
CONVEYING PARTY DATA																															
<table border="1" style="width:100%; border-collapse: collapse;"> <thead> <tr> <th style="width:70%;">Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Genesis Microchip Inc., a Delaware corporation</td> <td align="center">03/13/2009</td> </tr> <tr> <td>Genesis Microchip (Canada) Co.</td> <td align="center">03/13/2009</td> </tr> </tbody> </table>		Name	Execution Date	Genesis Microchip Inc., a Delaware corporation	03/13/2009	Genesis Microchip (Canada) Co.	03/13/2009																								
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Genesis Microchip Inc., a Delaware corporation	03/13/2009																														
Genesis Microchip (Canada) Co.	03/13/2009																														
RECEIVING PARTY DATA																															
Name:	Tamiras Per Pte. Ltd., LLC																														
Street Address:	160 Greentree Drive, Suite 101																														
City:	Dover																														
State/Country:	DELAWARE																														
Postal Code:	19904																														
PROPERTY NUMBERS Total: 14																															
<table border="1" style="width:100%; border-collapse: collapse;"> <thead> <tr> <th style="width:30%;">Property Type</th> <th>Number</th> </tr> </thead> <tbody> <tr><td>Patent Number:</td><td>5365277</td></tr> <tr><td>Patent Number:</td><td>5602599</td></tr> <tr><td>Patent Number:</td><td>5379241</td></tr> <tr><td>Patent Number:</td><td>5502662</td></tr> <tr><td>Patent Number:</td><td>5440653</td></tr> <tr><td>Patent Number:</td><td>5479454</td></tr> <tr><td>Patent Number:</td><td>5550764</td></tr> <tr><td>Patent Number:</td><td>5559905</td></tr> <tr><td>Patent Number:</td><td>6525586</td></tr> <tr><td>Patent Number:</td><td>5991463</td></tr> <tr><td>Patent Number:</td><td>6219464</td></tr> <tr><td>Patent Number:</td><td>6266092</td></tr> <tr><td>Patent Number:</td><td>6693496</td></tr> <tr><td>Application Number:</td><td>08133367</td></tr> </tbody> </table>		Property Type	Number	Patent Number:	5365277	Patent Number:	5602599	Patent Number:	5379241	Patent Number:	5502662	Patent Number:	5440653	Patent Number:	5479454	Patent Number:	5550764	Patent Number:	5559905	Patent Number:	6525586	Patent Number:	5991463	Patent Number:	6219464	Patent Number:	6266092	Patent Number:	6693496	Application Number:	08133367
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Patent Number:	6266092																														
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Application Number:	08133367																														

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CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	531811
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NAME OF SUBMITTER:	Kevan L. Morgan
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Total Attachments: 15
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ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Genesis Microchip Inc., a Delaware corporation, having an office at 1310 Electronics Drive, Carrollton, TX 75006 and Genesis Microchip (Canada) Co., a Nova Scotia Unlimited Liability Company organized under the laws of Canada, having an office at 165 Commerce Valley Dr. West, Thornhill, ON L3T 7V8 Canada (collectively "**Assignor**"), does hereby sell, assign, transfer, and convey unto Tamiras Per Pte. Ltd., LLC, a Delaware limited liability company, with an address at 160 Greentree Drive, Suite 101, Dover, DE 19904 ("**Assignee**"), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the "**Patent Rights**"):

(a) the provisional patent applications, patent applications and patents listed in the table below (the "**Patents**");

(b) all patents and patent applications, excluding the provisional patent applications, patent applications and patents listed in Table 2 below, (i) to which any of the Patents directly or indirectly claims priority, (ii) for which any of the Patents directly or indirectly forms a basis for priority;

(c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);

(d) all foreign patents, patent applications, and counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;

(e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents below and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;

(f) inventions, invention disclosures, and discoveries described in any of the Patents and/or any item in the foregoing categories (b) through (e) that (i) are included in any claim in the Patents and/or any item in the foregoing categories (b) through (e), (ii) are subject matter capable of being reduced to a patent claim in a reissue or reexamination proceedings brought on any of the Patents and/or any item in the foregoing categories (b) through (e), and/or (iii) could have been included as a claim in any of the Patents and/or any item in the foregoing categories (b) through (e);

(g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (f), including, without limitation, under the

Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;

(h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

- (1) damages,
- (2) injunctive relief, and
- (3) any other remedies of any kind

for past, current, and future infringement; and

(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h).

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
5,365,277 (08/133,372)	US	11/15/1994 (10/8/1993)	Apparatus With Reduction/Magnification Image Size Processing For Producing Low-Pass Filtered Images Greggain, Lance
5,602,599 (08/294,049)	US	2/11/1997 (8/24/1994)	Apparatus And Method Incorporating Digital Video Processing For Reduction/Magnification Of Digital Video Images To Produce Low-Pass Filtered Images Greggain, Lance
DE69419768 (DE94928739.5)	DE	7/28/1999 (10/3/1994)	Apparatus With Reduction/Magnification Image Size Processing For Producing Low-Pass Filtered Images Greggain, Lance
KR10-305237 (KR10-1996-0701744)	KR	7/27/2001 (10/3/1994)	Apparatus With Reduction/Magnification Image Size Processing For Producing Low-Pass Filtered Images Greggain, Lance
FR0722598 (FR94928739.5)	FR	7/28/1999 (10/3/1994)	Apparatus With Reduction/Magnification Image Size Processing and method For Producing Low-Pass Filtered Images; Greggain, Lance
GB0722598 (GB94928739.5)	GB	7/28/1999 (10/3/1994)	Apparatus With Reduction/Magnification Image Size Processing And Method For Producing Low-Pass Filtered Images Greggain, Lance

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
NL0722598 (NL94928739.5)	NL	7/28/1999 (10/3/1994)	Apparatus With Reduction/Magnification Image Size Processing For Producing Low-Pass Filtered Images Greggain, Lance
5,379,241 (08/172,065)	US	1/3/1995 (12/23/1993)	Method And Apparatus For Quadratic Interpolation Greggain, Lance
5,502,662 (08/329,163)	US	3/26/1996 (10/26/1994)	Method And Apparatus For Quadratic Interpolation Greggain, Lance
DE69423240 (DE95902733.5)	DE	3/1/2000 (12/13/1994)	Method And Apparatus For Quadratic Interpolation Greggain, Lance
FR0736206 (FR95902733.5)	FR	3/1/2000 (12/13/1994)	Method And Apparatus For Quadratic Interpolation Greggain, Lance
GB0736206 (GB95902733.5)	GB	3/1/2000 (12/13/1994)	Method And Apparatus For Quadratic Interpolation Greggain, Lance
NL0736206 (NL95902733.5)	NL	3/1/2000 (12/13/1994)	Method And Apparatus For Quadratic Interpolation Greggain, Lance
JP3760385 (JPH07-517062)	JP	1/20/2006 (12/13/1994)	Method And Apparatus For Quadratic Interpolation Greggain, Lance
KR10-286601 (10-1996-0702329)	KR	1/16/2001 (12/13/1994)	Method And Apparatus For Quadratic Interpolation Greggain, Lance
5,440,653 (08/126,388)	US	8/8/1995 (9/24/1993)	Image Mirroring And Image Extension For Digital Filtering Greggain, Lance; Mandl, Peter
DE69410811 (DE94928228.9)	DE	6/3/1998 (9/22/1994)	Image Mirroring And Image Extension For Digital Filtering (Title In German: Spiegelbild- Und Erweiterungsfunktion Für Digitale Filterung) Greggain, Lance; Mandl, Peter
FR0721632 (FR94928228.9)	FR	6/3/1998 (9/22/1994)	Image Mirroring And Image Extension For Digital Filtering Greggain, Lance; Mandl, Peter
GB0721632 (GB94928228.9)	GB	6/3/1998 (9/22/1994)	Image Mirroring And Image Extension For Digital Filtering Greggain, Lance; Mandl, Peter

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
NL0721632 (NL94928228.9)	NL	6/3/1998 (9/22/1994)	Image Mirroring And Image Extension For Digital Filtering Greggain, Lance; Mandl, Peter
KR10-322939 (KR10-1996- 0701524)	KR	1/21/2002 (9/22/1994)	A Method Of Image Extension, Image Filtering Of Image Boundary And Its Device For Digital Signal Processing System Greggain, Lance; Mandl, Peter
5,479,454 (08/124,201)	US	12/26/1995 (9/21/1993)	Digital Filter With Improved Numerical Precision Greggain, Lance; Mandl, Peter
5,550,764 (08/413,895)	US	8/27/1996 (3/30/1995)	Image Filtering With An Efficient Implementation Of High Order Decimation Digital Filters Mandl, Peter
5,559,905 (08/125,530)	US	9/24/1996 (9/22/1993)	Digital Image Resizing Apparatus Greggain, Lance; Mandl, Peter; Intihar, Bruce
DE69419167 (DE94928235.4)	DE	6/16/1999 (9/21/1994)	Digital Image Resizing Apparatus (Title In German: Digitale Vorrichtung Zur Änderung Der Grösse Eines Bildes Und Verwendungsmethode Desgleichen) Greggain, Lance; Mandl, Peter; Intihar, Bruce
FR0723688 (FR94928235.4)	FR	6/16/1999 (9/21/1994)	Digital Image Resizing Apparatus Greggain, Lance; Mandl, Peter; Intihar, Bruce
GB0723688 (GB94928235.4)	GB	6/16/1999 (9/21/1994)	Digital Image Resizing Apparatus And Method Of Using The Same Greggain, Lance; Mandl, Peter; Intihar, Bruce
NL0723688 (NL94928235.4)	NL	6/16/1999 (9/21/1994)	Digital Image Resizing Apparatus Greggain, Lance; Mandl, Peter; Intihar, Bruce
KR10-301543 (KR10-1996- 0701509)	KR	6/26/2001 (3/22/1996)	Digital Image Resizing Apparatus Greggain, Lance; Mandl, Peter; Intihar, Bruce
TW5163278 (TW20000128140)	TW	1/1/2003 (12/28/2000)	Digital Image Resizing Apparatus Greggain, Lance; Mandl, Peter; Intihar, Bruce

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
6,525,586 (09/986,640)	US	2/25/2003 (11/9/2001)	Programmable Delay Element Using Differential Technique Ahmed, Abdullah; Bizzan, Sami; Prather, Lawrence A.
5,991,463 (08/555,289)	US	11/23/1999 (11/8/1995)	Source Data Interpolation Method And Apparatus Greggain, Lance; Ngo, Calvin
JP3739405 (JP09-517699)	JP	11/11/2005 (10/22/1996)	Method And Apparatus For Video Source Data Interpolation Greggain, Lance; Ngo, Calvin
6,219,464 (09/382,119)	US	4/17/2001 (8/24/1999)	Source Data Interpolation Method And Apparatus Greggain, Lance; Ngo, Calvin
6,266,092 (09/075,836)	US	7/24/2001 (5/12/1998)	Method And Apparatus For Video Line Multiplication With Enhanced Sharpness Wang, Zhongde; Selby, Steve; Greggain, Lance
TW111244 (TW87116070)	TW	2/1/2000 (9/28/1998)	Method And Apparatus For Video Line Multiplication With Enhanced Sharpness Wang, Zhong De; Selby, Steve; Greggain, Lance
GB2337391 (GB9906152.5)	GB	5/8/2002 (3/17/1999)	Method And Apparatus For Video Line Multiplication With Enhanced Sharpness Wang, Zhong De; Selby, Steve; Greggain, Lance
KR10-0629981 (KR10-1999- 0015639)	KR	9/22/2006 (4/30/1999)	Method And Apparatus For Video Line Multiplication With Enhanced Sharpness Wang, Zhong De; Selby, Steve; Greggain, Lance
DE19920812.3	DE	5/6/1999	Interpolated Lines Creating Device For Conversion Of Interlaced Video Signals To Progressively Swept Format Without Interlacing (Title In German: Einrichtung Zum Erzeugen Einer Interpolierten Videozeile) Wang, Zhong De; Selby, Steve; Greggain, Lance
FR2779852 (FR9906081)	FR	6/18/2004 (5/12/1999)	Method And Apparatus For Video Line Multiplication With Enhanced Sharpness Wang, Zhong De; Selby, Steve; Greggain, Lance

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
NL1012021	NL	5/10/1999	Method And Apparatus For Video Line Multiplication With Enhanced Sharpness Wang, Zhong De; Selby, Steve; Greggain, Lance
JP11-131871	JP	5/12/1999	Method And Apparatus For Video Line Multiplication With Enhanced Sharpness Wang, Zhong De; Selby, Steve; Greggain, Lance
6,693,496 (10/098,758)	US	2/17/2004 (3/13/2002)	Method And System For Low Power, Low Jitter, Wide Range, Self-Adaptive Multi-Frequency Phase Locked Loop Lebouleux, Nicolas

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

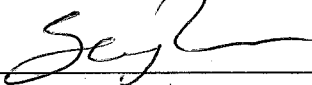
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The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Carrollton on March 13, 2009.

ASSIGNOR:

GENESIS MICROCHIP INC.

By: 
Name: Steven Rose
Title: Vice-President
(Signature MUST be attested)

ATTESTATION OF SIGNATURE PURSUANT TO 28 U.S.C. 1746

The undersigned witnessed the signature of Steven Rose ("Assignor's Representative") to the above Assignment of Patent Rights on behalf of Genesis Microchip Inc. and makes the following statements:

1. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.
2. Assignor's Representative is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me to execute the above Assignment of Patent Rights on behalf of Genesis Microchip Inc.
3. Assignor's Representative subscribed to the above Assignment of Patent Rights on behalf of Genesis Microchip Inc.


I declare under penalty of perjury under the laws of the United States of America that the statements made in the three (3) numbered paragraphs immediately above are true and correct.

EXECUTED on March 13, 2009 (date)

Signature: 
Print Name: Debbie Ramos

ASSIGNOR:

GENESIS MICROCHIP (CANADA) CO.

By: 
Name: Steven Rose
Title: Vice-President
(Signature *MUST* be attested)

ATTESTATION OF SIGNATURE PURSUANT TO 28 U.S.C. 1746

The undersigned witnessed the signature of Steven Rose ("Assignors' Representative") to the above Assignment of Patent Rights on behalf of Genesis Microchip (Canada) Co. of Nova Scotia and makes the following statements:

4. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.
5. Assignors's Representative is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me to execute the above Assignment of Patent Rights on behalf of Genesis Microchip (Canada) Co. of Nova Scotia
6. Assignors's Representative subscribed to the above Assignment of Patent Rights on behalf of Genesis Microchip (Canada) Co. of Nova Scotia

I declare under penalty of perjury under the laws of the United States of America that the statements made in the three (3) numbered paragraphs immediately above are true and correct.

EXECUTED on March 13, 2009 (date)


Signature: 
Print Name: Debbie Ramos

TABLE 2
Excluded Assets

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
5,225,910 (07/615,700)	US	7/6/1993 (11/20/1990)	Adaptive Operation Type Low Noise Television System Sugimori, Yoshio; Ito, Toshiya; Faroudja, Yves C.
JP2549025 (JPH03-096292)	JP	8/8/1996 (4/1/1991)	Adaptive Operation Type Low Noise Television System Sugimori, Yoshio; Ito, Toshiya; Faroudja, Yves C.
60/323,968	US	9/20/2001	Method and Apparatus for Synchronizing an Analog Video Signal to an LCD Monitor Neal, Greg
7,034,815 (10/071,409)	US	4/25/2006 (2/8/2002)	Method And Apparatus For Synchronizing An Analog Video Signal To An LCD Monitor Neal, Greg
11/264,261	US	10/31/2005	Method And Apparatus For Synchronizing An Analog Video Signal To An LCD Monitor Neal, Greg
60/620,094	US	10/18/2004	Virtual extended display identification data (EDID) Ali Noorbakhsh, David Keene, John Lattanzi, Ram Chilukuri
11/060,917	US	2/18/2005	Power Management in a display controller Ali Noorbakhsh, David Keene, John Lattanzi, Ram Chilukuri
11/061,151	US	2/18/2005	Virtual extended display information data (EDID) in a flat panel controller Ali Noorbakhsh, David Keene, John Lattanzi, Ram Chilukuri
11/061,165	US	2/18/2005	Method for acquiring extended display identification data (EDID) in a powered down EDID compliant display controller Ali Noorbakhsh, David Keene, John Lattanzi, Ram Chilukuri
60/561,042	US	4/9/2004	LCD Overdrive Data Compression For Reducing Memory Bandwidth And Data Threshold For Keeping Video Quality Wu, Che Ming; Wang, Vincent; Doung, Cheen

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
10/874,849	US	6/22/2004	Selective Use Of LCD Overdrive For Reducing Motion Artifacts In An LCD Device Wu, Che Ming; Wang, Vincent; Doung, Cheen
60/532,427	US	12/23/2003	Recursive hierarchical motion compensated frame rate conversion Hari N. Nair, Gordon Petrides, Peter Swartz, Steve Selby
7,346,109 (10/832,838)	US	3/18/2008 (4/26/2004)	Motion vector computation for video sequences Nari N. Nair, Gordon Petrides
11/924,463	US	10/25/2007	Motion vector computation for video sequences Hari N. Nair, Gordon Petrides
CN20041102089	CN	12/22/2004	Vector selection decision for pixel interpolation Hari N. Nair, Gordon Petrides
EP20040257855	EP	12/16/2004	Motion compensated frame rate conversion Hari N. Nair, Gordon Petrides
JP20040369560	JP	12/21/2004	Frame rate conversion for motion compensation Hari N. Nair, Gordon Petrides
KR20040100133	KR	12/2/2004	Motion compensated frame rate conversion Hari N. Nair, Gordon Petrides
SG20040006853	SG	11/24/2004	Motion compensated frame rate conversion Hari N. Nair, Gordon Petrides
60/170,607	US	12/14/1999	Method of Processing Data Greicar, Richard K.
60/170,668	US	12/14/1999	Multi-Component Processor Greicar, Richard K.
PCT/US2000/033117	WO	12/5/2000	Multi-Component Processor Greicar, Richard K.
6,775,757 (09/678,857)	US	8/10/2004 (10/2/2000)	Multi-Component Processor Greicar, Richard K.
10/875,364	US	6/23/2004	Multi-Component Processor Greicar, Richard K.

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
4,982,280 (07/381,497)	US	1/1/1991 (7/18/1989)	Motion sequence pattern detector for video Thomas C. Lyon, Jack J. Campbell
JP3187409 (JP02-187018)	JP	5/11/2001 (7/12/1990)	Motion sequence pattern detector Thomas C. Lyon, Jack J. Campbell

ASSIGNMENT OF RIGHTS IN CERTAIN ASSETS

For good and valuable consideration, the receipt of which is hereby acknowledged, Genesis Microchip Inc., a Delaware corporation, having an office at 1310 Electronics Drive, Carrollton, TX 75006, and Genesis Microchip (Canada) Co., a Nova Scotia Unlimited Liability Company organized under the laws of Canada, having an office at 165 Commerce Valley Dr. West, Thornhill, ON L3T 7V8 Canada (collectively "*Assignor*"), does hereby sell, assign, transfer, and convey unto Tamiras Per Pte. Ltd., LLC, a Delaware limited liability company, with an address at 160 Greentree Drive, Suite 101, Dover, DE 19904 ("*Assignee*"), or its designees, all of Assignor's right, title, and interest which Assignor has, if any, in and to any and all of the following provisional patent applications, patent applications, patents, and other governmental grants or issuances of any kind (the "*Certain Assets*"):

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventors</u>
PCT/CA94/000546	WO	10/3/1994	Apparatus With Reduction/Magnification Image Size Processing For Producing Low-Pass Filtered Images Greggain, Lance
EP0722598 (EP94928739.5)	EP	7/28/1999 (10/3/1994)	Apparatus With Reduction/Magnification Image Size Processing For Producing Low-Pass Filtered Images Greggain, Lance
MC0722598 (MC94928739.5)	MC	7/28/1999 (10/3/1994)	Apparatus With Reduction/Magnification Image Size Processing For Producing Low-Pass Filtered Images Greggain, Lance
PCT/CA94/000686	WO	12/13/1994	Method And Apparatus For Quadratic Interpolation Greggain, Lance
EP0736206 (EP95902733.5)	EP	3/1/2000 (12/13/1994)	Method And Apparatus For Quadratic Interpolation Greggain, Lance
IE0736206 (IE95902733.5)	IE	3/1/2000 (12/13/1994)	Method And Apparatus For Quadratic Interpolation Greggain, Lance
LU0736206 (LU95902733.5)	LU	3/1/2000 (12/13/1994)	Method And Apparatus For Quadratic Interpolation Greggain, Lance
MC0736206 (MC95902733.5)	MC	3/1/2000 (12/13/1994)	Method And Apparatus For Quadratic Interpolation Greggain, Lance

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventors</u>
08/133,367	US	10/8/1993	Image Filtering With An Efficient Implementation Of High Order Decimation Digital Filters Mandl, Peter
PCT/CA94/000545	WO	10/3/1994	Image Filtering With An Efficient Implementation Of High Order Decimation Digital Filters Mandl, Peter
PCT/CA94/000532	WO	9/21/1994	Digital Image Resizing Apparatus Greggain, Lance; Mandl, Peter; Intihar, Bruce
EP0723688 (EP94928235.4)	EP	6/16/1999 (9/21/1994)	Digital Image Resizing Apparatus And Method Of Using The Same Greggain, Lance; Mandl, Peter; Intihar, Bruce
MC0723688 (MC94928235.4)	MC	6/16/1999 (9/21/1994)	Digital Image Resizing Apparatus Greggain, Lance; Mandl, Peter; Intihar, Bruce
JP07-509469	JP	9/21/1994	Digital Image Resizing Apparatus Greggain, Lance; Mandl, Peter; Intihar, Bruce
JP07-509464	JP	9/22/1994	Image Mirroring And Image Extension For Digital Filtering Greggain, Lance; Mandl, Peter
MC0721632 (MC94928228.9)	MC	6/3/1998 (9/22/1994)	Image Mirroring And Image Extension For Digital Filtering Greggain, Lance; Mandl, Peter
PCT/CA94/000525	WO	9/22/1994	Image Mirroring And Image Extension For Digital Filtering Greggain, Lance; Mandl, Peter
PCT/CA94/000531	WO	9/21/1994	Digital Filter With Improved Numerical Precision Greggain, Lance; Mandl, Peter

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventors</u>
EP1093086 (EP00203305.8)	EP	1/23/2008 (9/25/2000)	Method And Apparatus For Digital Image Rescaling With Adaptive Contrast Enhancement Berbecel, Gheorghe; Selby, Steve
PCT/CA96/000703	WO	10/22/1996	Method And Apparatus For Video Source Data Interpolation Greggain, Lance; Ngo, Calvin
EP96934253.4	EP	10/22/1996	Method And Apparatus For Video Source Data Interpolation Greggain, Lance; Ngo, Calvin
KR10-1998-0703428	KR	10/22/1996	Method And Apparatus For Video Source Data Interpolation Greggain, Lance; Ngo, Calvin

Assignor assigns to Assignee all of Assignor's rights to the inventions, invention disclosures, and discoveries which Assignor has, if any, in the assets listed above, together, with the rights, if any, to revive prosecution of claims under such assets and to sue or otherwise enforce any claims under such assets for past, present or future infringement.

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to make available to Assignee all records regarding the Certain Assets.


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The terms and conditions of this Assignment of Rights in Certain Assets will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

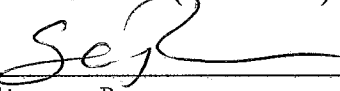
DATED this 13 day of March 2009.

ASSIGNOR:

GENESIS MICROCHIP INC.

By: 
Name: Steven Rose
Title: Vice-President

GENESIS MICROCHIP (CANADA) CO.

By: 
Name: Steven Rose
Title: Vice-President