PATENT ASSIGNMENT

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NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
SANYO Electric Co., Ltd.	09/17/2008

RECEIVING PARTY DATA

Name:	Patrenella Capital Ltd., LLC
Street Address:	1209 Orange Street
City:	Wilmington
State/Country:	DELAWARE
Postal Code:	19801

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	12471059

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Total Attachments: 23
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Exhibit B

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, SANYO Electric Co., Ltd., a Japanese corporation, with an office at 2-5-5 Keihan Hondori, Moriguchi-City, Osaka 570-8677, Japan (***"Assignor"***), does hereby sell, assign, transfer, and convey unto Patrenella Capital Ltd., LLC, a Delaware limited liability company, having an address at 1209 Orange Street, Wilmington, Delaware 19801 (***"Assignee"***), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the ***"Patent Rights"***):

(a) the provisional patent applications, patent applications and patents listed in the table below (the ***"Patents"***);

Patent or Application No.	**Country**	**Filing Date**	**Title of Patent and First Named Inventor**
JP2000-021596	JP	1/31/2000	Device and method for providing solid model Matsumoto, Yukinori; Fujimura, Kota; Sugimoto, Kazuhide; Oue, Yasuhiro; Kitamura, Toru; Ota, Osamu
EP00904031.2	EP	2/18/2000	Real 3-D model providing device Matsumoto, Yukinori; Fujimura, Kota; Sugimoto, Kazuhide; Oue, Yasuhiro; Kitamura, Toru; Ota, Osamu
7,006,952 (09/913,095)	US	2/28/2006 (2/18/2000)	3-D model providing device Matsumoto, Yukinori; Fujimura, Kota; Sugimoto, Kazuhide; Oue, Yasuhiro; Kitamura, Toru; Ota, Osamu
EP00904032.0	EP	2/18/2000	3-D model providing device Matsumoto, Yukinori; Fujimura, Kota; Sugimoto, Kazuhide; Oue, Yasuhiro; Kitamura, Toru; Ota, Osamu
6,977,651 (09/913,270)	US	12/20/2005 (2/18/2000)	3-D model providing device Matsumoto, Yukinori; Fujimura, Kota; Sugimoto, Kazuhide; Oue, Yasuhiro; Kitamura, Toru; Ota, Osamu
6,965,690 (09/847,011)	US	11/15/2005 (5/1/2001)	Three-dimensional modeling apparatus, method, and medium, and three-dimensional shape data recording apparatus, method, and medium Matsumoto, Yukinori

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
JP2001-255261	JP	8/24/2001	Three dimensional modeling apparatus Terauchi, Tomoya; Fujimura, Kouta; Hojo, Mikio
6,809,728 (10/225,382)	US	10/26/2004 (8/21/2002)	Three dimensional modeling apparatus Terauchi, Tomoya; Fujimura, Kouta; Hojo, Mikio
7,098,998 (10/879,756)	US	8/29/2006 (6/30/2004)	Depth measuring method and depth measuring apparatus Terauchi, Tomoya; Oue, Yasuhiro
6,538,670 (09/487,808)	US	3/25/2003 (1/20/2000)	Pointing method Kido, Kazutaka
6,611,242 (09/501,223)	US	8/26/2003 (2/10/2000)	Information transmission system to transmit work instruction information Hongo, Hitoshi; Yasumoto, Mamoru
JP3877514 (JP2000-361680)	JP	11/10/2006 (11/28/2000)	System for monitoring entrance and exit Makiyama, Soichiro; Yoshida, Hiroshi
6,617,970 (09/989,189)	US	9/9/2003 (11/21/2001)	Ingress-egress monitoring system Makiyama, Soichiro; Yoshida, Hiroshi
JP3789721 (JP2000-098746)	JP	4/7/2006 (3/31/2000)	Burglary prevention device Matsudaira, Shinji
ZL01116773.4 (CN01116773.4)	CN	2/11/2004 (3/31/2001)	Theft-proof system Matsudaira, Shinji
6,531,961 (09/822,416)	US	3/11/2003 (4/2/2001)	Antitheft system Matsudaira, Shinji
JP3540661 (JPH11-070424)	JP	4/2/2004 (3/16/1999)	Magneto-optical recording medium Noguchi, Hitoshi; Yamaguchi, Atsushi
JP3540659 (JPH11-066876)	JP	4/2/2004 (3/12/1999)	Magneto-optical recording medium Noguchi, Hitoshi; Yamaguchi, Atsushi
CN99801981.X	CN	8/18/1999	Magneto-optic recording medium Yamaguchi, Atsushi; Takagi, Naoyuki; Mitani, Kenichiro; Noguchi, Hitoshi
KR10-0574734 (KR10-2000-7004694)	KR	4/21/2006 (8/18/1999)	Magneto-optic recording medium

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			Yamaguchi, Atsushi; Takagi, Naoyuki; Mitani, Kenichiro; Noguchi, Hitoshi
EP99939617.9	EP	8/18/1999	Magneto-optic recording medium Yamaguchi, Atsushi; Takagi, Naoyuki; Mitani, Kenichiro; Noguchi, Hitoshi
6,492,035 (09/530,497)	US	12/10/2002 (8/18/1999)	Magneto-optical recording medium with intermediate layer having a controlled saturation magnetization Yamaguchi, Atsushi; Takagi, Naoyuki; Mitani, Kenichiro; Noguchi, Hitoshi
6,590,836 (09/669,454)	US	7/8/2003 (9/25/2000)	Magneto optical recording medium capable of preventing a reproduction layer from having a degraded characteristic Yamaguchi, Atsushi; Noguchi, Hitoshi; Ishida, Hiroki
JP3568476 (JP2000-528981)	JP	6/25/2004 (1/21/1999)	Reproducing method for magneto-optic recording medium and magneto-optic disk device Takagi, Naoyuki; Yamaguchi, Atsushi; Mitani, Kenichiro
KR10-0385406 (KR10-1999-7008622)	KR	5/14/2003 (1/21/1999)	Reproducing method for magneto-optic recording medium and magneto-optic disk device Takagi, Naoyuki; Yamaguchi, Atsushi; Mitani, Kenichiro
6,650,599 (09/922,836)	US	11/18/2003 (8/7/2001)	Method and apparatus for determining power level of laser beam in magneto-optical recording device Takagi, Naoyuki; Yamaguchi, Atsushi; Mitani, Kenichiro
6,693,856 (09/953,194)	US	2/17/2004 (9/17/2001)	Magneto-optical disk apparatus capable of accurate reproduction of signal by removing magnetic influence by magnet included in optical head and method of detecting intensity of magnetic field applied by magnet Shidochi, Masaaki; Koyama, Kanichi; Tsuchiya, Yoichi; Maeda, Mitsuhiko;

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			Kajiyama, Seiji; Takahashi, Seiichiro
6,697,304 (10/247,691)	US	2/24/2004 (9/20/2002)	Method and apparatus for recording with a magneto-optical recording medium Yamaguchi, Atsushi; Takagi, Naoyuki
6,920,087 (10/395,701)	US	7/19/2005 (3/25/2003)	Magneto-optical recording apparatus capable of adjusting the magnetic head Sumi, Satoshi; Suzuki, Yoshihisa; Okada, Sayoko
JP3505497 (JP2000-285667)	JP	12/19/2003 (9/20/2000)	Dielectric element Matsushita, Shigeharu; Harada, Mitsuaki
6,720,096 (09/711,512)	US	4/13/2004 (11/14/2000)	Dielectric element Matsushita, Shigeharu; Harada, Mitsuaki
KR10-0744280 (KR10-2000-0067936)	KR	7/24/2007 (11/16/2000)	Dielectric element Harada, Mitsuaki; Matsushita, Shigeharu
6,762,476 (10/060,260)	US	7/13/2004 (2/1/2002)	Dielectric element including oxide dielectric film and method of manufacturing the same Matsushita, Shigeharu; Gueshi, Tatsurou
CN02808544.2	CN	4/15/2002	Ferroelectric memory and operating method therefor Matsushita, Shigeharu
JP2002-584333	JP	4/15/2002	Ferroelectric memory and operating method therefor Matsushita, Shigeharu
KR10-0745938 (KR10-2003-7013578)	KR	7/27/2007 (4/15/2002)	Ferroelectric memory and operating method therefor Matsushita, Shigeharu
TW201021 (TW91107546)	TW	4/21/2004 (4/15/2002)	Ferroelectric memory and operating method therefor Matsushita, Shigeharu
6,785,155	US	8/31/2004	Ferroelectric memory and operating

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
(10/124,262)		(4/18/2002)	method therefor Matsushita, Shigeharu
JP2002-330172	JP	11/14/2002	Memory device Sakai, Takeshi; Ishizuka, Yoshiyuki; Matsushita, Shigeharu
6,816,398 (10/308,064)	US	11/9/2004 (12/3/2002)	Memory device Sakai, Takeshi; Matsushita, Shigeharu; Ishizuka, Yoshiyuki
CN02155780.2	CN	12/4/2002	Storage device Sakai, Takeshi; Matsushita, Shigeharu; Ishizuka, Yoshiyuki
6,888,189 (09/956,817)	US	5/3/2005 (9/21/2001)	Dielectric element including oxide-based dielectric film and method of fabricating the same Matsushita, Shigeharu; Honma, Kazunari
CN02814416.3	CN	7/12/2002	Semiconductor memory device Takano, Yoh; Matsushita, Shigeharu
JP3920851 (JP2003-514560)	JP	2/23/2007 (7/12/2002)	Semiconductor memory device Takano, Yoh; Matsushita, Shigeharu
KR10-0582148 (KR10-2004-7000684)	KR	5/15/2006 (7/12/2002)	Semiconductor memory device Takano, Yoh; Matsushita, Shigeharu
6,891,742 (10/480,247)	US	5/10/2005 (7/12/2002)	Semiconductor memory device Takano, Yoh; Matsushita, Shigeharu
KR10-0589569 (KR10-2006-7004648)	KR	6/7/2006 (7/12/2002)	Semiconductor memory device Takano, Yoh; Matsushita, Shigeharu
JP2007-008756	JP	1/18/2007	Semiconductor memory device Takano, Yoh; Matsushita, Shigeharu
JP3979947 (JP2003-027209)	JP	7/6/2007 (2/4/2003)	Ferroelectric memory Matsushita, Shigeharu
6,901,002 (10/768,018)	US	5/31/2005 (2/2/2004)	Ferroelectric memory Matsushita, Shigeharu
KR10-0682435	KR	2/7/2007	Ferroelectric memory

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
(KR10-2004-0007018)		(2/3/2004)	Matsushita, Shigeharu
CN200410003104.4	CN	2/4/2004	Ferroelectric memory Matsushita, Shigeharu
JP4046523 (JP2002-071148)	JP	11/30/2007 (3/15/2002)	Ferroelectric memory Matsushita, Shigeharu
CN03121676.5	CN	3/14/2003	Strong inductor memory and its action method and memory device Matsushita, Shigeharu; Takano, Yoh; Sekine, Satoru
KR10-0671385 (KR10-2003-0016003)	KR	1/12/2007 (3/14/2003)	Ferroelectric memory and operating method therefor, and memory device Matsushita, Shigeharu; Takano, Yoh; Sekine, Satoru
6,930,906 (10/387,869)	US	8/16/2005 (3/14/2003)	Ferroelectric memory and operating method therefor, and memory device Matsushita, Shigeharu; Takano, Yoh; Sekine, Satoru
JP2003-146124	JP	5/23/2003	Memory device TAKANO, Yo; MATSUSHISTA, Shigeharu; SEKINE, Satoru
6,975,530 (10/721,252)	US	12/13/2005 (11/26/2003)	Memory device comprising hysteretic capacitance means Takano, Yoh
JP4024220 (JP2004-050968)	JP	10/12/2007 (2/26/2004)	Memory Sakai, Naofumi; TAKANO, Yo
CN200410007945.2	CN	3/5/2004	Memory Inventorship not available
KR10-0529989 (KR10-2004-0015025)	KR	11/14/2005 (3/5/2004)	Memory Sakai Naoshi; Takano Hiroshi
7,016,217 (10/792,926)	US	3/21/2006 (3/5/2004)	Memory Sakai, Naofumi; Takano, Yoh
JP3970259 (JP2004-126244)	JP	6/15/2007 (4/22/2004)	Memory MIYAMOTO, Hideaki; ISHIZUKA,

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			Yoshiyuki; SAKAI, Naofumi
7,110,279 (10/935,554)	US	9/19/2006 (9/8/2004)	Memory Miyamoto, Hideaki; Ishizuka, Yoshiyuki; Sakai, Naofumi
KR10-0610283 (KR10-2004-0072225)	KR	8/1/2006 (9/9/2004)	Memory Ishizuka, Yoshiyuki; Miyamoto, Hideaki; Sakai, Naofumi
CN200410075264.X	CN	9/13/2004	Memory Miyamoto, Hideaki; Ishizuka, Yoshiyuki; Sakai, Naofumi
JP2003-344467	JP	10/2/2003	Semiconductor storage device TAKANO, Yo
7,133,305 (10/947,367)	US	11/7/2006 (9/23/2004)	Semiconductor memory device Takano, Yoh
JP2001-366527	JP	11/30/2001	Ferroelectric memory Matsushita, Shigeharu
7,167,386 (10/304,691)	US	1/23/2007 (11/27/2002)	Ferroelectric memory and operating method therefor Matsushita, Shigeharu
CN02154317.8	CN	11/29/2002	Strong media memory and operation method thereof Matsushita, Shigeharu
JP2005-241943	JP	8/24/2005	Memory Miyamoto, Hideaki; Matsushita, Shigeharu
CN200610121356.6	CN	8/22/2006	Memory Matsushita, Shigeharu; Miyamoto, Hideaki
KR10-2006-79243	KR	8/22/2006	Memory Matsushita, Shigeharu; Miyamoto, Hideaki
11/509,057	US	8/24/2006	Memory Miyamoto, Hideaki; Matsushita, Shigeharu

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
JP2006-085628	JP	3/27/2006	Memory Miyamoto, Hideaki
CN200710088480.1	CN	3/27/2007	Memory Miyamoto, Hideaki
KR10-2007-0029951	KR	3/27/2007	Memory Miyamoto, Hideaki
11/727,466	US	3/27/2007	Memory Miyamoto, Hideaki
JP3625622 (JPH09-217550)	JP	12/10/2004 (8/12/1997)	Device for preparing stereoscopic model and method therefor and medium for recording program for preparing stereoscopic model Matsumoto, Yukinori; Terasaki, Hajime; Sugimoto, Kazuhide; Arakawa, Tsutomu
JP3625624 (JPH09-234829)	JP	12/10/2004 (8/29/1997)	Texture information imparting method, medium recorded with texture information imparting program and texture information imparting device Matsumoto, Yukinori
6,356,272 (09/254,127)	US	3/12/2002 (8/28/1997)	Texture information giving method, object extracting method, three-dimensional model generating method and apparatus for the same Matsumoto, Yukinori; Terasaki, Hajime; Sugimoto, Kazuhide; Arakawa, Tsutomu
6,847,371 (09/754,282)	US	1/25/2005 (1/5/2001)	Texture information assignment method, object extraction method, three-dimensional model generating method, and apparatus thereof Matsumoto, Yukinori; Terasaki, Hajime; Sugimoto, Kazuhide; Arakawa, Tsutomu
7,106,348 (09/994,829)	US	9/12/2006 (11/28/2001)	Texture information assignment method, object extraction method, three-dimensional model generating method, and apparatus thereof

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			Matsumoto, Yukinori; Terasaki, Hajime; Sugimoto, Kazuhide; Arakawa, Tsutomu
6,363,169 (09/120,468)	US	3/26/2002 (7/23/1998)	Apparatus and method of three-dimensional modeling Ritter, Dieter; Matsumoto, Yukinori; Sugimoto, Kazuhide; Arakawa, Tsutomu
JP3813343 (JPH10-005982)	JP	6/9/2006 (1/14/1998)	Three-dimensional modeling device, three-dimensional method, medium recording three-dimensional modeling program and medium recording three-dimensional shape estimation program Matsumoto, Yukinori
6,256,036 (09/149,046)	US	7/3/2001 (9/8/1998)	Three-dimensional modeling apparatus for producing three-dimensional model of object of interest and method therefor Matsumoto, Yukinori
JP3384741 (JPH10-052278)	JP	12/27/2002 (3/4/1998)	Thin film magnetic head and its production Umeda, Katsumi; Shimizu, Yoshiaki; Tatezono, Fumio; Matono, Naoto; Ogura, Takashi; Okuda, Hiroyuki
6,012,218 (09/042,631)	US	1/11/2000 (3/17/1998)	Process for producing thin film magnetic heads Shimizu, Yoshiaki; Tatezono, Fumio; Matono, Naoto; Ogura, Takashi; Okuda, Hiroyuki; Umeda, Katsumi
JP2781849 (JPS63-167407)	JP	5/22/1998 (7/5/1988)	Magnetic head and production Okuda, Hiroyuki; Shimizu, Yoshiaki; Ino, Kazuo; Ishihara, Kousou; Ogura, Takashi
4,953,049 (07/218,018)	US	8/28/1990 (7/12/1988)	Metal-in-gap head with heat resistant layers Okuda, Hiroyuki; Shimizu, Yoshiaki; Ino, Kazuo; Ishihara, Kousou; Ogura, Takashi
5,195,004	US	3/16/1993	Method of manufacturing a magnetic

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
(07/826,192)		(1/21/1992)	core half Okuda, Hiroyuki; Shimizu, Yoshiaki; Ino, Kazuo; Ishihara, Kousou; Ogura, Takashi
5,278,716 (07/974,757)	US	1/11/1994 (11/12/1992)	Magnetic head with suppressed generation of pseudogap Okuda, Hiroyuki; Shimizu, Yoshiaki; Ino, Kazuo; Ishihara, Kousou; Ogura, Takashi
5,007,158 (07/307,654)	US	4/16/1991 (2/8/1989)	Method of manufacturing magnetic heads Ino, Kazuo; Shimizu, Yoshiaki; Okuda, Hiroyuki; Ishihara, Kousou; Ogura, Takashi
JP2123126 (JPH01-100768)	JP	12/20/1996 (4/20/1989)	Production of magnetic head Ogura, Takashi; Shimizu, Yoshiaki; Okuda, Hiroyuki; Ino, Kazuo; Ishihara, KOZO; YAMANO TAKAO; SHIMIZU TSUKASA
5,099,376 (07/650,814)	US	3/24/1992 (1/31/1991)	A magnetic head having a plurality of gap portions (Original title: A magnetic head having a plurality of gap portions) Ino, Kazuo; Shimizu, Yoshiaki; Okuda, Hiroyuki; Ishihara, Kousou; Ogura, Takashi
5,056,353 (07/333,400)	US	10/15/1991 (4/5/1989)	Marker for detecting amount of working and process for producing thin film magnetic head Matono, Naoto
JP2567725 (JPH02-231676)	JP	10/3/1996 (8/31/1990)	Production of floating type magnetic head Ogawa, Takahiro; Inoue, Atsushi; Koga, Kazuhiko; Ito, Kiyotaka; Ueda, MINORU
5,146,671 (07/753,157)	US	9/15/1992 (8/30/1991)	Method of manufacturing floating type magnetic head

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			Ogawa, Takahiro; Inoue, Atsushi; Koga, Kazuhiko; Ito, Kiyotaka; Ueta, Yutaka
5,510,941 (08/227,277)	US	4/23/1996 (4/13/1994)	Magneto-resistive type magnetic head with a shunt layer of molybdenum Ohyama, Tatsushi; Nakata, Masahiro; Matono, Naoto
5,609,971 (08/264,918)	US	3/11/1997 (6/24/1994)	Thin film magnetic head Matono, Naoto; Noguchi, Hitoshi; Yamamoto, Tomomi; Kobayashi, Shinji; Nakata, Masahiro
5,585,198 (08/326,731)	US	12/17/1996 (10/20/1994)	Magnetorsistance effect element Maeda, Atsushi; Oikawa, Satoru; Kume, Minoru
5,699,213 (08/638,871)	US	12/16/1997 (4/29/1996)	Magnetoresistive head having a magnetic domain control layer Ohyama, Tatsushi; Matono, Naoto
JP2931529 (JPH06-265650)	JP	5/21/1995 (10/28/1994)	Combined thin-film magnetic head Matono, Naoto; Kobayashi, Shinji; Nakada, Masahiro
5,739,991 (08/761,933)	US	4/14/1998 (12/9/1996)	Composite thin film head having magnetoresistive and inductive head portions with nonmagnetic thin film interposed between core and shield portions of core layer Matono, Naoto; Kobayashi, Shinji; Nakata, Masahiro
5,620,784 (08/511,012)	US	4/15/1997 (8/3/1995)	Magnetoresistive film Tanuma, Toshio; Kume, Minoru
5,864,452 (08/831,835)	US	1/26/1999 (4/2/1997)	Thin-film magnetic head and method of forming carbon film Hirano, Hitoshi; Kuramoto, Keiichi; Domoto, Yoichi; Kiyama, Seiichi
5,955,211 (08/890,508)	US	9/21/1999 (7/9/1997)	Magnetoresistive film Maeda, Atsushi; Oikawa, Satoru
6,104,275 (09/147,845)	US	8/15/2000 (9/16/1997)	Magnetoresistive element Maeda, Atsushi

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
6,146,775 (08/972,212)	US	11/14/2000 (11/17/1997)	Magnetoresistive film Fujita, Masayuki; Maeda, Atsushi; Oikawa, Satoru; Yamano, Koji; Kume, Minoru
5,986,857 (09/023,016)	US	11/16/1999 (2/11/1998)	Thin film magnetic head including adhesion enhancing interlayers, and upper and lower gap insulative layers having different hydrogen contents and internal stress states Hirano, Hitoshi; Domoto, Yoichi; Kuramoto, Keiichi; Tarui, Hisaki
JP3354500 (JPH10-206423)	JP	9/27/2002 (7/22/1998)	Anti-theft system Matsudaira, Shinji
6,043,744 (09/131,876)	US	3/28/2000 (8/10/1998)	Antitheft system Matsudaira, Shinji
JP3374090 (JPH10-298447)	JP	11/22/2002 (10/20/1998)	Thieft prevention system Matsudaira, Shinji
JP3349464 (JPH11-009439)	JP	9/13/2002 (1/18/1999)	Burglar preventing system and monitoring system Matsudaira, Shinji
6,304,181 (09/419,909)	US	10/16/2001 (10/18/1999)	Antitheft system and monitoring system Matsudaira, Shinji
5,771,211 (08/639,365)	US	6/23/1998 (4/26/1996)	Magneto-optical recording media having a reading layer with a specified range of temperature coefficients of a kerr rotation angle Tanase, Kenji; Suzuki, Yoshihisa; Yamaguchi, Atsushi
5,796,683 (08/823,757)	US	8/18/1998 (3/25/1997)	Magneto-optical recording device having a controllable polarizing filter Sumi, Satoshi; Tanase, Kenji; Suzuki, Yoshihisa; Yamaguchi, Atsushi; Tsuchiya, Yoichi
JP3374086 (JPH10-243894)	JP	11/22/2002 (8/28/1998)	Magneto-optical recording medium Mitani, Kenichiro
6,256,266	US	7/3/2001	Magneto-optical recording medium

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
(09/382,672)		(8/25/1999)	having lands and grooves of discontinue regions Mitani, Kenichiro
JP3357864 (JPH11-283714)	JP	10/4/2002 (10/5/1999)	Magneto-optical disk device, signal recording method and signal reproducing method Mitani, Kenichiro; Takagi, Naoyuki; Noguchi, Hitoshi; Yamaguchi, Atsushi; Ishida, Koki
6,295,253 (09/666,549)	US	9/25/2001 (9/21/2000)	Magnetooptical disk unit capable of recording or reproducing signals having different domain lengths under the same conditions, signal recording method and signal reproducing method Mitani, Kenichiro; Takagi, Naoyuki; Noguchi, Hitoshi; Yamaguchi, Atsushi; Ishida, Hiroki
JP3357852 (JPH10-359226)	JP	10/4/2002 (12/17/1998)	Magneto-optical disk drive Suzuki, Yoshihisa; Tanaka, Sayoko
6,314,062 (09/464,777)	US	11/6/2001 (12/16/1999)	Magneto-optical disk apparatus that can adjust position of magnetic head with respect to optical head Suzuki, Yoshihisa; Tanaka, Sayoko
6,335,908 (09/522,467)	US	1/1/2002 (3/9/2000)	Magneto optical recording medium with mask layer Tanase, Kenji; Nakatani, Morio
6,370,089 (09/208,853)	US	4/9/2002 (12/10/1998)	Magneto-optical recording medium simplifying transfer of magnetic domain Yamaguchi, Atsushi; Mitani, Kenichiro
KR10-0324882 (KR10-1999-7007105)	KR	2/4/2002 (2/6/1998)	Information recording/reproducing apparatus Torazawa, Kenji; Asano, Kenji; Sumi, Satoshi; Uchihara, Yoshiharu; Mamiya, Noboru; Hioki, Toshiaki
EP98901548.2	EP	2/6/1998	Information recording/reproducing apparatus Torazawa, Kenji; Asano, Kenji; Sumi,

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			Satoshi; Uchihara, Yoshiharu; Mamiya, Noboru; Hioki, Toshiaki
6,400,653 (09/355,831)	US	6/4/2002 (2/6/1998)	Information recording/reproducing apparatus to record/reproduce information on a recording medium recorded with an address mark Torazawa, Kenji; Asano, Kenji; Sumi, Satoshi; Uchihara, Yoshiharu; Mamiya, Noboru; Hioki, Toshiaki
JP3108397 (JPH09-345085)	JP	9/8/2000 (12/15/1997)	Magneto-optical recording medium Tanase, Kenji; Suzuki, Yoshihisa
6,400,656 (09/160,045)	US	6/4/2002 (9/25/1998)	Magneto-optical recording medium comprising recording layer and reproducing layer Tanase, Kenji; Suzuki, Yoshihisa
6,418,088 (09/367,934)	US	7/9/2002 (12/22/1998)	Apparatus and method for reproducing information from a magneto optical recording medium by the magnetic domain magnification method Takagi, Naoyuki; Yamaguchi, Atsushi
EP00109904.3	EP	5/10/2000	Magneto-optical recording apparatus and method Tanaka, Sayoko; Suzuki, Yoshihisa
6,456,570 (09/570,437)	US	9/24/2002 (5/12/2000)	Magneto-optical recording apparatus and method using phase different optimization technique Tanaka, Sayoko; Suzuki, Yoshihisa
6,483,783 (09/713,034)	US	11/19/2002 (11/16/2000)	Magneto-optical disk apparatus capable of accurately enlarging and reproducing a magnetic domain and method of reproducing the same Mitani, Kenichiro; Takagi, Naoyuki; Noguchi, Hitoshi; Ishida, Hiroki; Yamaguchi, Atsushi; Nakatani, Morio
6,483,784 (09/670,612)	US	11/19/2002 (9/27/2000)	Magneto-optical recording medium having a heat-sink layer Nakatani, Morio; Tanase, Kenji
JP3281839	JP	2/22/2002	Dielectric element, dielectric memory

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(JPH09-158809)		(6/16/1997)	and its manufacture Ogasawara, Satoru; Harada, Mitsuaki; Furukawa, Hiroaki; Goto, Takashi; Geshi, Tatsuro; Ishizuka, Yoshiyuki
6,194,752 (09/094,592)	US	2/27/2001 (6/15/1998)	Dielectric device, dielectric memory and method of fabricating the same Ogasahara, Satoru; Harada, Mitsuaki; Furukawa, Hiroaki; Goto, Takashi; Gueshi, Tatsuro; Ishizuka, Yoshiyuki
JP4024166 (JP2003-057179)	JP	10/12/2007 (3/4/2003)	FERROELECTRIC MEMORY Sakai, Naofumi
KR10-0629295 (KR10-2003-0017086)	KR	9/21/2006 (3/19/2003)	Memory having storage means Sakai, Naofumi
JP4024289 (JP2007-085997)	JP	10/12/2007 (3/28/2007)	FERROELECTRIC MEMORY Sakai, Naofumi
6,795,351 (10/390,649)	US	9/21/2004 (3/19/2003)	Memory having storage means Sakai, Naofumi
10/874,168	US	6/24/2004	Memory having storage means Sakai, Naofumi
CN03110799.0	CN	3/20/2003	Memory with meory component Sakai, Naofumi
JP3920863 (JP2004-076952)	JP	2/23/2007 (3/17/2004)	Manufacturing method of memory Honma, Kazunari; Matsushita, Shigeharu
JP2007-008638	JP	1/18/2007	MEMORY Honma, Kazunari; Matsushita, Shigeharu
6,977,402 (10/802,786)	US	12/20/2005 (3/18/2004)	Memory device having storage part and thin-film part Honma, Kazunari; Matsushita, Shigeharu
7,297,559 (11/260,243)	US	11/20/2007 (10/28/2005)	Method of fabricating memory and memory Honma, Kazunari; Matsushita,

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			Shigeharu
KR10-0747404 (KR10-2004-0020024)	KR	8/1/2007 (3/24/2004)	Memory manufacturing method Honma, Kazunari; Matsushita, Shigeharu
KR10-0747403 (KR10-2007-0052768)	KR	8/1/2007 (3/24/2004)	Memory Honma, Kazunari; Matsushita, Shigeharu
ZL200410008536.4 (CN200410008536.4)	CN	2/20/2008 (3/25/2004)	Storage mfg. method and storage Honma, Kazunari; Matsushita, Shigeharu
CN200710109088.0	CN	3/25/2004	Manufacturing method of memory, and memory Honma, Kazunari; Matsushita, Shigeharu
CN200510106359.8	CN	9/22/2005	Memory Miyamoto, Hideaki
KR10-0682436 (KR10-2005-0088546)	KR	2/7/2007 (9/23/2005)	Memory Miyamoto, Hideaki
7,245,545 (11/228,215)	US	7/17/2007 (9/19/2005)	Memory Miyamoto, Hideaki
JP4036707 (JP2002-234562)	JP	11/9/2007 (8/12/2002)	DIELECTRIC ELEMENT AND MANUFACTURING METHOD OF DIELECTRIC ELEMENT Honma, Kazunari ; Matsushita, Shigeharu
7,247,900 (10/631,858)	US	7/24/2007 (8/1/2003)	Dielectric device having dielectric film terminated by halogen atoms Honma, Kazunari; Matsushita, Shigeharu
JP2004-006396	JP	1/14/2004	Memory Sakai, Naofumi
7,251,153 (11/024,688)	US	7/31/2007 (12/30/2004)	Memory Sakai, Naofumi
CN200510004322.4	CN	1/13/2005	Memory

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			Sakai, Naofumi
KR10-0675246 (KR10-2005-0003103)	KR	1/22/2007 (1/13/2005)	Memory Naofumi,Sakai
JP2004-276510	JP	9/24/2004	Memory Sakai, Naofumi
7,262,985 (11/229,763)	US	8/28/2007 (9/20/2005)	Memory Sakai, Naofumi
CN200510106303.2	CN	9/21/2005	Memory Sakai, Naofumi
KR10-0745325 (KR10-2005-0088738)	KR	7/26/2007 (9/23/2005)	Memory Sakai, Naofumi
7,319,606 (11/090,660)	US	1/15/2008 (3/28/2005)	Memory Miyamoto, Hideaki
JP3920827 (JP2003-314889)	JP	2/23/2007 (9/8/2003)	Semiconductor memory device Miyamoto, Hideaki; Sakai, Naofumi; Ishizuka, Yoshiyuki
10/932,081	US	9/2/2004	Semiconductor memory device Miyamoto, Hideaki; Sakai, Naofumi; Ishizuka, Yoshiyuki
CN200410068725.0	CN	9/6/2004	Semiconductor memory device Miyamoto, Hideaki; Sakai, Naofumi; Ishizuka, Yoshiyuki
KR10-0682438 (KR10-2004-0070707)	KR	2/7/2007 (9/6/2004)	Semiconductor memory device Miyamoto, Hideaki; Sakai, Naofumi; Ishizuka, Yoshiyuki
JP4024196 (JP2003-340560)	JP	10/12/2007 (9/30/2003)	FERROELECTRIC MEMORY Dan, Toru; Sakai, Naofumi; Matsushita, Shigeharu; Ishizuka, Yoshiyuki
10/936,593	US	9/9/2004	Memory Dan, Toru; Sakai, Naofumi; Matsushita, Shigeharu; Ishizuka, Yoshiyuki
KR10-0610705 (KR10-2004-0076888)	KR	8/2/2006 (9/24/2004)	Memory Dan, Toru; Sakai, Naofumi; Matsushita,

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
			Shigeharu; Ishizuka, Yoshiyuki
CN200410085731.7	CN	9/30/2004	Memory Dan, Toru; Sakai, Naofumi; Matsushita, Shigeharu; Ishizuka, Yoshiyuki
JP2005-013710	JP	1/21/2005	Memory Miyamoto, Hideaki; Sakai, Naofumi; Yamada, Kouichi; Matsushita, Shigeharu
CN200610005919.5	CN	1/19/2006	Memory Miyamoto, Hideaki; Sakai, Naofumi; Yamada, Kouichi; Matsushita, Shigeharu
KR10-0778202 (KR10-2006-0006256)	KR	11/14/2007 (1/20/2006)	Memory Miyamoto, Hideaki; Sakai, Naofumi; Yamada, Kouichi; Matsushita, Shigeharu
7,366,004 (11/328,223)	US	4/29/2008 (1/10/2006)	Memory Miyamoto, Hideaki; Sakai, Naofumi; Yamada, Kouichi; Matsushita, Shigeharu
JP2006-020168	JP	1/30/2006	Memory Murayama, Yoshiki; Yamada, Kouichi
CN200610009230.X	CN	2/14/2006	Memory Murayama, Yoshiki; Yamada, Kouichi
KR10-2006-0013998	KR	2/14/2006	Memory Murayama, Yoshiki; Yamada, Kouichi
11/353,089	US	2/14/2006	Memory Murayama, Yoshiki; Yamada, Kouichi
JP2005-182952	JP	6/23/2005	Memory Miyamoto, Hideaki
11/473,083	US	6/23/2006	Memory Miyamoto; Hideaki
JP2005-364769	JP	12/19/2005	Memory Miyamoto, Hideaki; Matsushita,

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CN200610105771.2	CN	7/26/2006	Memory Miyamoto, Hideaki; Matsushita, Shigeharu
KR10-2006-0070663	KR	7/27/2006	Memory Miyamoto, Hideaki; Matsushita, Shigeharu
7,362,642 (11/494,748)	US	4/22/2008 (7/28/2006)	Memory Miyamoto, Hideaki; Matsushita, Shigeharu
JP2005-279013	JP	9/27/2005	Memory Miyamoto, Hideaki; Matsushita, Shigeharu
US7,379,323 (11/524,273)	US	(9/21/2006)	Memory Miyamoto, Hideaki; Matsushita, Shigeharu
CN200610154091.X	CN	9/22/2006	Memory Miyamoto, Hideaki; Matsushita, Shigeharu
KR10-2006-93315	KR	9/26/2006	Memory Miyamoto, Hideaki; Matsushita, Shigeharu
JP2005-313317	JP	10/27/2005	Memory Murayama, Yoshiki; Matsushita, Shigeharu
11/584,491	US	10/23/2006	Memory Murayama, Yoshiki; Matsushita, Shigeharu
JP2006-013351	JP	1/23/2006	Memory Miyamoto, Hideaki
CN200710002032.5	CN	1/18/2007	Memory Miyamoto, Hideaki
KR10-2007-0006543	KR	1/22/2007	Memory Miyamoto, Hideaki

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11/656,480	US	1/23/2007	Memory Miyamoto, Hideaki
JP2006-528486	JP	6/16/2005	Memory Miyamoto, Hideaki; Sakai, Naofumi; Yamada, Kouichi; Matsushita, Shigeharu
KR10-2006-7021560	KR	6/16/2005	Memory Miyamoto, Hideaki; Sakai, Naofumi; Yamada, Kouichi; Matsushita, Shigeharu
11/630,851	US	12/22/2006	Memory Miyamoto, Hideaki; Sakai, Naofumi; Yamada, Kouichi; Matsushita, Shigeharu
CN200580019279.7	CN	6/16/2005	Memory Miyamoto, Hideaki
CN200710101092.2	CN	4/26/2007	Memory Miyamoto, Hideaki
CN200710110081.0	CN	6/14/2007	Memory Miyamoto, Hideaki
JP2006-250483	JP	9/15/2006	Memory Miyamoto, Hideaki
JP2006-285959	JP	10/20/2006	Memory and control unit Miyamoto, Hideaki
11/762,566	US	6/13/2007	Memory and control unit Miyamoto, Hideaki
CN200710110080.6	CN	6/14/2007	Memory Miyamoto, Hideaki
11/739,336	US	4/24/2007	Memory Miyamoto, Hideaki
KR10-2007-0040948	KR	4/26/2007	Memory Miyamoto, Hideaki
11/760,383	US	6/8/2007	Memory

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			Miyamoto, Hideaki
KR10-2007-0058818	KR	6/15/2007	Memory Miyamoto, Hideaki
JP2007-109364	JP	4/18/2007	Memory Miyamoto, Hideaki
JP2006-121425	JP	4/26/2006	Memory Miyamoto, Hideaki
CN200710096661.9	CN	4/19/2007	Memory Miyamoto, Hideaki
KR10-2007-0040453	KR	4/25/2007	Memory Miyamoto, Hideaki
11/739,754	US	4/25/2007	Memory Miyamoto, Hideaki
6,826,130 (10/297,916)	US	11/30/2004 (6/7/2001)	Magneto-optic disk device and method of positioning magnetic head of the device Kamimura, Takaya
JP3819662 (JP2000-052422)	JP	6/23/2006 (2/28/2000)	Device, Method, And Medium For Three-Dimensional Modeling, And Device, Method, And Medium For Three- Dimensional Shape Data Recording Matsumoto, Yukinori
TW188937	TW		

(b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, or (ii) for which any of the Patents directly or indirectly forms a basis for priority;

(c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);

(d) all foreign patents, patent applications, and counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of

invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;

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(ii) injunctive relief, and
(iii) any other remedies of any kind for past, current, and future infringement; and

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(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h).

The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Osaka, Japan on Sep. 17, 2008.

ASSIGNOR:
SANYO Electric Co., Ltd.

By: Shigeharu Yoshii
Name: Shigeharu Yoshii
Title: Officer, General Counsel

ATTESTATION

The undersigned witnessed the signature of Shigeharu Yoshii to the above Assignment of Patent Rights on behalf of SANYO Electric Co., Ltd. and makes the following statements:

1. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.

2. Shigeharu Yoshii is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me Sep. 17, 2008 to execute the above Assignment of Patent Rights on behalf of SANYO Electric Co., Ltd.

3. Shigeharu Yoshii subscribed to the above Assignment of Patent Rights on behalf of SANYO Electric Co., Ltd..

I declare under penalty pf perjury under the laws of the United States of America that the foregoing is true and correct.

EXECUTED on September 17, 2008 (date)

By: Noriyuki Fujibayashi
Name: Noriyuki Fujibayashi