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<b>9. Signature:</b> <u>Kyle Eppele</u> <span style="float: right;"><u>October 25, 2010</u></span> <div style="display: flex; justify-content: space-between;"> <span>Signature</span> <span>Date</span> </div> <div style="display: flex; justify-content: space-between; margin-top: 10px;"> <span>KYLE EPPELE, PATENT COUNSEL Name of Person Signing</span> <div style="border: 1px solid black; padding: 5px; text-align: center;">             Total number of pages including cover sheet, attachments, and documents: <b>2</b> </div> </div>	

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**PATENT**  
**REEL: 025207 FRAME: 0521**

CONFIRMATORY INSTRUMENT

APPLICATION FOR (TITLE OF INVENTION)

Method of Creating Defect Free High Ge Content (>25%) SiGe-On-Insulator (SGOI)  
Substrates Using Wafer Bonding Techniques

INVENTOR(S): Jack O. Chu, Michael A. Cobb, Philip A. Saunders, Leathen Shi

SERIAL NO. 10/939736	FILING DATE: September 13, 2004
CONTRACTOR: INTERNATIONAL BUSINESS MACHINES CORPORATION	CONTRACT NO. N66001-00-C-8086

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