PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT
NATURE OF CONVEYANCE:	Corrective Assignment to correct the Execution Date for inventor IYENGAR,Vinay Raja from 09/01/2008 to 02/01/2008 previously recorded on Reel 025155 Frame 0545. Assignor(s) hereby confirms the assignment to Netlogic Microsystems, Inc.

CONVEYING PARTY DATA

Name	Execution Date
Vinay Raja Iyengar	02/01/2008
Venkat Rajendher Reddy Gaddam	02/01/2008
Aparna Bharat	02/01/2008

RECEIVING PARTY DATA

Name:	Netlogic Microsystems, Inc.
Street Address:	3975 Freedom Circle
City:	Santa Clara
State/Country:	CALIFORNIA
Postal Code:	95054

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	12901886

CORRESPONDENCE DATA

(408)530-9797 Fax Number:

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 4085309700 Email: bsako@hollp.com Correspondent Name: Bradley T. Sako 162 North Wolfe Road Address Line 1:

Address Line 4: Sunnyvale, CALIFORNIA 94086

ATTORNEY DOCKET NUMBER: NETL-05901 NAME OF SUBMITTER: Bradley T. Sako

PATENT

REEL: 025452 FRAME: 0515

Total Attachments: 5

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PATENT REEL: 025452 FRAME: 0516



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PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Vinay Raja Iyengar	09/01/2008
Venkat Rajendher Reddy Gaddam	02/01/2008
Aparna Bharat	02/01/2008

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Application Number:	12901886

CORRESPONDENCE DATA

Fax Number: (408)530-9797

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

4085309700 Phone:

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Address Line 4: SUNNYVALE, CALIFORNIA 94086

ATTORNEY DOCKET

NUMBER:

NETL-05901

NAME OF SUBMITTER:

Bradley T. Sako

Signature:

/Bradley T. Sako/

Date:

10/11/2010

Total Attachments: 3

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RECEIPT INFORMATION

PAT1341435 EPAS ID: Receipt Date: 10/11/2010 Fee Amount: \$40

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PATENT

Attorney Docket No.: NETL-05900

ASSIGNMENT (Page 1 of 3)

In consideration of good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I/we, Vinay Raja Iyengar, Venkat Rajendher Reddy Gaddam and Aparna Bharat, the undersigned Assignor(s), believing to be the original, first and sole or joint inventor(s) of any and all new and useful improvements disclosed in the application for the United States patent entitled

PROCESSOR WITH COMPARE OPERATIONS BASED ON ANY OF MULTIPLE COMPARE DATA SEGMENTS

hereby sell, assign, and transfer to **NetLogic Microsystems**, **Inc.** a Delaware Corporation, having a principal place of business at 1875 Charleston Road, Mountain View, CA 94043, ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements that are disclosed in the same patent application

 which	was fi	iled '	with	the	United	States	Patent	and	Trademark	Office	on
 , and	which	ı was	s assi	igne	d Appl	ication	Numb	er_			•

X which has been executed by the undersigned prior hereto or concurrently herewith on the dates indicated below.

I/We hereby further sell, assign, and transfer to Assignee said application and all divisional applications, continuation applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions, and all other patent applications that have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original patents, reissued patents, reexamination certificates, and extensions, that have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application; and the right to file foreign applications directly in the name of the Assignee and to claim for any such foreign applications any priority rights to which such applications are entitled under international conventions, treaties or otherwise.

I/We agree that said Assignee may apply for and receive a patent or patents for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and representatives all facts known to the undersigned relating to said improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or representatives in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives.

PATENT REEL: 025452 FRAME: 0519

ASSIGNMENT (Page 2 of 3)

For Invention(s) Related to: PROCESSOR WITH COMPARE OPERATIONS BASED ON ANY OF MULTIPLE COMPARE DATA SEGMENTS

To: Netlogic Microsystems, Inc.

I/We hereby covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

I/We hereby authorize and request the Officials of the United States Patent and Trademark Office and any and all foreign Patent Offices to issue any and all Letters Patents, when granted, to said Assignee as the assignee of the entire right, title and interest in and to the same, for the sole use of said Assignee, its successors and assigns, to the full end of the term for which said Letters Patents may be granted.

I/We hereby grant to HAVERSTOCK & OWENS, LLP located at 162 North Wolfe Road, Sunnyvale, CA 94086, the power to insert on this document any further identification necessary or desirable to comply with the rules of the United States Patent and Trademark Office for recordation of this Assignment.

Each Inventor/Assignor: Please Sign and Date Below:

Name: Vinay Raja Iyengar (Signature)	Seb 15t (Date)	, 2008
Second Inventor: Name: Venkat Rajendher Reddy Gaddam	Teb 1st (Date)	, 2008
(Signature)		

ASSIGNMENT (Page 3 of 3)

For Invention(s) Related to: PROCESSOR WITH COMPARE OPERATIONS BASED ON ANY OF MULTIPLE COMPARE DATA SEGMENTS

To: Netlogic Microsystems, Inc.

<u>Third Inventor</u> :		
Name: Aparna Bharat	(Date)	, 2008
(Signature)		