

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE BY SECURED PARTY
CONVEYING PARTY DATA	
Name	Execution Date
Comerica Bank, as Agent, a Texas banking association (formerly a Michigan banking corporation)	12/01/2010
RECEIVING PARTY DATA	
Name:	Vishay Sprague, Inc., successor-in-interest to Vishay EFI, Inc. and Vishay Thin Film, LLC, a Delaware corporation
Street Address:	63 Lancaster Avenue
City:	Malvern
State/Country:	PENNSYLVANIA
Postal Code:	19355
Name:	Vishay Dale Electronics, Inc., a Delaware corporation
Street Address:	63 Lancaster Avenue
City:	Malvern
State/Country:	PENNSYLVANIA
Postal Code:	19355
Name:	Vishay Intertechnology, Inc., a Delaware corporation
Street Address:	63 Lancaster Avenue
City:	Malvern
State/Country:	PENNSYLVANIA
Postal Code:	19355
Name:	Siliconix Incorporated, a Delaware corporation
Street Address:	63 Lancaster Avenue
City:	Malvern
State/Country:	PENNSYLVANIA
Postal Code:	19355
Name:	Vishay Measurements Group, Inc., a Delaware corporation

PATENT

501378279

REEL: 025489 FRAME: 0184

CH \$12280.00 61169377

Street Address:	63 Lancaster Avenue
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State/Country:	PENNSYLVANIA
Postal Code:	19355

Name:	Vishay General Semiconductor, LLC, f/k/a General Semiconductor, Inc., a Delaware limited liability company
Street Address:	63 Lancaster Avenue
City:	Malvern
State/Country:	PENNSYLVANIA
Postal Code:	19355

Name:	Vishay Vitramon, Incorporated, a Delaware corporation
Street Address:	63 Lancaster Avenue
City:	Malvern
State/Country:	PENNSYLVANIA
Postal Code:	19355

Name:	Yosemite Investment, Inc., an Indiana corporation
Street Address:	63 Lancaster Avenue
City:	Malvern
State/Country:	PENNSYLVANIA
Postal Code:	19355

PROPERTY NUMBERS Total: 307

Property Type	Number
Application Number:	61169377
Application Number:	61169764
Patent Number:	6204744
Patent Number:	6460244
Patent Number:	5760669
Patent Number:	5880662
Patent Number:	5604477
Patent Number:	5091286
Patent Number:	5160912
Patent Number:	5287083
Patent Number:	5302932
Patent Number:	5179369

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Application Number:	11380293

Patent Number:	7426102
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Patent Number:	5825611
Patent Number:	6348712

CORRESPONDENCE DATA

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NAME OF SUBMITTER:	Angela Alvarez Sujek
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Total Attachments: 16

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PATENT

REEL: 025489 FRAME: 0194

FULL RELEASE OF SECURITY INTEREST IN PATENTS

THIS RELEASE dated as of December 1, 2010 (this "Release") is made by Comerica Bank, as Agent, a Texas banking association (formerly a Michigan banking corporation) ("Secured Party") under the Agreements dated December 13, 2002 and February 12, 2010 (the "Security Agreement") and recorded in the records of the United States Patent and Trademark Office at Patent Reel 013712, Frame 0412 and Reel 024006, Frame 0515, respectively, between Vishay Sprague, Inc., as successor-in-interest to Vishay EFI, Inc. and Vishay Thin Film, LLC, Vishay Dale Electronics, Inc., Vishay Intertechnology, Inc., Siliconix Incorporated, Vishay Measurements Group, Inc., General Semiconductor, Inc., Vishay Vitramon, Incorporated and Yosemite Investment, Inc. (collectively, "Debtors") and Secured Party.

WHEREAS, pursuant to the Security Agreement, the Debtors granted to the Secured Party a security interest in the Debtors' Patent Collateral (as defined in the Security Agreement), including the patents and patent applications listed on the attached Exhibit A.

WHEREAS, the Secured Party wishes to: (i) release all of its security interest covering the Patent Collateral; (ii) restore all right, title and interest in and to the Patent Collateral to Debtors; and (iii) dissolve any and all liens and encumbrances respecting the Patent Collateral under the Security Agreement.

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, the Secured Party does hereby release its security interest in the Patent Collateral, and discharges, quit claims and relinquishes unto the Debtors (in each case without recourse and without any representation or warranty) any and all right, title and interest it has in and to the Patent Collateral under the Security Agreement.

IN WITNESS WHEREOF, the Secured Party has caused this Release to be duly executed and delivered by its officer duly authorized as of the date above.

COMERICA BANK, as Agent,
a Texas banking association

By: 

Name: Dan Roman

Title: Senior Vice President

AnnArbor_182077_2

Exhibit A

Title: (Patent Description)	Serial No.	File Date	Patent No.	Issue Date
METAL STRIP RESISTOR FOR MITIGATING EFFECTS OF THERMAL EMF	61/169,377	4 /15/2009		
HERMETICALLY SEALED WET ELECTROLYTIC CAPACITOR	61/169,764	4 /16/2009		
HIGH CURRENT, LOW PROFILE INDUCTOR	08/963,224	11/3 /1997	6,204,744	3 /20/2001
METHOD FOR MAKING A HIGH CURRENT LOW PROFILE INDUCTOR	09/547,155	4 /11/2000	6,460,244	10/8 /2002
LOW PROFILE INDUCTOR/TRANSFORMER COMPONENT	08/736,333	10/23/1996	5,760,669	6 /2 /1998
HIGH SELF RESONANT FREQUENCY MULTILAYER INDUCTOR AND METHOD FOR MAKING SAME	08/915,875	8 /21/1997	5,880,662	3 /9 /1999
SURFACE MOUNT RESISTOR AND METHOD FOR MAKING SAME	08/350,960	12/7 /1994	5,604,477	2 /18/1997
LASER-FORMED ELECTRICAL COMPONENT AND METHOD FOR MAKING SAME	07/587,761	9 /24/1990	5,091,286	2 /25/1992
THERMISTOR	07/579,362	9 /7 /1990	5,160,912	11/3 /1992
BULK METAL CHIP RESISTOR	07/860,403	3 /30/1992	5,287,083	2 /15/1994
MONOLYTHIC MULTILAYER CHIP INDUCTOR AND METHOD FOR MAKING SAME	07/881,856	5 /12/1992	5,302,932	4 /12/1994
TOUCH PANEL AND METHOD FOR CONTROLLING SAME	07/806,143	12/12/1991	5,179,369	1 /12/1993
ELECTRICAL RESISTORS & METHODS OF MAKING SAME	07/695,044	5 /2 /1991	5,206,623	4 /27/1993
MONOLITHIC THICK FILM INDUCTOR METHOD FOR MAKING SAME	08/665,788	6 /18/1996	5,986,533	11/16/1999
METHOD OF MAKING A MONOLITHIC THICK FILM INDUCTOR	08/881,480	6 /24/1997	5,970,604	10/26/1999
THICK FILM LOW VALUE HIGH FREQUENCY INDUCTOR, AND METHOD OF MAKING THE SAME	08/936,193	9 /17/1997	5,922,514	7 /13/1999
THICK FILM LOW VALUE HIGH FREQUENCY INDUCTOR AND METHOD OF MAKING THE SAME	09/080,494	5 /18/1998	6,215,387	4 /10/2001
THE METHOD OF MAKING A THICK FILM LOW VALUE HIGH FREQUENCY INDUCTOR	09/448,676	11/24/1999	6,201,215	3 /13/2001
THICK FILM LOW VALUE HIGH FREQUENCY INDUCTOR, AND METHOD OF MAKING THE SAME	09/722,853	11/27/2000	6,391,526	5 /21/2002
STRUCTURE OF MAKING A THICK FILM LOW VALUE HIGH FREQUENCY INDUCTOR	09/834,123	4 /12/2001	6,366,192	4 /2 /2002
SURFACE MOUNT RESISTOR	09/441,434	11/16/1999	6,184,775	2 /6 /2001
SURFACE MOUNTED FOUR TERMINAL RESISTOR	09/247,490	2 /10/1999	5,999,085	12/7 /1999

<u>Title:</u> (Patent Description)	<u>Serial No.</u>	<u>File Date</u>	<u>Patent No.</u>	<u>Issue Date</u>
SURFACE MOUNTED FOUR TERMINAL RESISTOR	09/568,937	5 /11/2000	RE39,660	5 /29/2007
INDUCTOR COIL STRUCTURE	09/271,748	3 /18/1999	6,198,375	3 /6 /2001
METHOD FOR MAKING INDUCTOR COIL STRUCTURE	09/546,859	4 /10/2000	6,449,829	9 /17/2002
INDUCTOR COIL AND METHOD FOR MAKING SAME	10/244,777	9 /16/2002	6,946,944	9 /20/2005
INDUCTOR COIL AND METHOD FOR MAKING SAME	11/038,880	1 /20/2005	7,034,645	4 /25/2006
INDUCTOR COIL	11/409,651	4 /24/2006	7,221,249	5 /22/2007
METHOD FOR MAKING A HIGH CURRENT LOW PROFILE INDUCTOR	11/609,165	12/11/2006	7,263,761	9 /4 /2007
METHOD FOR MAKING A HIGH CURRENT LOW PROFILE INDUCTOR	11/782,020	7 /24/2007	7,345,562	3 /18/2008
METHOD FOR MAKING A HIGH CURRENT LOW PROFILE INDUCTOR	12/013,725	1 /14/2008		
METHOD FOR MAKING A HIGH CURRENT LOW PROFILE INDUCTOR	12/535,757	8 /5 /2009		
METHOD FOR MAKING OVERLAY SURFACE MOUNT RESISTOR	09/471,622	12/21/1999	6,401,329	6 /11/2002
OVERLAY SURFACE MOUNT RESISTOR	09/715,252	11/17/2000	6,441,718	8 /27/2002
METHOD FOR MAKING OVERLAY SURFACE MOUNT RESISTOR	10/078,311	2 /18/2002	6,725,529	4 /27/2004
METHOD FOR MAKING OVERLAY SURFACE MOUNT RESISTOR	10/797,866	3 /10/2004	6,901,655	6 /7 /2005
METHOD FOR MAKING OVERLAY SURFACE MOUNT RESISTOR	11/021,387	12/23/2004	7,278,202	10/9 /2007
METHOD FOR MAKING FORMED SURFACE MOUNT RESISTOR	09/471,617	12/21/1999	6,510,605	1 /28/2003
MONOLYTHIC METAL STRIP RESISTOR WITH HEAT SINKING WINGS	09/474,448	12/29/1999	6,181,234	1 /30/2001
METHOD AND APPARATUS FOR FAST HEAT RISE RESISTOR USING RESISTIVE FOIL WITH PHOTOLITHOGRAPHIC PRODUCTION	09/765,901	1 /19/2001	6,680,668	1 /20/2004
METHOD FOR MAKING A RESISTOR USING RESISTIVE FOIL	10/079,085	2 /20/2002	6,671,945	1 /6 /2004
METHOD FOR MANUFACTURING A FAST HEAT RISE RESISTOR	10/964,357	10/13/2004	7,247,250	7 /24/2007
SIDE-BY-SIDE COIL INDUCTOR	09/774,854	1 /31/2001	6,587,025	7 /1 /2003
METHOD FOR THIN FILM NTC THERMISTOR	09/810,206	3 /16/2001	6,880,234	4 /19/2005
PRECISION RESISTOR TUBE FEEDER	09/820,064	3 /28/2001	6,669,435	12/30/2003
METHOD FOR TANTALUM PENTOXIDE MOISTURE BARRIER IN FILM RESISTORS	09/829,169	4 /9 /2001	7,214,295	5 /8 /2007
Apparatus for tantalum pentoxide moisture barrier in film resistors	10/079,010	2 /19/2002	7,170,389	1 /30/2007
POWER CHIP RESISTOR	09/811,844	3 /19/2001	7,038,572	5 /2 /2006
METHOD FOR MANUFACTURING A POWER CHIP RESISTOR	10/091,792	3 /6 /2002	6,859,999	3 /1 /2005

<u>Title:</u> (Patent Description)	<u>Serial No.</u>	<u>File Date</u>	<u>Patent No.</u>	<u>Issue Date</u>
Optimization of BV and RDS-on by graded doping in LDD and other high voltage ICs	07/498,170	3 /23/1990	5,132,753	7 /21/1992
DMOS POWER TRANSISTORS WITH REDUCED NUMBER OF CONTACTS USING INTEGRATED BODY-SOURCE CONNECTIONS	08/047,723	4 /14/1993	5,410,170	4 /25/1995
DMOS POWER TRANSISTORS WITH REDUCED NUMBER OF CONTACTS USING INTEGRATED BODY-SOURCE CONNECTIONS	08/777,636	12/31/1996	5,866,931	2 /2 /1999
LOW TEMPERATURE OXIDE LAYER OVER FIELD IMPLANT MASK	07/949,288	9 /21/1992	5,328,866	7 /12/1994
LOW TEMPERATURE OXIDE LAYER OVER FIELD IMPLANT MASK	08/236,299	5 /2 /1994	5,439,842	8 /8 /1995
Short channel trenched DMOS transistor	08/031,798	3 /15/1993	5,341,011	8 /23/1994
METHOD FOR FABRICATING A SHORT CHANNEL TRENCHED DMOS TRANSISTOR	08/289,358	8 /11/1994	5,474,943	12/12/1995
REVERSE BATTERY PROTECTION DEVICE CONTAINING POWER MOSFET	08/067,373	5 /26/1993	5,517,397	5 /14/1996
A JUNCTION-ISOLATED FLOATING DIODE	08/067,372	5 /26/1993	5,414,292	5 /9 /1995
PUSH-PULL OUTPUT STAGE FOR DRIVING MOTORS WHICH GENERATES AUXILIARY VOLTAGE SUPPLY	08/062,504	5 /14/1993	5,377,094	12/27/1994
HEAD-RETRACT CIRCUIT FOR MOVING MEDIA STORAGE APPARATUS	08/062,968	5 /14/1993	5,455,496	10/3 /1995
DISCONNECT SWITCH CIRCUIT TO POWER HEAD RETRACT IN HARD DISK DRIVE MEMORIES	08/062,503	5 /14/1993	5,508,874	4 /16/1996
APPARATUS FOR GENERATING POSITIVE AND NEGATIVE SUPPLY RAILS FROM OPERATING MOTOR	08/062,969	5 /14/1993	5,459,654	10/17/1995
VERTICAL POWER MOSFET HAVING THICK METAL LAYER TO REDUCE DISTRIBUTED RESISTANCE	08/367,486	12/30/1994	5,665,996	9 /9 /1997
VERTICAL POWER MOSFET HAVING THICK METAL LAYER TO REDUCE DISTRIBUTED RESISTANCE	08/779,176	1 /6 /1997	6,066,877	5 /23/2000
METHOD OF FABRICATING VERTICAL POWER MOSFET HAVING LOW DISTRIBUTED RESISTANCE	08/966,553	11/10/1997	6,043,125	3 /28/2000
LATERAL POWER MOSFET HAVING METAL STRAP LAYER TO REDUCE DISTRIBUTED RESISTANCE	08/367,388	12/30/1994	5,767,546	6 /16/1998
INTEGRATED CIRCUIT DIE HAVING THICK BUS TO REDUCE DISTRIBUTED RESISTANCE	08/907,276	8 /6 /1997	5,945,709	8 /31/1999
METHOD OF FABRICATING LATERAL POWER MOSFET HAVING METAL STRAP LAYER TO REDUCE DISTRIBUTED RESISTANCE	09/264,602	3 /8 /1999	6,159,841	12/12/2000

<u>Title:</u> <u>(Patent Description)</u>	<u>Serial No.</u>	<u>File Date</u>	<u>Patent No.</u>	<u>Issue Date</u>
DMOS POWER TRANSISTOR WITH REDUCED NUMBER OF CONTACTS USING INTEGRATED BODY-SOURCE CONNECTIONS	777,636	12/31/1996	5,866,931	2 /2 /1999
LIGHTLY-DOPED DRAIN MOSFET WITH IMPROVED BREAKDOWN CHARACTERISTICS	07/697,356	5 /6 /1991	5,386,136	1 /31/1995
LIGHTLY-DOPED DRAIN MOSFET WITH IMPROVED BREAKDOWN CHARACTERISTICS	08/318,027	10/4 /1994	5,514,608	5 /7 /1996
LIGHTLY-DOPED DRAIN MOSFET WITH IMPROVED BREAKDOWN CHARACTERISTICS	08/040,684	3 /31/1993	5,374,843	12/20/1994
LOW ON-RESISTANCE POWER MOS TECHNOLOGY	07/881,589	5 /12/1992	5,304,831	4 /19/1994
STRUCTURE AND FABRICATION OF POWER MOSFETS, INCLUDING TERMINATION STRUCTURES	08/096,135	7 /22/1993	5,404,040	4 /4 /1995
LOW ON-RESISTANCE POWER MOS TECHNOLOGY	08/180,265	1 /12/1994	5,429,964	7 /4 /1995
STRUCTURE AND FABRICATION OF POWER MOSFETS, INCLUDING TERMINATION STRUCTURES	08/362,674	12/22/1994	5,521,409	5 /28/1996
THRESHOLD ADJUSTMENT IN FABRICATING VERTICAL DMOS DEVICES	07/854,162	3 /20/1992	5,248,627	9 /28/1993
THRESHOLD ADJUSTMENT IN VERTICAL DMOS DEVICES	08/062,370	5 /14/1993	5,465,000	11/7 /1995
THRESHOLD ADJUSTMENT IN FIELD EFFECT SEMICONDUCTOR DEVICES	08/281,314	7 /27/1994	5,648,288	7 /15/1997
THRESHOLD ADJUSTMENT IN FIELD EFFECT SEMICONDUCTOR DEVICES	08/482,341,	6 /6 /1995	5,726,477	3 /10/1998
DRIVER CIRCUIT FOR SINKING CURRENT TO TWO SUPPLY VOLTAGES	07/855,377	3 /20/1992	5,296,765	3 /22/1994
MOS TRANSISTOR WITH A CHARGE INDUCED DRAIN EXTENSION	07/802,352	12/4 /1991	5,243,212	9 /7 /1993
METAL CROSSOVER IN HIGH VOLTAGE IC WITH GRADUATED DOPING CONTROL	08/101,886	8 /4 /1993	5,426,325	6 /20/1995
BICDMOS STRUCTURES	08/226,419	4 /11/1994	5,426,328	6 /20/1995
BICDMOS PROCESS TECHNOLOGY	08/323,950	10/17/1994	5,559,044	9 /24/1996
PMOS TRANSISTORS WITH DIFFERENT BREAKDOWN VOLTAGES FORMED IN THE SAME SUBSTRATE	08/464,435	6 /5 /1995	5,583,061	12/10/1996
METHOD FOR FORMING A ZENER DIODE REGION AND AN ISOLATION REGION	08/464,978	6 /5 /1995	5,547,880	8 /20/1996
METHOD FOR FORMING A BIPOLAR TRANSISTOR HAVING SELECTED BREAKDOWN VOLTAGE	08/463,647	6 /5 /1995	5,541,123	7 /30/1996
METHOD FOR FORMING A LATERAL MOS TRANSISTOR HAVING LIGHTLY DOPED DRAIN FROMED ALONG WITH OTHER TRANSISTORS IN THE SAME SUBSTRATE	08/463,165	6 /5 /1995	5,541,125	7 /30/1996

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METHOD FOR FORMING AN ISOLATION STRUCTURE AND A BIPOLAR TRANSISTOR ON A SEMICONDUCTOR SUBSTRATE	08/647,073	5 /8 /1996	5,648,281	7 /15/1997
METHOD FOR FABRICATING AN MOS CAPACITOR USING ZENER DIODE REGION	08/667,219	6 /19/1996	5,643,820	7 /1 /1997
METHOD FOR FORMING A BICDMOS	08/026,932	3 /5 /1993	5,374,569	12/20/1994
ZENER DIODES ON THE SAME WAFER WITH BICDMOS STRUCTURES	08/705,910	8 /29/1996	5,751,054	5 /12/1998
BICDMOS STRUCTURE	08/026,930	3 /5 /1993	5,422,508	6 /6 /1995
METHOD OF MAKING BICDMOS STRUCTURES	08/225,270	4 /8 /1994	5,416,039	5 /16/1995
MOS TRANSISTOR HAVING ADJUSTED THRESHOLD VOLTAGE FORMED ALONG WITH OTHER TRANSISTORS	08/463,417	6 /5 /1995	5,618,743	4 /8 /1997
LOW THRESHOLD VOLTAGE EPITAXIAL DMOS TECHNOLOGY	08/131,114	10/1 /1993	5,479,037	12/26/1995
METHOD OF FORMING LOW THRESHOLD VOLTAGE VERTICAL POWER TRANSISTOR USING EPITAXIAL TECHNOLOGY	08/895,004	7 /17/1997	5,770,503	6 /23/1998
FIELD EFFECT TRENCH TRANSISTOR HAVING LIGHTLY DOPED EPITAXIAL REGION ON THE SURFACE PORTION THEREOF	07/918,954	7 /24/1992	5,910,669	6 /8 /1999
TRENCH FIELD EFFECT TRANSISTOR WITH REDUCED PUNCH0THROUGH SUSCEPTIBILITY AND LOW RDSON	08/386,895	2 /10/1995	5,558,313	9 /24/1996
METHOD OF MAKING A FIELD EFFECT TRENCH TRANSISTOR HAVING LIGHTLY DOPED EPITAXIAL REGION ON THE SURFACE PORTION THEREOF	08/447,484	5 /23/1995	5,532,179	7 /2 /1996
TRENCH FIELD EFFECT TRANSISTOR WITH REDUCED PUNCH0THROUGH SUSCEPTIBILITY AND LOW RDSON	08/658,115	6 /4 /1996	5,981,344	11/9 /1999
POWER DEVICE WITH BUFFERED GATE SHIELD	07/873,423	4 /23/1992	5,430,314	7 /4 /1995
POWER DEVICE WITH BUFFERED GATE SHIELD	08/242,519	5 /13/1994	5,445,978	8 /29/1995
HIGH VOLTAGE TRANSISTOR HAVING EDGE TERMINATION UTILIZING TRENCH TECHNOLOGY	07/918,996	7 /23/1992	5,430,324	7 /4 /1995
HIGH VOLTAGE TRANSISTOR HAVING EDGE TERMINATION UTILIZING TRENCH TECHNOLOGY	08/444,336	5 /18/1995	5,605,852	2 /25/1997
CONTACT GEOMETRY FOR IMPROVED LATERAL	08/062,507	5 /14/1993	5,412,239	5 /2 /1995
GATE DRIVE TECHNIQUE FOR A BIDIRECTIONAL BLOCKING LATERAL MOSFET	08/160,560	11/30/1993	5,510,747	4 /23/1996
GATE DRIVE TECHNIQUE FOR A BIDIRECTIONAL BLOCKING LATERAL MOSFET	08/569,334	12/8 /1995	5,612,566	3 /18/1997

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GATE DRIVE TECHNIQUE FOR A BIDIRECTIONAL BLOCKING LATERAL MOSFET	08/636,258	4 /22/1996	5,731,732	3 /24/1998
GATE DRIVE TECHNIQUE FOR A BIDIRECTIONAL BLOCKING LATERAL MOSFET	08/907,216	8 /5 /1997	5,909,139	6 /1 /1999
A BIDIRECTIONAL BLOCKING LATERAL MOSFET WITH IMPROVED ON-RISISTANCE	08/160,539	11/30/1993	5,420,451	5 /30/1995
A BIDIRECTIONAL BLOCKING LATERAL MOSFET WITH IMPROVED ON-RISISTANCE	08/318,323	10/5 /1994	5,451,533	9 /19/1995
ELECTROSTATIC DISCHARGE PROTECTION DEVICE FOR INTEGRATED CIRCUIT	08/326,172	10/19/1994	5,545,909	8 /13/1996
METHOD FOR FORMING ELECTROSTATIC DISCHARGE PROTECTION DEVICE FOR INTEGRATED CIRCUIT	08/472,943	6 /6 /1995	5,677,205	10/14/1997
ELECTROSTATIC DISCHARGE PROTECTION DEVICE FOR INTEGRATED CIRCUIT	08/486,280	6 /6 /1995	5,654,574	8 /5 /1997
METHOD OF FORMING ELECTROSTATIC DISCHARGE PROTECTION DEVICE FOR INTEGRATED CIRCUIT	08/873,781	6 /12/1997	5,877,534	3 /2 /1999
VOLTAGE CONVERTER WITH FREQUENCY SHIFT PROTECTION AGAINST OVERLOAD CURRENT	08/295,271	8 /23/1994	5,528,483	6 /18/1996
RELIABILITY TEST METHOD FOR SEMICONDUCTOR TRENCH DEVICES	08/268,755	6 /30/1994	5,486,772	1 /23/1996
TRENCHED DMOS TRANSISTOR WITH CHANNEL BLOCK AT CELL TRENCH CORNERS	08/253,527	6 /3 /1994	5,468,982	11/21/1995
METHOD OF MAKING A TRENCH MOSFET WITH MULTI-RESISTIVITY DRAIN TO PROVIDE LOW ON-RESISTANCE BY VARING DOPANT.....	08/429,414	4 /26/1995	5,674,766	10/7 /1997
TRENCH MOSFET WITH MULTI-RESISTIVITY DRAIN TO PROVIDE LOW ON-RESISTANCE	08/701,035	8 /21/1996	5,895,952	4 /20/1999
METHOD FOR MAKING TERMINATION STRUCTURE FOR POWER MOSFET	08/423,588	4 /17/1995	5,597,765	1 /28/1997
EDGE TERMINATION STRUCTURE FOR POWER MOSFET	08/632,052	4 /15/1996	5,614,751	3 /25/1997
LOW-SIDE BIDIRECTIONAL BATTERY DISCONNECT	08/367,515	12/30/1994	5,689,209	11/18/1997
PROTECTIVE CIRCUIT FOR PROTECTING LOAD AGAINST EXCESSIVE INPUT VOLTAGE	08/325,860	10/19/1994	5,585,991	12/17/1996
VOLTAGE REGULATOR HAVING IMPROVED STAABILITY	08/326,408	10/20/1994	5,559,424	9 /24/1996
BAND GAP VOLTAGE COMPENSATION CIRCUIT	08/388,535	2 /14/1995	5,596,265	1 /21/1997
OUTPUT CONTROL CIRCUIT FOR A VOLTAGE REGULATOR	08/389,705	2 /14/1995	5,506,496	4 /9 /1996

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HIGH DENSITY TRENCHED DMOS TRANSISTOR	08/533,814	8 /21/1995	5,689,128	11/18/1997
PUNCH-THROUGH FIELD EFFECT TRANSISTOR	08/415,009	3 /31/1995	5,592,005	1 /7 /1997
METHOD OF MAKING PUNCH-THROUGH FIELD EFFECT TRANSISTOR	08/962,885	11/12/1997	6,069,043	5 /30/2000
VOLTAGE-CLAMPED POWER ACCUMULATION-MODE MOSFET	08/459,054	6 /2 /1995	5,856,692	1 /5 /1999
BIDIRECTIONAL TRENCH GATED POWER MOSFET WITH SUBMERGED BODY BUS EXTENDING UNDERNEATH GATE TRENCH	08/884,826	6 /30/1997	5,877,538	3 /2 /1999
BIDIRECTIONAL TRENCH GATED POWER MOSFET WITH SUBMERGED BODY BUS EXTENDING UNDERNEATH GATE TRENCH	09/186,216	11/3 /1998	6,096,608	8 /1 /2000
TRENCHED DMOS TRANSISTOR WITH LIGHTLY DOPED TUB	08/610,563	3 /6 /1998	5,821,583	10/13/1998
TRENCHED DMOS TRANSISTOR WITH BURIED LAYER FOR REDUCED ON-RESISTANCE AND RUGGEDNESS	08/537,157	8 /21/1995	5,629,543	5 /13/1997
TRENCH-GATED POWER MOSFET WITH PROTECTIVE DIODE	08/846,688	4 /30/1997	5,998,836	12/7 /1999
TRENCH-GATED POWER MOSFET WITH PROTECTIVE DIODE HAVING ADJUSTABLE BREAKDOWN VOLTAGE	08/919,523	8 /28/1997	5,998,837	12/7 /1999
TRENCH-GATED POWER MOSFET WITH PROTECTIVE DIODE	08/962,867	11/3 /1997	6,140,678	10/31/2000
TRENCH-GATED POWER MOSFET WITH BIDIRECTIONAL VOLTAGE CLAMPING	08/920,330	8 /28/1997	6,049,108	4 /11/2000
BIDIRECTIONAL BLOCKING ACCUMULATION-MODE TRENCH POWER MOSFET	08/459,559	6 /2 /1995	5,661,322	8 /26/1997
PSEUDO-SCHOTTKY DIODE	08/648,334	5 /15/1996	5,818,084	10/6 /1998
HIGH DENSITY TRENCH DMOS TRANSISTOR WITH TRENCH BOTTOM IMPLANT	08/964,419	11/4 /1997	5,929,481	7 /27/1999
COMMUTATION DELAY GENERATOR FOR A MULTIPHASE BRUSHLESS DC MOTOR	08/595,812	2 /2 /1996	5,767,643	6 /16/1998
QUIET COMMUTATION CIRCUIT FOR AN ELECTRC MOTOR	08/573,802	12/18/1995	5,825,145	10/20/1998
CLOSED-LOOP FREQUENCY-TO-CURRENT CONVERTER WITH INTEGRABLE CAPACITANCES	08/594,676	2 /2 /1996	5,736,879	4 /7 /1998
CLOSED-LOOP FREQUENCY-TO-CURRENT CONVERTER WITH INTEGRABLE CAPACITANCES	08/979,837	11/26/1997	5,955,903	9 /21/1999
TRENCH-GATED MOSFET WITH INTEGRAL TEMPERATURE DETECTION DIODE	08/946,613	10/7 /1997	6,046,470	4 /4 /2000
SWITCHING DEVICE INCLUDING POWER MOSFET WITH INTERNAL POWER SUPPLY CIRCUIT	08/538,105	10/2 /1995	5,726,594	3 /10/1998

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SWITCHING DEVICE INCLUDING POWER MOSFET WITH INTERNAL POWER SUPPLY CIRCUIT	09/041,368	3 /11/1998	6,087,862	7 /11/2000
MULTIPLE GATED MOSFET FOR USE IN DC-DC CONVERTER	08/542,611	10/13/1995	5,616,945	4 /1 /1997
MULTIPLE GATED MOSFET FOR USE IN DC-DC CONVERTER	08/828,474	3 /31/1997	5,973,367	10/26/1999
RAPID CHARGING TECHNIQUE FOR LITHIUM ION BATTERIES	08/615,517	3 /12/1996	5,670,862	9 /23/1997
LOW VOLTAGE MOSFET WITH LOW ON-RESISTANCE AND HIGH BREAKDOWN VOLTAGE	08/570,876	12/12/1995	5,939,752	8 /17/1999
VERTICAL POWER MOSFET HAVING REDUCED SENSITIVITY TO VARIATIONS IN THICKNESS OF EPITAXIAL LAYER	08/616,393	3 /15/1996	5,814,858	9 /29/1998
SHORT CIRCUIT PROTECTED DC-DC CONVERTER USING DISCONNECT SWITCHING AND METHOD OF PROTECTING LOAD AGAINST SHORT CIRCUITS	08/956,257	10/22/1997	6,031,702	2 /29/2000
METHOD OF FABRICATING A FIELD EFFECT TRANSISTOR	08/767,708	12/17/1996	6,090,716	7 /18/2000
LONG-CHANNEL TRENCH-GATED POWER MOSFET HAVING FULLY DEPLETED BODY REGION	08/651,232	5 /22/1996	5,998,834	12/7 /1999
THREE-TERMINAL POWER MOSFET SWITCH FOR USE AS SYNCHRONOUS RECTIFIER OR VOLTAGE CLAMP	08/648,266	5 /15/1996	5,744,994	4 /28/1998
THREE-TERMINAL POWER MOSFET SWITCH FOR USE AS SYNCHRONOUS RECTIFIER OR VOLTAGE CLAMP	08/937,941	9 /25/1997	5,929,690	7 /27/1999
FOUR-TERMINAL POWER MOSFET SWITCH HAVING REDUCED THRESHOLD VOLTAGE AND ON-RESISTANCE AND METHOD OF SWITCHING A CURRENT	08/649,747	5 /15/1996	5,689,144	11/18/1997
SYNCHRONOUS CURRENT SHARING PULSE WIDTH MODULATOR	08/701,114	8 /21/1996	5,808,453	9 /15/1998
HIGH VOLTAGE MOS TRANSISTORS WITH REDUCED PARASITIC CURRENT GAIN	07/849,723	3 /11/1995	5,218,228	6 /8 /1993
METHOD OF MAKING A TRENCH MOSFET WITH HEAVILY DOPED DELTA LAYER TO PROVIDE LOW ON-RISISTANCE	08/482,357	6 /6 /1995	5,688,725	11/18/1997
TRENCH MOSFET WITH HEAVILY DOPED DELTA LAYER TO PROVIDE LOW ON-RISISTANCE	08/895,497	7 /16/1997	6,008,520	12/28/1999
SEPARATE CIRCUIT DEVICES IN AN INTRA-PACKAGE CONFIGURATION AND ASSEMBLY TECHNIQUES	08/556,369	11/13/1995	6,066,890	5 /23/2000
TRENCHED FIELD EFFECT TRANSISTOR WITH PN DEPLETION BARRIER	08/742,326	10/31/1996	5,917,216	6 /29/1999
FABRICATION OF HIGH-DENSITY TRENCH DMOS USING SIDEWALL SPACERS	08/646,593	5 /8 /1996	5,904,525	5 /18/1999

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TRENCH-GATED SCHOTTKY DIODE WITH INTEGRAL CLAMPING DIODE	08/832,012	4 /2 /1997	6,078,090	6 /20/2000
SAFETY SWITCH FOR LITHIUM ION BATTERY	08/899,001	7 /24/1997	5,909,103	6 /1 /1999
PLANAR DMOS TRANSISTOR FABRICATED BY A THREE MASK PROCESS	08/922,672	9 /3 /1997	5,923,979	7 /13/1999
ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT	09/002,179	12/31/1997	6,060,752	5 /9 /2000
VERTICAL DMOS FIELD EFFECT TRANSISTOR WITH CONFORMAL BURIED LAYER FOR REDUCED ON-RESISTANCE	09/071,729	5 /1 /1998	6,072,216	6 /6 /2000
COMPLEMENTARY, ISOLATED DMOS IC TECHNOLOGY	07/268,839	11/8 /1988	5,156,989	10/20/1992
ISOLATED DMOS IC TECHNOLOGY	07/904,402	6 /24/1992	5,485,027	1 /16/1996
LIGHTLY DOPED DRAIN MOSFET WITH REDUCED ON-RESISTANCE	07/210,959	6 /24/1988	5,237,193	8 /17/1993
LIGHTLY DOPED DRAIN MOSFET WITH REDUCED ON-RESISTANCE	08/046,058	4 /12/1993	5,306,656	4 /26/1994
CLOSED CELL TRANSISTOR WITH BUILT-IN VOLTAGE CLAMP	07/597,118	10/12/1990	5,136,349	8 /4 /1992
METHOD OF FRBRICATING A SHORT-CHANNEL LOW VOLTAGE DMOS TRANSISTOR	07/420,971	10/13/1989	4,931,408	6 /5 /1990
TRENCH MOSFET HAVING IMPROVED BREAKDOWN AND ON-RESISTANCE CHARACTERISTICS	09/200,197	11/25/1998	6,084,264	7 /4 /2000
TRENCH DMOS POWER TRANSISTOR WITH FIELD-SHAPING BODY PROFILE AND THREE-DIMENSIONAL GEOMETRY	07/762,103	9 /18/1991	5,298,442	3 /29/1994
METHOD OF MAKING A VERTICAL CURRENT FLOW FIELD EFFECT TRANSISTOR	07/107,725	10/8 /1987	5,164,325	11/17/1992
METHOD OF MAKING A VERTICAL CURRENT FLOW FIELD EFFECT TRANSISTOR	07/910,864	7 /8 /1992	5,298,781	3 /29/1994
METHOD OF MAKING A VERTICAL CURRENT FLOW FIELD EFFECT TRANSISTOR	07/978,201	11/18/1992	5,576,245	11/19/1996
POWER MOSFET HAVING VOLTAGE-CLAMPED GATE	09/306,003	5 /5 /1999	6,172,383	1 /9 /2001
POWER MOSFET INCLUDING INTERNAL POWER SUPPLY CIRCUITRY	09/041,368	3 /11/1998	6,087,862	7 /11/2000
FLOATING DRIVE TECHNIQUE FOR REVERSE BATTERY PROTECTION	08/067,365	5 /26/1993	5,539,610	7 /23/1996
SURFACE MOUNT AND FLIP CHIP TECHNOLOGY FOR TOTAL INTEGRATED CIRCUIT ISOLATION	08/444,660	5 /19/1995	5,753,529	5 /19/1998
SURFACE MOUNT AND FLIP CHIP TECHNOLOGY FOR TOTAL INTEGRATED CIRCUIT ISOLATION	08/603,512	2 /20/1996	5,757,081	5 /26/1998

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SURFACE MOUNT AND FLIP CHIP TECHNOLOGY WITH DIAMOND FILM PASSIVATION FOR TOTAL INTEGRATED CIRCUIT ISOLATION	08/634,957	4 /19/1996	5,767,578	6 /16/1998
BIDIRECTIONAL CURRENT BLOCKING MOSFET FOR BATTERY DISCONNECT SWITCHING	08/159,900	11/30/1993	5,536,977	7 /16/1996
BIDIRECTIONAL CURRENT BLOCKING MOSFET FOR BATTERY DISCONNECT SWITCHING INCLUDING PROTECTION AGAINST REVERSE CONNECTED BATTERY CHARGER	08/541,345	10/10/1995	5,682,050	10/28/1997
METHOD OF BLOCKING BIDIRECTIONAL FLOW OF CURRENT	08/636,367	4 /23/1996	5,747,891	5 /5 /1998
PORTABLE COMPUTER CONTAINING BIDIRECTIONAL CURRENT BLOCKING MOSFET FOR BATTERY DISCONNECT SWITCHING	09/006,774	1 /14/1998	6,087,740	7 /11/2000
TRENCHED DMOS TRANSISTOR FABRICATION USING SIX MASKS	07/928,909	8 /12/1992	5,316,959	5 /31/1994
TRENCHED DMOS TRANSISTOR FABRICATION HAVING THICK TERMINATION REGION OXIDE	08/603,047	2 /16/1996	5,639,676	6 /17/1997
TRENCHED DMOS TRANSISTOR HAVING THICK FIELD OXIDE IN TERMINATION REGION	08/625,639	3 /29/1996	5,578,851	11/26/1996
PWM MULTIPLEXED SOLENOID DRIVER	08/480,469	6 /7 /1995	5,621,604	4 /15/1997
METHOD OF FORMING A LATERAL FIELD EFFECT TRANSISTOR HAVING REDUCED DRAIN-TO-SOURCE ON-RESISTANCE	08/479,308	6 /7 /1995	5,750,416	5 /12/1998
SURGE CURRENT CHIP RESISTOR	10/002,868	11/15/2001	6,873,028	3 /29/2005
FLIP CHIP RESISTOR AND ITS MANUFACTURING METHOD	10/233,184	9 /3 /2002	6,727,798	4 /27/2004
Method of manufacturing flip chip resistor	10/440,941	5 /19/2003	7,089,652	8 /15/2006
GOLD DIFFUSION THIN FILM RESISTORS AND PROCESS	07/404,590	9 /8 /1989	5,023,589	6 /11/1991
MULTI-TAP THIN FILM INDUCTOR	09/074,185	5 /7 /1998	6,159,817	12/12/2000
High precision power resistors	10/304,261	11/25/2002	6,892,443	5 /17/2005
HIGH PRECISION POWER RESISTORS	10/762,609	1 /22/2004	7,154,370	12/26/2006
METHOD OF MANUFACTURING A RESISTOR	10/967,883	10/18/2004	7,278,201	10/9 /2007
FOIL STRAIN GAGE FOR AUTOMATED HANDLING AND PACKAGING	10/958,545	10/5 /2004	7,150,199	12/19/2006
High power resistor having an improved operating temperature range and method of making same	10/441,649	5 /20/2003	7,102,484	9 /5 /2006
METHOD FOR MAKING HIGH POWER RESISTOR HAVING IMPROVED OPERATING TEMPERATURE RANGE	10/744,846	12/23/2003	6,925,704	8 /9 /2005
High power resistor having an improved operating temperature range	11/123,508	5 /5 /2005	7,042,328	5 /9 /2006

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SURFACE MOUNT ELECTRICAL RESISTOR WITH THERMALLY CONDUCTIVE, ELECTRICALLY INSULATIVE FILLER AND METHOD FOR USING SAME	11/066,865	2 /25/2005	7,190,252	3 /13/2007
METHOD FOR INTERWOVEN SPREADING CODES	11/050,077	2 /3 /2005	7,394,845	7 /1 /2008
PRECISION HIGH-FREQUENCY CAPACITOR FORMED ON SEMICONDUCTOR SUBSTRATE	09/661,483	9 /14/2000	6,538,300	3 /25/2003
PRECISION HIGH-FREQUENCY CAPACITOR FORMED ON SEMICONDUCTOR SUBSTRATE	10/208,121	7 /29/2002	6,621,142	9 /16/2003
PRECISION HIGH-FREQUENCY CAPACITOR FORMED ON SEMICONDUCTOR SUBSTRATE	10/208,599	7 /29/2002	6,621,143	9 /16/2003
UNIVERSAL CASSETTE	11/229,875	9 /19/2005		
PROCESS OF FABRICATING A CHIP SCALE SURFACE MOUNT PACKAGE FOR SEMICONDUCTOR DEVICE	09/395,095	9 /13/1999	6,271,060	8 /7 /2001
CHIP SCALE SURFACE MOUNT PACKAGE FOR SEMICONDUCTOR DEVICE AND PROCESS OF FABRICATING THE SAME	09/733,823	12/8 /2000	6,441,475	8 /27/2002
CHIP SCALE SURFACE MOUNT PACKAGES FOR SEMICONDUCTOR DEVICE AND PROCESS OF FABRICATING THE SAME	09/395,094	9 /13/1999	6,316,287	11/13/2001
CHIP SCALE SURFACE MOUNT PACKAGE FOR SEMICONDUCTOR DEVICE AND PROCESS OF FABRICATING THE SAME	09/844,934	4 /26/2001	6,562,647	5 /13/2003
CHIP SCALE SURFACE MOUNT PACKAGE FOR SEMICONDUCTOR DEVICE AND PROCESS OF FABRICATING THE SAME	10/157,584	5 /28/2003	6,876,061	4 /5 /2005
FLUX CHanneled, HIGH CURRENT INDUCTOR	11/380,293	4 /26/2006		
HIGH PRECISION CAPACITOR WITH STANDOFF	11/415,039	5 /1 /2006	7,426,102	9 /16/2008
INDUCTOR WITH THERMALLY STABLE RESISTANCE	11/535,758	9 /27/2006		
CERAMIC DIELECTRIC FORMULATION FOR BROAD BAND UHF ANTENNA	11/759,523	6 /7 /2007		
POWER RESISTOR	11/862,572	9 /27/2007		
HIGH POWERED INDUCTORS USING A MAGNETIC BIAS	12/134,240	6 /6 /2008		
RESISTOR AND METHOD FOR MAKING SAME	12/026,939	2 /6 /2008		
HIGHLY COUPLED INDUCTOR	12/114,057	5 /2 /2008		
RESISTOR AND METHOD FOR MAKING SAME	12/205,197	9 /5 /2008		
BULK CAPACITOR AND METHOD	61/094,317	9 /4 /2008		
METAL STRIP RESISTOR FOR MITIGATING EFFECTS OF THERMAL EMF	61/161,636	3 /19/2009		
METAL STRIP RESISTOR FOR MITIGATING EFFECTS OF THERMAL EMF	12/536,792	8 /6 /2009		

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SUPER TRENCH MOSFET INCLUDING BURIED SOURCE ELECTRODE	11/698519	1/26/2007	7,557,409	7/7/2009
METHOD OF MANUFACTURING SURFACE MOUNT CAPACITOR	11/266915	11/4/2005	7,449,032	11/11/2008
PROCESS FOR MANUFACTURING TRENCH MIS DEVICE HAVING IMPLANTED DRAIN-DRIFT REGION AND THICK BOTTOM OXIDE	10/872931	6/21/2004	7,435,650	10/14/2008
METHOD OF FABRICATING TRENCH MIS DEVICE WITH THICK OXIDE LAYER IN BOTTOM OF TRENCH	11/335747	1/19/2006	7,416,947	8/26/2008
SEMICONDUCTOR PACKAGE INCLUDING DIE INTERPOSED BETWEEN CUP-SHAPED LEAD FRAME AND LEAD FRAME HAVING MESAS AND VALLEYS	10/996148	11/23/2004	7,394,150	7/1/2008
HIGH VOLTAGE CAPACITORS	11/359711	2/22/2006	7,336,475	2/26/2008
TRENCH MIS DEVICE HAVING IMPLANTED DRAIN-DRIFT REGION AND THICK BOTTOM OXIDE	11/158382	6/22/2005	7,326,995	2/5/2008
TRENCH MIS DEVICE HAVING IMPLANTED DRAIN-DRIFT REGION AND THICK BOTTOM OXIDE	10/454031	6/4/2003	7,291,884	11/6/2007
SURFACE MOUNT CHIP CAPACITOR	11/293673	12/2/2005	7,283,350	10/16/2007
TERMINATION FOR TRENCH MIS DEVICE HAVING IMPLANTED DRAIN-DRIFT REGION	11/232613	9/21/2005	7,268,032	9/11/2007
METHOD OF FABRICATING SEMICONDUCTOR PACKAGE INCLUDING DIE INTERPOSED BETWEEN CUP-SHAPED LEAD FRAME HAVING MESAS AND VALLEYS	10/996149	11/23/2004	7,238,551	7/3/2007
TRIPLE-DIFFUSED TRENCH MOSFET	11/150016	6/10/2005	7,233,043	6/19/2007
SURFACE MOUNT MELF CAPACITOR	11/264977	11/2/2005	7,221,555	5/22/2007
SUPER TRENCH MOSFET INCLUDING BURIED SOURCE ELECTRODE AND METHOD OF FABRICATING THE SAME	10/836833	4/30/2004	7,183,610	2/27/2007
SURFACE MOUNT CHIP CAPACITOR	11/266632	11/3/2005	7,179,309	2/20/2007
SURFACE MOUNT MELF CAPACITOR	11/259503	10/26/2005	7,167,357	1/23/2007
SURFACE MOUNT CAPACITOR AND METHOD OF MAKING SAME	11/132116	5/17/2005	7,161,797	1/9/2007
PROCESS OF FABRICATING TERMINATION REGION FOR TRENCH MIS DEVICE	11/141942	6/1/2005	7,118,953	10/10/2006
SURFACE MOUNT MELF CAPACITOR	10/792138	3/2/2004	7,088,573	8/8/2006
SURFACE MOUNT CHIP CAPACITOR	10/792639	3/2/2004	7,085,127	8/1/2006
TERMINATION FOR TRENCH MIS DEVICE HAVING IMPLANTED DRAIN-DRIFT REGION	10/810031	3/26/2004	7,045,857	5/16/2006
TRENCH MIS DEVICE HAVING IMPLANTED DRAIN-DRIFT REGION AND THICK BOTTOM OXIDE AND PROCESS FOR MANUFACTURING THE SAME	10/326311	12/19/2002	7,033,876	4/25/2006

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SELF-ALIGNED DIFFERENTIAL OXIDATION IN TRENCHES BY ION IMPLANTATION	10/180154	6/25/2002	7,012,005	3/14/2006
TRENCH MIS DEVICE WITH THICK OXIDE LAYER IN BOTTOM OF GATE CONTACT TRENCH	10/722984	11/25/2003	7,009,247	3/7/2006
METHOD OF SUPPRESSING THE OXIDATION CHARACTERISTICS OF NICKEL	11/219792	9/6/2005	7,208,218	4/24/2007
RF MODEM AND COMMUNICATIONS TRANSCEIVER UTILIZING SAW DEVICE AND PULSE SHAPING	09/688300	10/13/2000	6,970,496	11/29/2005
TERMINATION FOR TRENCH MIS DEVICE HAVING IMPLANTED DRAIN-DRIFT REGION	10/811443	3/26/2004	6,927,451	8/9/2005
METHOD FOR MAKING TRENCH MIS DEVICE WITH REDUCED GATE-TO-DRAIN CAPACITANCE	10/264816	10/3/2002	6,921,697	7/26/2005
SURFACE MOUNT FLIPCHIP CAPACITOR	10/792135	3/2/2004	6,914,770	7/5/2005
TRIPLE-DIFFUSED TRENCH MOSFET AND METHOD OF FABRICATING THE SAME	10/657830	9/8/2003	6,913,977	7/5/2005
SEMICONDUCTOR ASSEMBLY WITH PACKAGE USING CUP-SHAPED LEAD FRAME	10/291153	11/7/2002	6,909,170	6/21/2005
TRENCH MIS DEVICE WITH GRADUATED GATE OXIDE LAYER	10/106812	3/26/2002	6,903,412	6/7/2005
TRENCH MIS DEVICE WITH REDUCED GATE-TO-DRAIN CAPACITANCE	09/927320	8/10/2001	6,882,000	4/19/2005
METHOD OF FABRICATING TRENCH MIS DEVICE WITH GRADUATED GATE OXIDE LAYER	10/106896	3/26/2002	6,875,657	4/5/2005
TRENCH MIS DEVICE WITH ACTIVE TRENCH CORNERS AND THICK BOTTOM OXIDE AND METHOD OF MAKING THE SAME	09/927143	8/10/2001	6,849,898	2/1/2005
STRUCTURES OF AND METHODS OF FABRICATING TRENCH-GATED MIS DEVICES	10/104811	3/22/2002	6,838,722	1/4/2005
METHOD FOR MAKING TRENCH MOSFET HAVING IMPLANTED DRAIN-DRIFT REGION	10/317568	12/12/2002	6,764,906	7/20/2004
SEMICONDUCTOR DIE PACKAGE INCLUDING CUP-SHAPED LEADFRAME	09/468249	12/10/1999	6,744,124	6/1/2004
THICKER OXIDE FORMATION AT THE TRENCH BOTTOM BY SELECTIVE OXIDE DEPOSITION	10/176570	6/21/2002	6,709,930	3/23/2004
TRENCH DMOS POWER TRANSISTOR WITH FIELD-SHAPING BODY PROFILE AND THREE-DIMENSIONAL GEOMETRY	08/851608	5/5/1997	6,627,950	9/30/2003
TRENCH MOSFET HAVING IMPLANTED DRAIN-DRIFT REGION	10/211438	8/2/2002	6,600,193	7/29/2003
LOW-SIDE BIDIRECTIONAL BATTERY DISCONNECT SWITCH	08/800972	2/19/1997	6,590,440	7/8/2003

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PROCESS FOR MANUFACTURING TRENCH GATED MOSFET HAVING DRAIN/DRIFT REGION	09/898652	7/3/2001	6,569,738	5/27/2003
METHOD OF FORMING TERMINATION ON CHIP COMPONENTS	09/758800	1/11/2001	6,541,302	4/1/2003
METHOD OF FABRICATING TRENCH-GATED POWER MOSFET	09/816717	3/21/2001	6,534,366	3/18/2003
METHOD OF MAKING TRENCH-GATED MOSFET HAVING CESIUM GATE OXIDE LAYER	10/094476	3/7/2002	6,509,233	1/21/2003
PSEUDO-SCHOTTKY DIODE	09/037557	3/9/1998	6,476,442	11/5/2002
VERTICAL STRUCTURE AND PROCESS FOR SEMICONDUCTOR WAFER-LEVEL CHIP SCALE PACKAGES	09/545287	4/7/2000	6,392,290	5/21/2002
BARRIER ACCUMULATION MODE MOSFET	09/476320	12/30/1999	6,285,060	9/4/2001
METHOD OF FORMING VERTICAL PLANAR DMOSFET WITH SELF-ALIGNED CONTACT	09/293380	4/16/1999	6,277,695	8/21/2001
IC CHIP PACKAGE WITH DIRECTLY CONNECTED LEADS	09/089310	6/2/1998	6,249,041	6/19/2001
LOW RESISTANCE POWER MOSFET OR OTHER DEVICE CONTAINING SILICON-GERMANIUM LAYER	08/919386	8/28/1997	6,239,463	5/29/2001
VERTICAL TRENCH-GATED POWER MOSFET HAVING STRIPE GEOMETRY AND HIGH CELL DENSITY	09/089250	6/2/1998	6,204,533	3/20/2001
GAS-BASED SUBSTRATE DEPOSITION PROTECTION	08/487789	6/7/1995	5,925,411	7/20/1999
METHOD FOR FABRICATING A HIGH VOLTAGE MOS TRANSISTOR	07/678578	3/29/1991	5,132,235	7/21/1992
FUZED SOLID ELECTROLYTE CAPACITOR	07/677203	3/29/1991	5,099,397	3/24/1992
MOLDED FUZED SOLID ELECTROLYTE CAPACITOR	07/677204	3/29/1991	5,053,927	10/1/1991
METHOD FOR MAKING A LEAD FRAME WITH FUSE AND PRODUCT MADE THEREBY	07/500351	3/26/1990	5,011,067	4/30/1991
RF MODEM UTILIZING SAW RESONATOR AND CORRELATOR AND COMMUNICATIONS TRANSCEIVER CONSTRUCTED THEREFROM	09/419824	10/15/1999	6,535,545	3/18/2003
METHOD OF OPERATION OF PUNCH-THROUGH FIELD EFFECT TRANSISTOR	09/481135	1/11/2000	6,444,527	9/3/2002
METHOD AND APPARATUS FOR DELIVERING POWER SIMULTANEOUSLY TO RECHARGEABLE BATTERY AND ELECTRONIC DEVICE	09/502546	2/10/2000	6,300,744	10/9/2001
METHOD OF FABRICATING TRENCH JUNCTION BARRIER RECTIFIER	10/146539	5/14/2002	7,186,609	3/6/2007
METHOD FOR MAKING CONDUCTIVE POLYMER CAPACITOR	09/824287	4/2/2001	6,451,074	9/17/2002
METHOD FOR DOPING SINTERED TANTALUM AND NIOBIUM PELLETS WITH NITROGEN	09/514221	2/25/2000	6,410,083	6/25/2002

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METHOD FOR CENTERING CAPACITOR IN MOLD	09/577745	5/23/2000	6,368,364	4/9/2002
METHOD OF SUPPRESSING THE OXIDATION CHARACTERISTICS OF NICKEL	10/047709	1/15/2002	6,723,280	4/20/2004
SINTERED TANTALUM AND NIOBIUM CAPACITOR PELLETS DOPED WITH NITROGEN, AND METHOD OF MAKING THE SAME	09/726866	11/30/2000	6,447,570	9/10/2002
CAPACITOR TERMINATION ASSEMBLY	09/843109	4/25/2001	6,411,499	6/25/2002
TANTALUM CHIP CAPACITOR AND METHOD	09/167690	10/7/1998	6,238,444	5/29/2001
METHOD FOR DOPING SINTERED TANTALUM AND NIOBIUM PELLETS WITH NITROGEN	09/146685	9/3/1998	6,185,090	2/6/2001
METHOD FOR DOPING SINTERED TANTALUM PELLETS WITH NITROGEN	09/064475	4/22/1998	6,010,660	1/4/2000
APPARATUS AND METHOD FOR CONFORMALLY COATING A CAPACITOR	08/710364	9/16/1996	5,888,590	3/30/1999
DOPED SINTERED TANTALUM PELLETS WITH NITROGEN IN A CAPACITOR	08/790293	1/29/1997	5,825,611	10/20/1998
HIGH DENSITY TRENCH-GATED POWER MOSFET	09/428299	10/27/1999	6,348,712	2/19/2002
BIDIRECTIONAL CURRENT BLOCKING MOSFET FOR BATTERY DISCONNECT SWITCHING	08/159900	11/30/1993	5,536,977	7/16/1996