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SUBMISSION TYPE: NEW ASSIGNMENT

NATURE OF CONVEYANCE: ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Industrial Technology Research Institute	11/24/2006

RECEIVING PARTY DATA

Name:	TransPacific IP Ltd.
Street Address:	No. 205, Dunhua N. Road, Songshan District
Internal Address:	Room 1402, 14F
City:	Taipei City
State/Country:	TAIWAN
Postal Code:	105

PROPERTY NUMBERS Total: 2

Property Type	Number	
Application Number:	11541517	
Patent Number:	7852376	

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NAME OF SUBMITTER: Masako Calico

Total Attachments: 3

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> PATENT REEL: 025542 FRAME: 0602

ASSIGNMENT OF PATENT

Atty Dkt No.: 3///- 783

WHEREAS, Industrial Technology Research Institute, of Hsinchu, Taiwan, whose address is No. 195, Sec. 4, Chung Hsing Rd., Chutung, Hsinchu, Taiwan, 310, R.O.C., is the owner of the entire right, title and interest of the following U.S. Patent(s):

Patent No. <u>Title</u>

SEE ATTACHED APPENDIX A
(2 pages indicating 39 separate U.S. Patents)

WHEREAS, TransPacific IP Ltd., of Taipei, Taiwan, whose address is Room 1402, 14F, No. 205, Dunhua N. Road, Songshan District, Taipei City 105, Taiwan, R.O.C., hereinafter referred to as "assignee", is desirous of acquiring the entire right, title and interest in the same;

NOW, THEREFORE, for good and valuable consideration, the receipt whereof is acknowledged, we, the patent owners, by these presents do sell, assign and transfer unto said assignee the entire right, title and interest in and to the aforementioned Patents; the same to be held and enjoyed by the said assignee for his own use and behoof, and for his legal representatives and assigns, to the full end of the term for which said Patents is granted, as fully and entirely as the same would have been held by me had this assignment and sale not been made.

Executed this 24th day of November, 2006, at Hsinchu

Benjamin Wang

Depuly General Director
(Title)

PATENT REEL: 025542 FRAME: 0603

ASSIGNMENT OF PATENT APPENDIX A

Atty Dkt No.:

BHT/

PATENT NOS.	TITLES
5,274,364	WINDOW CLIPPING METHOD AND DEVICE
5,307,422	METHOD AND SYSTEM FOR IDENTIFYING LINES OF TEXT IN A DOCUMENT
5,577,221	METHOD AND DEVICE FOR EXPANDING ROM CAPACITY
5,253,305	CHARACTER BOUNDARY IDENTIFICATION METHOD AND SYSTEM
5,659,707	TRANSFER LABELING MECHANISM FOR MULTIPLE OUTSTANDING READ REQUESTS ON A SPLIT TRANSACTION BUS
5,357,582	CHARACTER BOUNDARY IDENTIFICATION METHOD AND SYSTEM
5,410,614	RUN-BASED METHOD FOR SMOOTHING HANDWRITTEN CHINESE CHARACTERS
5,638,467	BIT-REVERSING METHOD AND SYSTEM FOR LINEAR IMAGE SCALING
5,255,366	ADDRESS PROCESSING UNIT FOR A GRAPHICS CONTROLLER
5,299,309	FAST GRAPHICS CONTROL SYSTEM CAPABLE OF SIMULTANEOUSLY STORING AND EXECUTING GRAPHICS COMMANDS
5,522,021	WORD BOUNDARY PARAMETER EVALUATION FOR BIT BLOCK TRANSFER OPERATION IN A COMPUTER GRAPHICS SYSTEM
5,623,635	MEMORY CONSISTENT PRE-OWNERSHIP METHOD AND SYSTEM FOR TRANSFERRING DATA BETWEEN AND I/O DEVICE AND A MAIN MEMORY
5,844,825	BIDIRECTIONAL SHIFTER CIRCUIT
5,841,902	SYSTEM AND METHOD FOR UNCONSTRAINED ON-LINE ALPHA-NUMERICAL HANDWRITING RECOGNITION
5,268,840	METHOD AND SYSTEM FOR MORPHOLOGIZING TEXT
5,388,207	ARCHITECTURE FOR A WINDOW-BASED GRAPHICS SYSTEM
5,615,334	MEMORY REFELECTION SYSTEM AND METHOD FOR REDUCING BUS UTILIZATION AND DEVICE IDLE TIME IN THE EVENT OF FAULTS
6,011,558	INTELLIGENT STITCHER FOR PANORAMIC IMAGE-BASED VIRTUAL WORLDS
6,751,599	FUZZY INFERENCE SYSTEM FOR SIMPLIFYING MESH
6,798,923	APPARATUS AND METHOD FOR PROVIDING PANORAMIC IMAGES

6,373,495	APPARATUS AND METHOD FOR TEXTURE MAPPING USING MULTIPLE LEVELS OF DETAIL
6,477,268	PRODUCING TRANSITIONS BETWEEN VISTAS
6,243,786	APPARATUS AND METHOD FOR GENERATING AN INTERRUPT PROHIBITED ZONE IN PIPELINED DATA PROCESSORS
6,209,086	METHOD AND APPARATUS FOR FAST RESPONSE TIME INTERRUPT CONTROL IN A PIPELINED DATA PROCESSOR
6,486,908	AN IMAGE-BASED METHOD AND SYSTEM FOR BUILDING SPHERICAL PANORAMAS
5,903,279	METHOD FOR ANTIALIASING
5,956,751	AN IMPROVED COMPUTER MEMORY MANAGEMENT SYSTEM USING PHYSICAL SEGMENTATION SPACE ALLOCATION
6,028,584	REAL-TIME PLAYER FOR PANORAMIC IMAGED-BASED VIRTUAL WORLDS
5,799,207	NON-BLOCKING PERIPHERAL ACCESS ARCHITECTURE HAVING A REGISTER CONFIGURED TO INDICATE A PATH SELECTION FOR DATA TRANSFER BETWEEN A MASTER, MEMORY, AND AN I/O DEVICE
6,115,551	SYSTEM FOR MINIMIZING THE NUMBER OF CONTROL SIGNALS AND MAXIMIZING CHANNEL UTILIZATION BETWEEN AND I/O BRIDGE AND A DATA BUFFER
5,822,772	MEMORY CONTROLLER AND METHOD OF MEMORY ACCESS SEQUENCE REORDERING THAT ELIMINATES PAGE MISS AND ROW MISS PENALTIES
6,266,065	METHOD FOR RENDERING 3D TRIANGLE PRIMITIVES
5,940,870	ADDRESS TRANSLATION FOR SHARED MEMORY MULTIPROCESSOR CLUSTERING
5,923,778	HIERARCHICAL REPRESENTATION OF REFERENCE DATABASE FOR AN ON-LINE CHINESE CHARACTER RECOGNITION SYSTEM
5,796,867	STROKE-NUMBER-FREE AND STROKE-ORDER-FREE ON-LINE CHINESE CHARACTER RECOGNITION METHOD
5,909,509	STATISTICAL-BASED RECOGNITION OF SIMILAR CHARACTERS
5,923,792	SCREEN DISPLAY METHODS FOR COMPUTER-AIDED DATA ENTRY
5,740,344	TEXTURE FILTER APPARATUS FOR COMPUTER GRAPHICS SYSTEM
5,754,185	APPARATUS FOR BLENDING PIXELS OF A SOURCE OBJECT AND DESTINATION PLANE

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