PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	EXECUTIVE ORDER 9424, CONFIRMATORY LICENSE

CONVEYING PARTY DATA

Name	Execution Date
University of Michigan	11/19/2010

RECEIVING PARTY DATA

Name:	National Science Foundation	
Street Address:	4201 Wilson Blvd	
Internal Address:	Room 1265	
City:	Arlington	
State/Country:	VIRGINIA	
Postal Code:	22230	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	12945020

CORRESPONDENCE DATA

Fax Number: (703)292-9041

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Email: nsfpatents@nsf.gov

Correspondent Name: National Science Foundation

Address Line 1: 4201 Wilson Blvd Address Line 2: Room 1265

Address Line 4: Arlington, VIRGINIA 22230

NAME OF SUBMITTER: Robin Clay

Total Attachments: 1 source=20#page1.tif

PATENT REEL: 025568 FRAME: 0640

License to the United States Government

Invention Title: Automatic Scalable Verification for Hardware Designs at the Register Transfer Level

Inventor(s): Mark Liffiton, Zaher S Andraus, Karem A Sakallah

U.S. Filing/Issue Date: <u>11/12/2010</u> / <u>00/00/00</u>

Patent or Application Serial No.: 12/945,020

Grant/Contract Numbers:: NSF - IIS0705103, DOD/DARPA - HR0011-04-3-0002.

Foreign Applications filed/intended in (countries):

The invention identified above is a Subject Invention under 35 U.S.C. 200, et seq., and the Standard Patent Rights clause at 37 CFR 401.14, FAR 52.227-11 or FAR 52.227-12 (if applicable) which are included among the terms of the above identified grant or contract award from the United State Government This document is confirmatory of:

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2. All other rights acquired by the Government by reason of the above identified grant/contract award and the laws and regulations that are applicable to the award.

The Government is hereby granted an irrevocable power to inspect and make copies of the above-identified patent application.

Signed this 19th day of November, 2010.

By: Michael Hallman

(Name of Grantee/Contractor Official)

(Signature)

Title: Patent Administrator

For: University of Michigan

(Grantee/Contractor Organization)

At: 1214 S. University Ave. 2nd Floor, Ann Arbor, MI 48104-2592

(Business Address)

PATENT REEL: 025568 FRAME: 0641

RECORDED: 12/27/2010