

PATENT ASSIGNMENT

Electronic Version v1.1
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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
CoWare, LLC	02/03/2011

RECEIVING PARTY DATA

Name:	SYNOPSYS, INC.
Street Address:	700 East Middlefield Road
City:	Mountain View
State/Country:	CALIFORNIA
Postal Code:	94043

PROPERTY NUMBERS Total: 18

Property Type	Number
Patent Number:	7313773
Patent Number:	7373638
Patent Number:	7512732
Patent Number:	7716031
Patent Number:	7742905
Patent Number:	7783997
Patent Number:	7788078
Patent Number:	7881921
Application Number:	10309554
Application Number:	10816328
Application Number:	10941457
Application Number:	11069616
Application Number:	11356578
Application Number:	11540146
Application Number:	11541315

CH \$720.00 7313773

Application Number:	11584402
Application Number:	11707413
Application Number:	12871884

CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	SNSY-1371.US
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NAME OF SUBMITTER:	Anthony C. Murabito
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Total Attachments: 2
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ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, COWARE, LLC, a Delaware limited liability corporation, having a place of business at 700 East Middlefield Road, Mountain View, CA 94043 ("ASSIGNOR"), has sold, assigned, and transferred and does hereby sell, assign, and transfer to SYNOPSYS, INC. a Delaware corporation, having a place of business at 700 East Middlefield Road, Mountain View, CA 94043 ("ASSIGNEE"), for itself and its successors, transferees, and assignees, the following:

1. The entire worldwide right, title, and interest in all inventions and improvements ("SUBJECT MATTER") that are disclosed in the following applications and Letters Patents ("PATENT RIGHTS"):

Country	Application No.	Title	Filing Date
US	10309554	METHOD AND SYSTEM FOR INSTRUCTION-SET ARCHITECTURE SIMULATION USING JUST IN TIME COMPILATION	12-03-2002
US	10816328	RESOURCE MANAGEMENT IN A MULTICORE ARCHITECTURE	03-31-2004
US	10941457	DEBUG IN A MULTICORE ARCHITECTURE	09-14-2004
US	11069616	EFFICIENT CLOCK MODELS AND THEIR USE IN SIMULATION	02-28-2005
US	11356578	SCALABLE LANGUAGE INFRASTRUCTURE FOR ELECTRONIC SYSTEM LEVEL TOOLS	02-17-2006
US	11540146	SCHEDULING IN A MULTICORE PROCESSOR	09-29-2006
US	11541315	MANAGING POWER CONSUMPTION IN A MULTICORE PROCESSOR	09-29-2006
US	11584402	DYNAMIC HOST CODE GENERATION FROM ARCHITECTURE DESCRIPTION FOR FAST SIMULATION	10-19-2006
US	11707413	RUN-TIME SWITCHING FOR SIMULATION WITH DYNAMIC RUN-TIME ACCURACY ADJUSTMENT	02-16-2007
US	12871884	TECHNIQUES FOR PROCESSOR/MEMORY CO-EXPLORATION AT MULTIPLE ABSTRACTION LEVELS	08-30-2010

Country	Patent No.	Title	Issue Date
US	7313773	METHOD AND DEVICE FOR SIMULATOR GENERATION BASED ON SEMANTIC TO BEHAVIORAL TRANSLATION	12-25-2007
US	7373638	AUTOMATIC GENERATION OF STRUCTURE AND CONTROL PATH USING HARDWARE DESCRIPTION LANGUAGE	05-13-2008
US	7512732	METHOD OF PROTOCOL CONVERSION BETWEEN SYNCHRONOUS PROTOCOLS THAT ARE SUITABLE FOR SYNTHESIS	03-31-2009
US	7716031	INTERFACE CONVERTER FOR UNIFIED VIEW OF MULTIPLE COMPUTER SYSTEM SIMULATIONS	05-11-2010
US	7742905	METHOD AND SYSTEM FOR DYNAMICALLY ADJUSTING SPEED VERSUS ACCURACY OF COMPUTER PLATFORM SIMULATION	06-22-2010
US	7783997	LARGE SCALE FINITE STATE MACHINES	08-24-2010
US	7788078	PROCESSOR/MEMORY CO-EXPLORATION AT MULTIPLE ABSTRACTION LEVELS	08-31-2010
US	7881921	CACHING INFORMATION TO MAP SIMULATION ADDRESSES TO HOST ADDRESSES IN COMPUTER SYSTEM SIMULATIONS	02-01-2011

and;


2. The entire worldwide right, title, and interest in and to:
(a) the PATENT RIGHTS, including any right of priority; (b) any provisional, divisional, continuation, substitute, renewal, reissue, and other related applications thereto which have been or may be filed in the United States or elsewhere in the world; (c) any patents which may be granted on the applications set forth in (a) and (b) above; and (d) the right to sue in its own name and to recover for past infringement of any or all of any applications or patents issuing therefrom together with all rights to recover damages for infringement of provisional rights.

ASSIGNOR agrees to do the following, when requested, and without further consideration, in order to carry out the intent of this Assignment: (1) execute all oaths, assignments, powers of attorney, applications, and other papers necessary or desirable to fully secure to ASSIGNEE the rights, titles and interests herein conveyed; (2) communicate to ASSIGNEE all known facts relating to the SUBJECT MATTER of the above-identified patent applications and Letters Patents; and (3) generally do all lawful acts that ASSIGNEE shall consider desirable for securing, maintaining, and enforcing worldwide patent protection relating to the SUBJECT MATTER of the above-identified patent applications and Letters Patents and for vesting in ASSIGNEE the rights, titles, and interests herein conveyed. ASSIGNOR further agrees to provide any successor, transferee, assignee, or legal representative of ASSIGNEE with the benefits and assistance provided to ASSIGNEE hereunder.

ASSIGNOR represents that ASSIGNOR has the rights, titles, and interests to convey as set forth herein, and covenants with ASSIGNEE that the ASSIGNOR has made or will make hereafter no assignment, grant, mortgage, license, or other agreement affecting the rights, titles, and interests herein conveyed.

Duly Authorized Representative of ASSIGNOR

Date of Signature

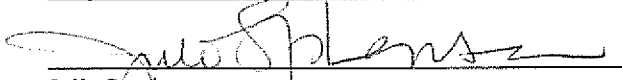


February 3, 2011

Brian E. Cabrera
Manager
COWARE, LLC
700 East Middlefield Road
Mountain View, CA 94043

Duly Authorized Representative of ASSIGNEE

Date of Signature



February 3, 2011

Julie Stephenson
Associate General Counsel, Chief IP Counsel
SYNOPSIS, INC.
700 East Middlefield Road
Mountain View, CA 94043