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TO:CHRISTOPHER F. REGAN COMPANY:255 S. ORANGE AVE.

**PATENT ASSIGNMENT**

Electronic Version v1.1  
Stylesheet Version v1.1



SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

**CONVEYING PARTY DATA**

Name	Execution Date
STMICROELECTRONICS S.R.L.	07/28/2008

**RECEIVING PARTY DATA**

Name:	ST WIRELESS SA
Street Address:	30 CHEMIN DU CHAMP-DES-FILLES
Internal Address:	1228 PLAN-LES-OUATES
City:	GENEVA
State/Country:	SWITZERLAND

**PROPERTY NUMBERS Total: 1**

Property Type	Number
Application Number:	11197241

**CORRESPONDENCE DATA**

Fax Number: (321)984-7078  
*Correspondence will be sent via US Mail when the fax attempt is unsuccessful.*  
Phone: 407-841-2330  
Email: cregan@addmg.com  
Correspondent Name: CHRISTOPHER F. REGAN  
Address Line 1: 255 S. ORANGE AVE.  
Address Line 2: STE. 1401  
Address Line 4: ORLANDO, FLORIDA 32801

ATTORNEY DOCKET NUMBER:	53492 (02-AQ-471/GC)
NAME OF SUBMITTER:	CHRISTOPHER F. REGAN

Total Attachments: 10  
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TO:CHRISTOPHER F. REGAN COMPANY:255 S. ORANGE AVE.

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**DEED OF TRANSFER FOR ST PATENTS**

WHEREAS, STMicroelectronics NV, a public company with limited liability incorporated under the laws of the Netherlands, with corporate seat in Amsterdam, the Netherlands, and address at WTC Schiphol Airport, Schiphol Boulevard 265, 1118 BH Schiphol Airport, Amsterdam, the Netherlands, and

STMicroelectronics S.r.l., (formerly known as SGS-Thomson Microelectronics S.r.l.), a corporation organized under the laws of Italy, having a place of business at Via C. Olivetti, 2-20041 Agrate Brianza, Milano, Italy,

(hereinafter collectively referred to as "Assignor") has rights in and to the following:

**LISTED IN EXHIBIT A**

(said patents, patent applications and invention disclosures listed in Exhibit A being hereinafter referred to as "SAID PATENT RIGHTS"); and

WHEREAS, ST Wireless SA, a legal entity incorporated under the laws of Switzerland, and address at 39 Chemin du Champ-des-Filles, 1228 Plan-les-Quates, Geneva, Switzerland (hereinafter referred to as "Assignee") is desirous of obtaining rights to SAID PATENT RIGHTS;

NOW, THEREFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, Assignor does hereby sell, convey, transfer and assign to Assignee, its successors, assigns and legal representatives, subject to prior encumbrances, all its right, title and interest in and to SAID PATENT RIGHTS, together with its right to claim priority, together with any and all continuations, continuations-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with its right to sue for and be entitled to any

damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof. Assignee hereby accepts such conveyance, transfer and assignment of SAID PATENT RIGHTS.

Signed this 28th day of July, 2008.

STMicroelectronics NV

By: [Signature]

Printed Name: ALDO ROMANO

Title: General Counsel

Place: Geneva

STMicroelectronics S.r.l. (formerly known as SGS-Thomson Microelectronics S.r.l.)

By: [Signature]

Printed Name: ALDO ROMANO

Title: PRESIDENT

Place: AGRATE B.

[Signature]  
PIETRO PALELLA  
GENERAL MANAGER  
AGRATE B.

ST Wireless SA

By: [Signature]

Printed Name: RODOLFO

Title: MANAGER

Place: GENEVA

## Exhibit A

US Patent	Country	Current Status	Non-US Patent	Filing Number	Title
5515854	US	Granted		10033384	TEST BOARD DE-EMBEDDING METHOD TO IMPROVE RF MEASUREMENTS ACCURACY ON AN AUTOMATIC TESTING EQUIPMENT FOR IC WAFERS
5997801	US	Granted		10488790	HIGH-SPEED, HIGH-RESOLUTION AND LOW-CONSUMPTION ANALOG/DIGITAL CONVERTER WITH SINGLE-ENDED INPUT
	IT	Granted	1405419	02743185.7	
	GB	Granted	1405419	02743185.7	
	FR	Granted	1405419	02743185.7	
	DE	Granted	60205809	02743185.7	
	CN	Publication		20028013648	
5621444	US	Granted		10174501	A HIGH SPEED, LOW POWER SWITCHED-CAPACITOR DIGITAL-TO-ANALOG CONVERTER WITH A PRECHARGE ARRANGEMENT
6720903	US	Granted		10172376	METHOD OF OPERATING SAR-TYPE ADC AND AN ADC USING THE METHOD
6812770	US	Granted		10352788	MIXER WITH EXPONENTIALLY VARIABLE GAIN
	IT	Granted	1337035	02425035.9	
	GB	Granted	1337035	02425035.9	
	FR	Granted	1337035	02425035.9	
	DE	Granted	1337035	02425035.9	
6080615	US	Granted		10174528	METHOD AND DEVICE FOR CHECKING THE CHARGE STATE OF A BATTERY, IN PARTICULAR A RECHARGEABLE BATTERY FOR A CELLULAR MOBILE TELEPHONE
	FR	Abandoned	2828125	0108053	
7098331	US	Granted		10791883	HIGH RESOLUTION AND LOW CONSUMPTION DIGITAL-ANALOG CONVERTER
	US	Allowance		11272937	
	IT	Abandoned	1458102	03425160.3	
	GB	Abandoned	1458102	03425160.3	
	FR	Abandoned	1458102	03425160.3	
	EP	Abandoned		06116241.5	
	DE	Abandoned	00307009	03425160.3	
7158069	US	Granted		11007458	ANALOG-DIGITAL CONVERTER
	IT	Granted	1583242	04425242.7	
	GB	Granted	1583242	04425242.7	
	FR	Granted	1583242	04425242.7	
6133708	US	Granted		09087549	CONTROL CIRCUIT FOR THE CURRENT SWITCH EDGES OF A POWER TRANSISTOR
	IT	Abandoned	0881770	97830258.6	
	GB	Granted	0881770	97830258.6	
	FR	Granted	0881770	97830258.6	
6433724	US	Granted		09833015	ANALOG/DIGITAL CONVERTER WITH SINGLE-ENDED INPUT
	IT	Abandoned	1039842	99830170.9	
	GB	Abandoned	1039842	99830170.9	
6285233	US	Granted		09429779	LEVEL SHIFTER ELECTRONIC DEVICE HAVING A VERY LOW CONSUMPTION
	IT	Granted	0999644	98830653.6	
	GB	Granted	0999644	98830653.6	
6433647	US	Granted		09429700	LOW NOISE I-Q MIXER
	IT	Granted	0998025	98830657.7	
	FR	Granted	0998025	98830657.7	

## Exhibit A

US Patent	Country	Current Status	Non-US Patent	Filing Number	Title
6636678	US	Granted		09413713	SETTLING TIME REDUCTION PROCESS IN PLL CIRCUITS
6433510	US	Granted		09697232	CHARGING END-POINT CONTROL CIRCUIT FOR A BATTERY
0228348	IT	Granted	1318838	MIRBA02250	HIGH FREQUENCY HYSTERESIS COMPARATOR, WITH LOW DYNAMIC DIFFERENTIAL INPUT SIGNAL, AND SUPPLY COMPATIBLE
6518901	US	Granted		09884672	BOOSTED SWITCH DEVICE FOR A SAMPLER OF AN ANALOG/DIGITAL CONVERTER, AND OPERATING METHOD THEREOF
6624672	EP	Abandoned		00830430.6	
	US	Granted		10032232	OUTPUT BUFFER WITH CONSTANT SWITCHING CURRENT
	IT	Granted	1217744	00830838.3	
	GB	Granted	1217744	00830838.3	
	FR	Granted	1217744	00830838.3	
6582602	US	Granted		10032231	CIRCUIT GENERATING A STABLE REFERENCE VOLTAGE WITH RESPECT TO TEMPERATURE, PARTICULARLY FOR CMOS PROCESSES
6914457	US	Granted		10480035	DIGITAL SYSTEM WITH AN OUTPUT BUFFER WITH A SWITCHING CURRENT SETTABLE TO LOAD-INDEPENDENT CONSTANT VALUES
	EP	Abandoned		02428375.3	
6601406	US	Granted		09908975	DIGITAL FILTER
6861285	US	Granted		10075161	VARIABLE GAIN AMPLIFIER
6787832	US	Granted		10175885	METHOD FOR TESTING INTEGRATED CIRCUITS
	IT	Abandoned	1271189	01880417.0	
	GB	Abandoned	1271189	01880417.0	
7212148	US	Granted		11338857	CIRCUIT FOR SELECTIVELY ANALOG SIGNALS INTO DIGITAL CODES
7160300	US	Granted		11113854	SWITCHED CAPACITANCE CIRCUIT AND ANALOG/DIGITAL CONVERTER INCLUDING SAID CIRCUIT
	EP	Publication		04425219.3	
7106237	US	Granted		11097455	LOW CONSUMPTION AND LOW NOISE ANALOG-DIGITAL CONVERTER OF THE SAR TYPE AND METHOD OF EMPLOYING IT
	IT	Granted	1589244	04425241.9	
	GB	Granted	1589244	04425241.9	
	FR	Granted	1589244	04425241.9	
7280089	US	Granted		11423200	SINGLE-LOOP SWITCHED-CAPACITOR ANALOG-TO-DIGITAL SIGMA-DELTA CONVERTER
	EP	Publication		05425418.4	
5541655	US	Granted		08347574	HIGH PERFORMANCE TRANSCONDUCTANCE OPERATIONAL AMPLIFIER OF THE CMOS INTEGRATED TYPE

## Exhibit A

US Patent	Country	Current Status	Non-US Patent	Filing Number	Title
5856072	US	Granted		08791281	LOW DISTORTION CIRCUIT WITH SWITCHED CAPACITORS
5884425	US	Granted		08394671	ANALOG SWITCH FOR LOW SUPPLY VOLTAGE MOS INTEGRATED CIRCUITS
5917052	US	Granted		08509304	ELECTRONIC SWITCH HAVING REDUCED BODY EFFECT
5968484	US	Granted		08451030	CIRCUIT AND METHOD FOR DRIVING ALTERNATIVELY ELECTRIC LOADS WITH LOW IMPEDANCE
5745002	GB US	Granted Granted	0825106	84830285.8 08328107	LOW VOLTAGE, SWITCHED CAPACITANCE CIRCUIT EMPLOYING SWITCHED OPERATIONAL AMPLIFIERS WITH MAXIMIZED VOLTAGE SWING
5745029	US	Granted		08624126	MOS TRANSISTOR SWITCHING CIRCUIT WITHOUT BODY EFFECT
5821791	EP US	Abandoned Granted		85330122.8 08730899	LOW-CONSUMPTION AND HIGH-DENSITY D FLIP-FLOP CIRCUIT IMPLEMENTATION PARTICULARLY FOR STANDARD CELL LIBRARIES
5990748	US	Granted		09129288	FREQUENCY SELF-COMPENSATED OPERATIONAL AMPLIFIER
5834978	US GB FR	Granted Granted Granted	0777318 0777318	08758024 95630487.4 95630487.4	
5825250	US	Granted		08757384	OPERATIONAL AMPLIFIER HAVING AN ADJUSTABLE FREQUENCY COMPENSATION
5955922	GB US	Granted Granted	0777319	05830500.6 08986274	TWO-STAGE FULLY DIFFERENTIAL OPERATIONAL AMPLIFIER WITH EFFICIENT COMMON MODE FEED-BACK CIRCUIT
5973837	GB US	Granted Granted	0840442	98830555.7 08948985	COMMON MODE CONTROL CIRCUIT FOR A SWITCHABLE FULLY DIFFERENTIAL OP-AMP
6140851	GB US	Granted Granted	0536274	85830523.6 09162719	ZEROES PLACEMENT IN A DOUBLE SAMPLED SWITCHED CAPACITOR LOW PASS MULTIRATE FILTER OF A DELTA SIGMA D/A CONVERTER
5184811	IT GB US	Abandoned Granted Granted	0903862 0903862	97830458.2 97830458.2	DOUBLE SAMPLED DELTA SIGMA MODULATOR OF SECOND ORDER HAVING A SEMI-BILINEAR ARCHITECTURE
5100758	JP IT GB FR US	Publication Abandoned Granted Granted Granted	0801233 0901233 0901233	19980262850 97830440.0 97830440.0 97830440.0	A LOW VOLTAGE LOW NOISE DIFFERENTIAL FEEDBACK AMPLIFIER

## Exhibit A

US Patent	Country	Current Status	Non-US Patent	Filing Number	Title
6529088	US	Granted		08293558	LOW-AREA RECONSTRUCTION FILTER FOR CURRENT STEERING DACS
6400001	US	Granted		09493842	ACCUMULATION TO DEEP-DEPLETION VARACTOR
6388505	US	Granted		09473689	INTEGRATED CIRCUIT GENERATING AT LEAST A VOLTAGE LINEAR RAMP HAVING A LOW RAISE
6075366	FR	Granted	1017172	88830792.2	CURRENT MIRROR CIRCUIT WITH HIGH OUTPUT IMPEDANCE
	US	Granted		08400774	
6288866	IT	Granted	1302276	M198A02076	BICMOS/CMOS LOW DROP VOLTAGE REGULATOR
	US	Granted		08568762	
	IT	Abandoned	1061428	99830374.7	
	GB	Abandoned	1061428	99830374.7	
	FR	Abandoned	1061428	99830374.7	
6774731	US	Granted		10244113	METHOD AND CIRCUIT FOR MINIMIZING GLITCHES IN PHASE-LOCKED LOOPS
6593917	US	Granted		09553812	
	IT	Abandoned	1047196	99830234.3	
	GB	Granted	1047196	99830234.3	
6486800	US	Granted		08901727	A METHOD FOR TESTING AN INTEGRATED CIRCUIT
	JP	Publication		20000271802	
	IT	Granted	1085333	99830581.7	
	DE	Granted	1085333	99830581.7	
6681195	US	Granted		08766602	METHOD FOR TESTING A CMOS INTEGRATED CIRCUIT
	JP	Publication		20010010262	
	IT	Granted	1118867	00830021.2	
	GB	Granted	1118867	00830021.2	
	FR	Granted	1118867	00830021.2	
6292490	US	Granted		09650022	HIGH-PRECISION BIASING CIRCUIT FOR A CASCODED CMOS STAGE, PARTICULARLY FOR LOW NOISE AMPLIFIERS
	IT	Abandoned	1081573	99830542.9	
	GB	Granted	1081573	99830542.9	
6271995	US	Granted		09581101	MINIMUM-NOISE, HIGH PRECISION, CURRENT-MODE BIAS SCHEME FOR A BIPOLAR TRANSISTOR WITH EMITTER DEGENERATION
	IT	Abandoned	1313384	VA98A00010	
4871927	US	Granted		07169267	
4920325	US	Granted		07180643	LATCH-UP PREVENTION IN A TWO-POWER-SUPPLY CMOS INTEGRATED CIRCUIT BY MEANS OF A SINGLE INTEGRATED MOS TRANSISTOR
	US	Granted		07180643	INTEGRATED ACTIVE ELECTRONIC SWITCHED CAPACITOR FILTER HAVING EXTREMELY LOW SENSITIVITY TO VARIATIONS OF THE COMPONENTS
6287108	US	Granted		07187607	CIRCUIT FOR PCM CONVERSION OF AN ANALOG SIGNAL, WITH IMPROVEMENT IN GAIN-TRACKING
4829286	US	Granted		07206018	CMOS POWER OPERATIONAL AMPLIFIER
4683993	US	Granted		07279036	ANTIBOUNCE CIRCUIT FOR DIGITAL CIRCUITS



## Exhibit A

US Patent	Country	Current Status	Non-US Patent	Filing Number	Title
RE35484	US	Granted		08238973	HIGH PRECISION LOW-PASS FILTER
	JP	Granted	2793856	18590324599	
4865468	US	Granted		07417588	HIGH RESOLUTION, FULLY DIFFERENTIAL CMOS COMPARATOR
RE35379	US	Granted		08189578	COMPLETELY DIFFERENTIAL FILTER WITH SWITCHED CONDENSERS USING CMOS OPERATIONAL AMPLIFIERS WITH NO COMMON MODE FEED-BACK
	GB	Abandoned	0375017	89203109.7	
	FR	Abandoned	0375017	89203109.7	
5130886	US	Granted		07662243	BALANCED MICROPHONE PREAMPLIFIER IN CMOS TECHNOLOGY
5170133	US	Granted		07669896	LOW-NOISE AMPLIFIER WITH HIGH INPUT IMPEDANCE, PARTICULARLY FOR MICROPHONES
	IT	Granted	1238899	19900019789	
6970125	US	Granted		10784133	MULTISTAGE ANALOG-TO-DIGITAL CONVERTER
	FR	Granted	1441445	03425033.2	
	IT	Granted	1441445	03425033.2	
7142026	US	Granted		10787921	PHASE DIFFERENCE DETECTOR, PARTICULARLY FOR A PLL CIRCUIT
	IT	Filing		M100500465	
6960947	US	Granted		10501502	PHASE-ERROR-COMPENSATION TECHNIQUES IN A FRACTIONAL-N PLL FREQUENCY SYNTHESIZER
	IT	Granted	1458100	03425155.3	
	GB	Granted	1458100	03425155.3	
7005928	US	Granted		10798244	PHASE-LOCKED LOOP CIRCUIT WITH SWITCHED-CAPACITOR CONDITIONING OF THE CONTROL CURRENT
	IT	Granted	1458099	03425155.1	
	GB	Granted	1458099	03425155.1	

## Exhibit A (continued)

Country	Current Status	US Publication	Non-US Patent	Filing Number	Title
US	Filing			11589930	CIRCUIT FOR RECONSTRUCTING AN ANALOG SIGNAL FROM A DIGITAL SIGNAL AND TRANSMISSION SYSTEM, PARTICULARLY FOR WCDMA CELLULAR TELEPHONY, INCLUDING SUCH CIRCUIT
EP	Publication		1761963	06750127.2	
CN	Publication			20088018808	
US	Publication	20070273448		11747639	OUTPUT POWER CONTROL OF AN RF AMPLIFIER
EP	Publication			06425321.4	
IT	Filing			MI07A001399	SIGNAL POWER DETECTOR
US	Publication			11197241	METHOD FOR RECEIVING AN OFDM SIGNAL
EP	Granted	EP1624639		04425612.1	
US	Publication	US20070047473		11458860	PCM TYPE INTERFACE
JP	Publication			20080180249	
EP	Publication			06201075.9	
US	Publication	20050207598		11088600	PACKAGED DIGITAL MICROPHONE DEVICE WITH AUXILIARY LINE-IN FUNCTION
EP	Publication			04426098.3	
US	Allowance	20080267823		11420582	METHOD OF ADDING A DITHER SIGNAL IN OUTPUT TO THE LAST INTEGRATOR OF A SIGMA-DELTA CONVERTER AND RELATIVE SIGMA-DELTA CONVERTER
EP	Publication			06423374.5	
US	Publication	20060182498		11361335	CIRCUIT APPARATUS WITH LED DIODES
EP	Publication			06426086.7	
US	Publication	20060180937		11361290	SUPPLY DEVICE OF CIRCUIT BRANCHES WITH LED DIODES
EP	Publication			06425085.0	
US	Publication			11538589	NOTCH FILTER AND APPARATUS FOR RECEIVING AND TRANSMITTING RADIO FREQUENCY SIGNALS INCORPORATING SAME
EP	Publication			06425718.9	
US	Publication	20060214630		11378082	CONTROLLED SWITCHING CIRCUIT
IT	Filing			VA09A000018	
US	Filing				ANALOG DIGITAL CONVERTER
EP	Filing			06425870.5	
EP	Abandoned			06425811.0	CLASS-D AUDIO AMPLIFIER
EP	Filing			07103177.7	IMPROVED VOLTAGE REGULATOR WITH LEAKAGE CURRENT COMPENSATION
WO	Filing			IT0600629	DIFFERENTIAL TO SINGLE-ENDED CONVERSION CIRCUIT AND COMPARTOR EMPLOYING THE CIRCUIT
WO	Filing			IT0600629	BUFFER DEVICE FOR SWITCHED-CAPACITANCE CIRCUIT
US	Filing				ELECTRIC CIRCUIT PROVIDED WITH OVERVOLTAGE PROTECTION
IT	Filing			MI08A002237	
US	Filing				CALIBRATION CIRCUIT FOR CALIBRATING AN ADJUSTABLE CAPACITANCE OF AN INTEGRATED CIRCUIT HAVING A TIME CONSTANT DEPENDING ON SAID CAPACITANCE
EP	Filing			07426100.0	
US	Filing				HIGH PRECISION CALIBRATION CIRCUIT FOR CALIBRATING AN ADJUSTABLE CAPACITANCE OF AN INTEGRATED CIRCUIT HAVING A TIME CONSTANT DEPENDING ON SAID CAPACITANCE
EP	Filing			07425009.1	
US	Filing			11782249	A DEVICE FOR DETECTING THE PEAK VALUE OF A SIGNAL
IT	Filing			TO08A000515	
US	Filing			11779498	A DEVICE FOR COMPARING THE PEAK VALUE OF AT LEAST ONE VOLTAGE SIGNAL WITH A REFERENCE VOLTAGE
EP	Filing			06425528.0	
IT	Filing			MI07A00152	CIRCUIT FOR MATCHING THE LOAD IMPEDANCE OF AN ELECTRONIC DEVICE

PATENT

REEL: 025779 FRAME: 0450

## Exhibit A (continued)

Country	Current Status	US Publication	Non-US Patent	Filing Number	Title
EP	Filing			07425478.0	CURRENT STEERING DIGITAL-ANALOG CONVERTER PARTICULARLY INSENSITIVE TO PACKAGING STRESSES
IT	Filing			MI03A000183	AMPLIFYING DEVICE AND SYSTEM USING THE DEVICE
US	Allowance	2004207474		10801452	PHASE-LOCKED LOOP CIRCUIT WITH CURRENT-PULSE INJECTION FOR IMPROVING LINEARITY
IT	Filing			MI03A00484	
US	Publication	2004022357B		10801503	FRACTIONAL-TYPE PHASE-LOCKED LOOP CIRCUIT WITH COMPENSATION OF PHASE ERRORS
IT	Granted		1458101	03425157.9	
GB	Granted		1458101	03425157.9	

## EXHIBIT A (continued)

Invention Disclosure Title
HIGH SPEED BUFFER FOR AD CONVERTERS
ALU CON DITHER IN UN DSP DEDICATO AL PROCESSAMENTO DI SEGNALI AUDIO
LOW INPUT CAPACITANCE, FAST, LOW NOISE, HIGH INPUT IMPEDANCE ADC
HIGH BAND 2 STAGES OPERATIONAL TRANSCONDUCTANCE AMPLIFIER
DC-DC DOWN CONVERTER WITH INPUT CURRENT REGULATION USING ONLY OUTPUT SENSE RESISTOR