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03-09)  
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U.S. PTO  
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02/07/2011



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Koujirou SHIBUYA

Additional names(s) of conveying party(ies)  Yes  No

Nature of conveyance/Execution Date(s):

Execution Date(s): December 15, 2010

- Assignment  Merger
- Security Agreement  Change of Name
- Joint Research Agreement
- Government Interest Assignment
- Executive Order 9424, Confirmatory License
- Other

2. Name and address of receiving party(ies):

Name: RENESAS Electronics Corporation

Address: 1753, Shimonumabe, Nakahara-ku

City: Kawasaki-shi

State/Prov.: Kanagawa

Country: JAPAN

ZIP:

Additional name(s) & address(es) attached?  Yes  No

Application or patent numbers(s):

A. Patent Application No. (s)

This document is being filed together with a new application

B. Patent No.(s)

Additional numbers attached?  Yes  No

i. Name and address to whom correspondence concerning document should be mailed:

Name: Sean M. McGinn, Esq.

Registration No.: 34,386

Address: McGinn Intellectual Property Law Group, PLLC

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State/Prov.: Virginia

Country: USA

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Fax Number: (703) 761-2375 or 76

Email Address:

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00

- Authorized to be charged by credit card
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- None required (government interest not affecting title)

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Authorized User Name Sean M. McGinn, Esq.

9. Signature:

Signature

February 7, 2011

Date

Sean M. McGinn, Esq.

Name of Person Signing

Total number of pages including cover sheet, attachments, and document.

Documents to be recorded (including cover sheet) should be faxed to (571)273-0140, or mailed to: Mail Stop Assignment Recordation Services, Director of the USPTO, P.O. Box 1450, Alexandria, VA 22313-1450

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REEL: 025784 FRAME: 0032

# ASSIGNMENT

( 譲渡証 )

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by Renesas Electronics Corporation, a corporation organized under the laws of Japan, located at 1753, Shimonumabe, Nakahara-ku, Kawasaki-shi, Kanagawa, Japan, receipt of which is hereby acknowledged I do hereby sell and assign to said Renesas Electronics Corporation, its successors and assigns, all my right, title and interest, in and for the United States of America, in and to

**SEMICONDUCTOR DEVICE, SEMICONDUCTOR PACKAGE, INTERPOSER, SEMICONDUCTOR DEVICE MANUFACTURING METHOD AND INTERPOSER MANUFACTURING METHOD**

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said Renesas Electronics Corporation, its successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Renesas Electronics Corporation.

Signed on the date(s) indicated aside our signatures:

INVENTOR(S)  
(発明者フルネームサイン)

Date Signed  
(署名日)

1) Koujiro Shibuya Koujiro SHIBUYA

December 15, 2010

2) \_\_\_\_\_

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3) \_\_\_\_\_

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9) \_\_\_\_\_

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10) \_\_\_\_\_

**PATENT**