

**PATENT ASSIGNMENT**

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

**CONVEYING PARTY DATA**

Name	Execution Date
Po-Jui Liao	09/28/2010
Tsung-Lung Tsai	02/21/2011
Chien-Ting Lin	02/21/2011
Shao-Hua Hsu	02/21/2011
Shui-Yen Lu	02/21/2011
Pei-Yu Chou	02/21/2011
Shin-Chi Chen	02/21/2011
Jiunn-Hsiung Liao	02/21/2011
Shang-Yuan Tsai	02/21/2011
Chan-Lon Yang	02/21/2011
Teng-Chun Tsai	02/21/2011
Chun-Hsien Lin	02/21/2011

**RECEIVING PARTY DATA**

Name:	UNITED MICROELECTRONICS CORP.
Street Address:	No.3, Li-Hsin Road 2, Science-Based Industrial Park
City:	Hsin-Chu City
State/Country:	TAIWAN

**PROPERTY NUMBERS Total: 1**

Property Type	Number
Application Number:	13033616

**CORRESPONDENCE DATA**

Fax Number: (703)997-4517  
*Correspondence will be sent via US Mail when the fax attempt is unsuccessful.*  
 Phone: 3027291562  
 Email: Patent.admin.uspto.cr@naipo.com  
 Correspondent Name: WINSTON HSU

CH \$40.00 13033616

Address Line 1: P.O.BOX 506  
Address Line 4: Merrifield, VIRGINIA 22116

ATTORNEY DOCKET NUMBER:	NAUP1247USA
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NAME OF SUBMITTER:	JANINE CHANG
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Total Attachments: 6 source=1059376#page1.tif source=1059376#page2.tif source=1059376#page3.tif source=1059376#page4.tif source=1059376#page5.tif source=1059376#page6.tif
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**ASSIGNMENT OF INVENTION**

In consideration of the payment by ASSIGNEE to ASSIGNOR of the sum of One Dollar (\$1.00), the receipt of which is hereby acknowledged, and for other good and valuable consideration.

**ASSIGNOR(S) (Inventors):**

Name: Po-Jui Liao Nationality: TW

Name: Tsung-Lung Tsai Nationality: TW

Name: Chien-Ting Lin Nationality: TW

Name: Shao-Hua Hsu Nationality: TW

Name: Shui-Yen Lu Nationality: TW

Name: Pei-Yu Chou Nationality: TW

Name: Shin-Chi Chen Nationality: TW

Name: Jiunn-Hsiung Liao Nationality: TW

Name: Shang-Yuan Tsai Nationality: TW

Name: Chan-Lon Yang Nationality: TW

Name: Teng-Chun Tsai Nationality: TW

Name: Chun-Hsien Lin Nationality: TW

Hereby sells, assigns and transfers to

**ASSIGNEE(S):**

Name: UNITED MICROELECTRONICS CORP.

Address: No.3, Li-Hsin Road 2, Science-Based Industrial Park, Hsin-Chu City 300, Taiwan, R.O.C.

and the successors and assignees of the ASSIGNEE the entire right, title, and interest in and to any and all improvements which are disclosed in the invention entitled:

**"Method of Manufacturing Semiconductor Device Having Metal Gate"**

Which is found in :

- (a) + U.S. patent application executed on even date
- (b) \_\_\_\_\_ U.S. patent application executed on \_\_\_\_\_
- (c) \_\_\_\_\_ U.S. application serial no. \_\_\_\_\_
- (d) \_\_\_\_\_ patent no. \_\_\_\_\_ issued \_\_\_\_\_
- (e) \_\_\_\_\_ International application no. \_\_\_\_\_

PATENT

and, in and to, all Letters Patent to be obtained for said invention by the above application or any continuations, continuation-in-part, divisions, renewals, substitutes, or extensions thereof, and as to Letters Patent any reissue or re-examination thereof

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would conflict with this assignment;

ASSIGNOR further covenants that ASSIGNEE will, upon its request, be provided promptly with all pertinent facts and documents relating to said invention and said Letters Patent and legal equivalents as may be known and accessible to ASSIGNOR and will testify as to the same in any interference, litigation or proceeding related thereto and will promptly execute and deliver to ASSIGNEE or its legal representatives any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce said application, said invention and said Letters Patent and said equivalents thereof which may be necessary or desirable to carry out the proposes thereof.

IN WITNESS WHEREOF, We have hereunto set hand and seal this 09/28/2010 (Date of signing). (請發明人務必簽署上列日期。)

(Type name of inventor)

Signature of INVENTOR

Po-Jui Liao

Po-Jui Liao 廖柏瑞 SEP 28 2010

Tsung-Lung Tsai

Tsung-Lung Tsai FEB 21 2011

Chien-Ting Lin

Shao-Hua Hsu FEB 21 2011

Shao-Hua Hsu

Shui-Yen Lu

Shui-Yen Lu FEB 21 2011

Pei-Yu Chou

Pei Yu Chou FEB 21 2011

Shin-Chi Chen

Shin Chi Chen FEB 21 2011

Jiunn-Hsiung Liao

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Shang-Yuan Tsai

Shang-Yuan Tsai FEB 21 2011

Chan-Lon Yang

Chan-Lon Yang FEB 21 2011

Teng-Chun Tsai

Teng-Chun Tsai FEB 21 2011

Chun-Hsien Lin

Chun-Hsien Lin FEB 21 2011

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"Method of Manufacturing Semiconductor Device Having Metal Gate"

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- (a) + U.S. patent application executed on even date
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- (d) \_\_\_\_\_ patent no. \_\_\_\_\_ issued \_\_\_\_\_
- (e) \_\_\_\_\_ International application no. \_\_\_\_\_

PATENT

and, in and to, all Letters Patent to be obtained for said invention by the above application or any continuations, continuation-in-part, divisions, renewals, substitutes, or extensions thereof, and as to Letters Patent any reissue or re-examination thereof

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IN WITNESS WHEREOF, We have hereunto set hand and seal this FEB 21 2011 (Date of signing). (請發明人務必簽名上列日期。)

(Type name of inventor)

Signature of INVENTOR

Po-Jui Liao

Tsung-Lung Tsai

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*Chien-Ting Lin*  
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