

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SanDisk Corporation	04/04/2011
RECEIVING PARTY DATA	
Name:	SanDisk Technologies Inc.
Street Address:	6900 North Dallas Parkway
Internal Address:	Two Legacy Town Center
City:	Plano
State/Country:	TEXAS
Postal Code:	75024
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	5751635
CORRESPONDENCE DATA	
Fax Number:	(408)801-9019
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	4088012863
Email:	simona.benjamin@sandisk.com
Correspondent Name:	SanDisk Corporation
Address Line 1:	601 Mccarthy Boulevard
Address Line 2:	Patent Department/Simona Benjamin
Address Line 4:	Milpitas, CALIFORNIA 95035
ATTORNEY DOCKET NUMBER:	INV0175.003US
NAME OF SUBMITTER:	Simona Benjamin
Total Attachments: 5 source=page 1#page1.tif source=page 1#page2.tif source=page 1#page3.tif source=page 1#page4.tif source=page 1#page5.tif	

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PATENT
REEL: 026223 FRAME: 0820

ASSIGNMENT

WHEREAS, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

WHEREAS, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

ASSIGNOR further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4th April 2011

Judy Bruner
Name: Judy Bruner
Title: EVP and CFO

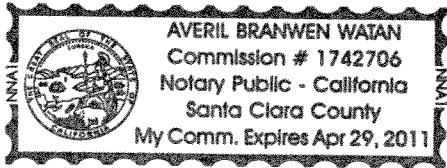
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4th day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: Averil Branwen Watan
Signature of Notary Public

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: 4/4/11

James F. Brelsford
Name: James F. Brelsford
Title: President and Secretary

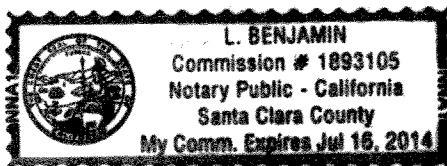
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: L. Benjamin
Signature of Notary Public

Exhibit A

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
IBM0001.000US	Wilbur D. Pricer	Endurance Management For Solid State Files	US	Issued	7635910	28-Dec-90	5222109	22-Jun-93
INV0169.001US	Carl W. Werner, Andreas M. Haeberli, Leon Sea Jiunn Wong, Cheng-Yuan Michael Wang, Hock C So, Sau C. Wong	Integrated Circuit with Multilevel and Analog Storage Cells and User Selectable Sampling Frequency	US	Issued	9343117	29-Jun-99	6549456	15-Apr-03
INV0169.002US	Carl W. Werner, Andreas M. Haeberli, Leon Sea Jiunn Wong, Cheng-Yuan Michael Wang, Sau C. Wong, Hock C So	Integrated circuit with analog or multilevel storage cells and user-selectable sampling frequency	US	Issued	10359476	5-Feb-03	7106632	12-Sep-06
INV0169.003US	Carl W. Werner, Andreas M. Haeberli, Leon Sea Jiunn Wong, Cheng-Yuan Michael Wang, Hock C So, Sau C. Wong	Integrated circuit with analog or multilevel storage cells and user selectable sampling frequency	US	Issued	11460325	27-Jul-06	7298670	20-Nov-07
INV0169.004US	Carl W. Werner, Andreas M. Haeberli, Leon Sea Jiunn Wong, Cheng-Yuan Michael Wang, Hock C So, Sau C. Wong	Integrated circuit with analog or multilevel storage cells and user selectable sampling frequency	US	Issued	11876093	22-Oct-07	7554844	30-Jun-09
INV0170.000US	James Sansbury, Sau C. Wong	Programmable Impedance Device	US	Issued	9159848	25-Sep-98	6201734	13-Mar-01
INV0170.002US	James Sansbury, Sau C. Wong	Programmable Impedance Device	US	Issued	9967513	27-Sep-01	6456529	24-Sep-02
INV0173.000US	Andreas M. Haeberli, Sau C. Wong, Carl W. Werner, Cheng-Yuan Michael Wang, Leon Sea Jiunn Wong, Hock C So	Charge Pump Circuit Adjustable In Response To An External Voltage Source	US	Issued	9343206	29-Jun-99	6370075	9-Apr-02
INV0173.001US	Sau C. Wong, Carl W. Werner, Leon Sea Jiunn Wong, Andreas M. Haeberli, Cheng- Yuan Michael Wang, Hock C So	Adjustable Circuits For Analog Or Multi-Level Memory	US	Issued	10109353	27-Mar-02	6556465	29-Apr-03
INV0173.002US	Andreas M. Haeberli, Sau C. Wong, Carl W. Werner, Cheng-Yuan Michael Wang, Leon Sea Jiunn Wong, Hock C So	Adjustable Charge Pump Circuit Adjustable In Response To An External Voltage Source	US	Issued	10382333	4-Mar-03	6760262	6-Jul-04
INV0175.000US	Sau C. Wong, Hock C So	High Resolution Analog Storage EPROM and Flash EPROM	US	Issued	8333381	2-Nov-94	5694356	2-Dec-97
INV0175.002US	Sau C. Wong, Hock C So	High Resolution Analog Storage EPROM and Flash EPROM	US	Issued	8585138	11-Jan-96	5638320	10-Jun-97
INV0175.003US	Sau C. Wong, Hock C So	Read Circuits For Analog Memory Cells	US	Issued	8585072	11-Jan-96	5751635	12-May-98
INV0176.000US	Sau C. Wong, Hock C So	Multi-Bit-Per-Cell Non-Volatile Memory With Error Detection and Correction	US	Issued	8924909	8-Sep-97	5909449	1-Jun-99
INV0176.001US	Sau C. Wong, Hock C So	Multi-Bit-Per-Cell Flash EEPROM Memory With Refresh	US	Issued	9200220	25-Nov-98	6151246	21-Nov-00
INV0176.004US	Hock C So, Sau C. Wong	Multi-Bit-Per-Cell Flash EEPROM Memory with Refresh	US	Issued	11101938	7-Apr-05	7170781	30-Jan-07
INV0176.005US	Hock C So, Sau C. Wong	Multi-Bit-Per-Cell Flash EEPROM Memory with Refresh	US	Issued	11620127	5-Jan-07	7397697	8-Jul-08
INV0177.000US	Sau C. Wong, Hock C So	Non-Volatile Memory With Analog and Digital Interface and Storage	US	Issued	8535441	28-Sep-95	5745409	28-Apr-98
INV0177.001US	Sau C. Wong, Hock C So	Testing Of An Analog Memory Using An On-Chip Digital Input/Output Interface	US	Issued	8847236	1-May-97	5801980	1-Sep-98
INV0177.002US	Sau C. Wong, Hock C So	Combined Digital Write and Analog Rewrite Process for Non-Volatile Memory	US	Issued	8847234	30-Apr-97	5815425	29-Sep-98
INV0178.000US	Sau C. Wong, Hock C So	Pipelined Record And Playback For Analog Non- Volatile Memory	US	Issued	8587310	16-Jan-96	5680341	21-Oct-97