

## PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SanDisk Corporation	04/04/2011
RECEIVING PARTY DATA	
Name:	SanDisk Technologies Inc.
Street Address:	6900 North Dallas Parkway
Internal Address:	Two Legacy Town Center
City:	Plano
State/Country:	TEXAS
Postal Code:	75024
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	5748533
CORRESPONDENCE DATA	
Fax Number:	(408)801-9019
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	4088012863
Email:	simona.benjamin@sandisk.com
Correspondent Name:	SanDisk Corporation
Address Line 1:	601 Mccarthy Boulevard
Address Line 2:	Patent Department/Simona Benjamin
Address Line 4:	Milpitas, CALIFORNIA 95035
ATTORNEY DOCKET NUMBER:	INV0181.000US
NAME OF SUBMITTER:	Simona Benjamin
Total Attachments: 5 source=page 2#page1.tif source=page 2#page2.tif source=page 2#page3.tif source=page 2#page4.tif source=page 2#page5.tif	

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PATENT  
REEL: 026224 FRAME: 0071

## ASSIGNMENT

**WHEREAS**, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

**WHEREAS**, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

**NOW, THEREFORE**, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

**ASSIGNOR** hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

**ASSIGNOR** further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

**SANDISK CORPORATION**

Dated: 4<sup>th</sup> April 2011

Judy Bruner  
Name: Judy Bruner  
Title: EVP and CFO

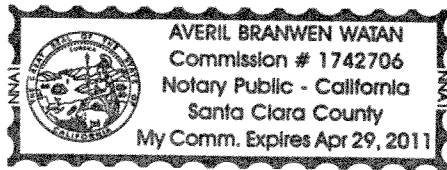
STATE OF California )  
COUNTY OF Santa Clara ) ss.

On this 4<sup>th</sup> day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: Averil Branwen Watan  
Signature of Notary Public

**We accept the Assignment**

**SANDISK TECHNOLOGIES INC.**

Dated: 4/4/11

James F. Brelsford  
Name: James F. Brelsford  
Title: President and Secretary

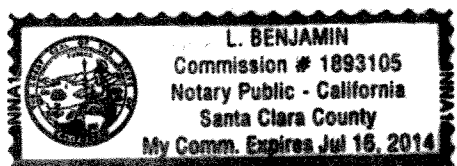
STATE OF California )  
COUNTY OF Santa Clara ) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: L. Benjamin  
Signature of Notary Public

## Exhibit A

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
INV0179.000US	Sau C. Wong, Hock C So	Digital Testing of Analog Memory Devices	US	Issued	8598485	8-Feb-96	5682352	28-Oct-97
INV0180.000US	Frank M. Dunlap, Sau C. Wong, Hock C So	Feedback Loop For Reading Threshold Voltage	US	Issued	8622333	26-Mar-96	5748534	5-May-98
INV0181.000US	Frank M. Dunlap, Sau C. Wong, Hock C So	Read Circuit Which Uses A Coarse-To-Fine Search When Reading The Threshold Voltage Of A Memory Cell	US	Issued	8622332	26-Mar-96	5748533	5-May-98
INV0182.000US	Hock C So, Sau C. Wong	Analog And Multi-Level Memory With Reduced Program Disturb	US	Issued	8684788	22-Jul-96	5818757	6-Oct-98
INV0183.000US	Hock C So, Sau C. Wong	Source Biasing In Non-Volatile Memory Having Row-Based Sectors	US	Issued	8781741	10-Jan-97	5923585	13-Jul-99
INV0184.000US	Sau C. Wong, Hock C So	Look-Ahead Erase For Sequential Data Storage	US	Issued	8839288	16-Apr-97	5949716	7-Sep-99
INV0185.000US	Hock C So, Sau C. Wong	Memory Architecture For Recording of Multiple Messages	US	Issued	8867473	2-Jun-97	5909387	1-Jun-99
INV0186.000US	Hock C So, Sau C. Wong	Multiple Array Architecture For Analog Or Multi-Bit-Cell Memory	US	Issued	8889111	7-Jul-97	5896340	20-Apr-99
INV0187.000US	Sau C. Wong, Hock C So	Address Scrambling In A Semiconductor Memory	US	Issued	8986210	5-Dec-97	5943283	24-Aug-99
INV0188.000US	Jing-Lu Gu	Multiple Function, Bi-Directional Input/Output Interface For Sound Processing System	US	Issued	8936559	24-Sep-97	6968065	22-Nov-05
INV0189.000US	Sau C. Wong, Cheng-Yuan Michael Wang, Roger Ying Kuen Lo, Hock C So	Flash Memory Permitting Simultaneous Read/Write and Erase Operations in a Single Memory Array	US	Issued	9199971	25-Nov-98	6345000	5-Feb-02
INV0190.000US	Sau C. Wong	High Resolution Multi-Bit-Per-Cell Memory	US	Issued	9053716	1-Apr-98	6038166	14-Mar-00
INV0191.000US	Sau C. Wong	Non-Linear Mapping Of Threshold Voltages For Analog/Multi-Level Memory	US	Issued	9200205	25-Nov-98	6154157	28-Nov-00
INV0192.000US	Sau C. Wong, Leo Petropoulos, John H. Chan	Analog Multi-Level Memory For Digital Imaging	US	Issued	9086785	28-May-98	6594036	15-Jul-03
INV0193.000US	Sau C. Wong, Hock C So	High-Bandwidth Read and Write Architecture For Non-Volatile Memories	US	Issued	9103623	23-Jun-98	5969986	19-Oct-99
INV0193.001US	Sau C. Wong	High-Bandwidth Read and Write Architecture For Non-Volatile Memories	US	Issued	9128225	3-Aug-98	6134145	17-Oct-00
INV0193.003US	Sau C. Wong	High Data Rate Write Process For Non-Volatile Flash Memories	US	Issued	10042504	18-Oct-01	6606267	12-Aug-03
INV0193.005US	Sau C. Wong	High Data Rate Write Process For Non-Volatile Flash Memories	US	Issued	11184322	18-Jul-05	7349255	25-Mar-08
INV0194.000US	Sau C. Wong	Programmable Arrays For Data Conversions Between Analog And Digital	US	Issued	9159704	23-Sep-98	6169503	2-Jan-01
INV0195.000US	Sau C. Wong, Leo Petropoulos	Analog Buffer Memory For High-Speed Digital Image Capture	US	Issued	9159397	23-Sep-98	7268809	11-Sep-07
INV0196.000US	Sau C. Wong	Data Encryption And Signal Scrambling Using Programmable Conversion Arrays	US	Issued	9200500	25-Nov-98	6498851	24-Dec-02
INV0196.001US	Sau C. Wong	Data Encryption And Signal Scrambling Using Programmable Data Conversion Arrays	US	Issued	10264970	4-Oct-02	6624773	23-Sep-03
INV0197.000US	Leo Petropoulos, Sau C. Wong	Correction Of Corrupted Elements In Sensors Using Analog/Multi-Level Non-Volatile Memory	US	Issued	9224168	31-Dec-98	6760068	6-Jul-04
INV0198.000US	Sau C. Wong	Dynamic Write Process For High Bandwidth Multi-Bit-Per-Cell And Analog/Multi-Level Non-Volatile Memories	US	Issued	9224656	31-Dec-98	6134141	17-Oct-00