

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SanDisk Corporation	04/04/2011
RECEIVING PARTY DATA	
Name:	SanDisk Technologies Inc.
Street Address:	6900 North Dallas Parkway
Internal Address:	Two Legacy Town Center
City:	Plano
State/Country:	TEXAS
Postal Code:	75024
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	7804371
CORRESPONDENCE DATA	
Fax Number:	(408)801-9019
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	4088012863
Email:	simona.benjamin@sandisk.com
Correspondent Name:	SanDisk Corporation
Address Line 1:	601 Mccarthy Boulevard
Address Line 2:	Patent Department/Simona Benjamin
Address Line 4:	Milpitas, CALIFORNIA 95035
ATTORNEY DOCKET NUMBER:	SDA-1091-US
NAME OF SUBMITTER:	Simona Benjamin
Total Attachments: 5 source=page 7#page1.tif source=page 7#page2.tif source=page 7#page3.tif source=page 7#page4.tif source=page 7#page5.tif	

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PATENT
REEL: 026227 FRAME: 0371

ASSIGNMENT

WHEREAS, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

WHEREAS, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

ASSIGNOR further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4th April 2011

Judy Bruner
Name: Judy Bruner
Title: EVP and CFO

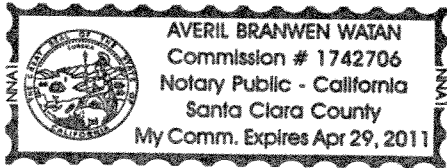
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4th day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: Averil Branwen Watan
Signature of Notary Public

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: 4/4/11

James F. Brelsford
Name: James F. Brelsford
Title: President and Secretary

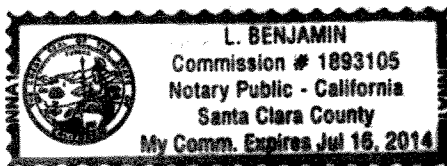
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: L. Benjamin
Signature of Notary Public

Exhibit A

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1034X-US	Deepak C. Sekar, Nima Mokhesi	Applying Adaptive Body Bias to Non-Volatile Storage	US	Issued	11618791	30-Dec-06	7468920	23-Dec-08
SDA-1034Y-US	Deepak C. Sekar, Nima Mokhesi	Non-Volatile Storage with Adaptive Body Bias	US	Issued	11618793	30-Dec-06	7525843	28-Apr-09
SDA-1035X-US	Feng Pan	Method for Reducing Wordline Recovery Time	US	Issued	11615868	22-Dec-06	7443735	28-Oct-08
SDA-1035Y-US	Feng Pan	System for Reducing Wordline Recovery Time	US	Issued	11615873	22-Dec-06	7495992	24-Feb-09
SDA-1038Y-US	Feng Pan, Trung Pham	Apparatus for Controlling Bitline Biasing Voltage	US	Issued	11617531	28-Dec-06	7529135	5-May-09
SDA-1043X-US	Warren Middlekauf, Robert C Miller	Method for Producing Portable Memory	US	Allowed	11551402	20-Oct-06		
SDA-1043Y-US	Warren Middlekauf, Robert C Miller	Portable Memory Devices	US	Allowed	11551423	20-Oct-06		
SDA-1045X-US	Steven T. Sprouse, Patricio Collantes, Dhaval Parikh	Method of Making an Electrical Connector with ESD Grounding Clip	US	Issued	11618292	29-Dec-06	7810235	12-Oct-10
SDA-1045Y-US	Steven T. Sprouse, Dhaval Parikh, Patricio Collantes	Electrical Connector with ESD Grounding Clip	US	Issued	11618280	29-Dec-06	7410370	12-Aug-08
SDA-1049X-US	Farookh Moogat, Teruhiko Kamei	Method of NAND Flash Memory Cell Array with Adaptive Memory State Partitioning	US	Issued	11618482	29-Dec-06	7489547	10-Feb-09
SDA-1049Y-US	Farookh Moogat, Teruhiko Kamei	NAND Flash Memory Cell Array with Adaptive Memory State Partitioning	US	Issued	11618498	29-Dec-06	7489548	10-Feb-09
SDA-1051X-US	Yingda Dong, Jeffrey Lutze	Reduced Program Disturb in Non-Volatile Storage using Early Source-side Boosting	US	Issued	11609804	12-Dec-06	7623386	24-Nov-09
SDA-1051Y-US	Yingda Dong, Jeffrey Lutze	Non-Volatile Storage with Early Source-side Boosting for Reducing Program Disturb	US	Issued	11609813	12-Dec-06	7623387	24-Nov-09
SDA-1076-1Y-US	Paul Paternoster, Po-Shen Lai, Paul Lassa, Yongliang Bill Wang	Selectively Powering Data Interfaces	US	Published	11686350	14-Mar-07		
SDA-1081-a-US	Mike Morganstern, Derek Niizawa, Jeffrey A Salazar, Junggi Sung	Flash Drive Design	US	Issued	29275796	5-Jan-07	D558763	1-Jan-08
SDA-1081-b-US	Mike Morganstern, Derek Niizawa, Jeffrey A Salazar, Junggi Sung, Mike Simmons, Arturo Meuniot, Michael Roberts	ELECTRONIC DEVICE WITH DUAL SLIDE ACTUATOR THAT EXTENDS AND RETRACTS A CONNECTOR	US	Issued	11943450	20-Nov-07	7674120	9-Mar-10
SDA-1081-US	Mike Morganstern, Derek Niizawa, Jeffrey A Salazar, Junggi Sung, Mike Simmons, Arturo Meuniot, Michael Roberts	ELECTRONIC DEVICE WITH DUAL SLIDE ACTUATOR THAT EXTENDS AND RETRACTS A CONNECTOR	US	Issued	11618436	29-Dec-06	7301596	27-Nov-07
SDA-1082-US	Mike Morganstern, Derek Niizawa, Jeffrey A Salazar, Junggi Sung, Mike Simmons, Arturo Meuniot, Michael Roberts, Jordan MacDonald	ELECTRONIC DEVICE WITH A RETRACTABLE CONNECTOR AND PROTECTIVE DOOR MECHANISM	US	Issued	11618447	29-Dec-06	7303411	4-Dec-07
SDA-1090-US	Darmin Jin, Brian Cheung, Steve Skala	Systems, Circuits, Chips and Methods with Protection at Power Island Boundaries	US	Published	11967382	31-Dec-07		
SDA-1091-US	Stanley Chapski, Darmin Jin, Brian Cheung	Systems, Modules, Chips, Circuits and Methods with Delay Trim Value Updates on Power-Up	US	Issued	11967467	31-Dec-07	7804371	28-Sep-10
SDA-1092-US	Yongliang Bill Wang, Daniel Nguyen	Systems, Methods, and Integrated Circuits with Inrush-Limited Power Islands	US	Allowed	11967580	31-Dec-07		
SDA-1094X-US	Baojing Liu, Radhakrishnan Nair, Paul Lassa	Method for Performing Full Transfer Automation in a USB Controller	US	Issued	11618865	31-Dec-06	7802034	21-Sep-10
SDA-1100-US	Brian Cheung, Emmanuel de Muizon	Integrated Circuits and Methods with Two Types of Decoupling Capacitors	US	Issued	11967778	31-Dec-07	7898013	9-Mar-11
SDA-1101X-US	Feng Pan	Generation of Analog Voltage Using Self-Biased Capacitive Feedback Stage	US	Issued	11618917	1-Jan-07	7626447	1-Dec-09