

**PATENT ASSIGNMENT**

Electronic Version v1.1  
 Stylesheet Version v1.1

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT

**CONVEYING PARTY DATA**

Name	Execution Date
SiGe Semiconductor Inc.	04/25/2011

**RECEIVING PARTY DATA**

<b>Name:</b>	Intellectual Ventures Holding 75 LLC
<b>Street Address:</b>	7251 W Lake Mead Blvd, Ste 300
<b>City:</b>	Las Vegas
<b>State/Country:</b>	NEVADA
<b>Postal Code:</b>	89128

**PROPERTY NUMBERS Total: 14**

Property Type	Number
Patent Number:	7187913
Patent Number:	7756500
Patent Number:	7196737
Application Number:	12711751
Application Number:	12711815
Patent Number:	6681181
Patent Number:	6947840
Patent Number:	6681103
Patent Number:	6873212
Patent Number:	6680655
Patent Number:	6995808
Patent Number:	7394505
Patent Number:	6917248
Patent Number:	6859028

**CORRESPONDENCE DATA**

**501523662**

**PATENT  
 REEL: 026239 FRAME: 0804**

**OP \$560.00 7187913**

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ATTORNEY DOCKET NUMBER:	32052.8000.0169
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NAME OF SUBMITTER:	Tracy Meeker
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Total Attachments: 5  
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source=Assignment to IV Holding 75#page4.tif  
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**REVISED EXHIBIT B-1**

**ASSIGNMENT OF PATENT RIGHTS**

For good and valuable consideration, the receipt of which is hereby acknowledged, SiGe Semiconductor Inc., a Canadian corporation, with an office at 100 – 1050 Morrison Dr., Ottawa, Ontario K2H 8K7 Canada (“*Assignor*”), does hereby sell, assign, transfer, and convey unto Intellectual Ventures Holding 75 LLC, a Nevada limited liability company with an address at 7251 W Lake Mead Blvd, Ste 300, Las Vegas, NV 89128 (“*Assignee*”), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the “*Patent Rights*”):

(a) the provisional patent applications, patent applications and patents listed in the table below (the “*Patents*”);

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
7,187,913	US	01/24/2003	Integrated circuit tuner with broad tuning range  Rahn, David
7,756,500	US	01/24/2003	Active inductor circuits for filtering in a cable tuner circuit  Fulga, Stefan
7,196,737	US	01/24/2003	Method of using control loops in a broadband cable tuner  Fulga, Stefan
12/711,751	US	02/24/2010	Active inductor circuits for filtering in a cable tuner circuit  Fulga, Stefan
12/711,815	US	02/24/2010	Active inductor circuits for filtering in a cable tuner circuit  Fulga, Stefan
6,681,181	US	05/20/2002	Gps receiver with improved immunity to burst transmissions  Fielder, Dennis Arthur
6,947,840	US	10/24/2003	Gps receiver with improved immunity to burst transmissions  Fielder, Dennis Arthur

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
CNZL03811603.0	CN	05/20/2003	Gps receiver with improved immunity to burst transmissions  Fielder, Dennis Arthur
DE60314870.0	DE	05/20/2003	Gps receiver with improved immunity to burst transmissions  Fielder, Dennis Arthur
GB1508054	GB	05/20/2003	Gps receiver with improved immunity to burst transmissions  Fielder, Dennis Arthur
IT1508054	IT	05/20/2003	Gps receiver with improved immunity to burst transmissions  Fielder, Dennis Arthur
6,681,103	US	08/25/2000	On-chip image reject filter  Rogers, John William Mitchell
DE60125379.5	DE	08/24/2001	A novel on-chip image reject filter  Rogers, John William Mitchell
6,873,212	US	05/17/2001	Integrated circuit radio frequency amplifier with notch filter  Rogers, John William Mitchell
6,680,655	US	08/01/2001	Automatic gain control for a voltage controlled oscillator  Rogers, John William Mitchell
6,995,808	US	01/24/2002	Television tuner  Kovacic, Stephen J.
7,394,505	US	02/19/2002	High linearity, low noise figure tuner front end circuit  Fulga, Stefan
6,917,248	US	07/18/2002	Broadband voltage controlled

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
			oscillator supporting improved phase noise  Nguyen, Glao M.
6,859,028	US	11/26/2002	Design-for-test modes for a phase locked loop  Toner, Michael F.
GB1312157	GB	8/24/2001	A novel on-chip image reject filter  Rogers, John William

(b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, (ii) for which any of the Patents directly or indirectly forms a basis for priority, and/or (iii) that were co-owned applications that incorporate by reference, or are incorporated by reference into, the Patents excluding Patents and Patent Applications numbered 6,559,021; 6,346,453; 6,756,604; 6,551,889; 6,433,611; CN200680024436.8; 6,518,840; GB1122883; 6,850,742; EP01810077.6; 10/096,899; 10/325,840; EP081810271.5; 10/349,955; 10/349,957; DE60120801.3; EP1122883; IE1122883; LU1122883; MC1122883;

(c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);

(d) all foreign patents, patent applications, and counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;

(e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents below and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;

(f) inventions, invention disclosures, and discoveries described in any of the Patents and/or any item in the foregoing categories (b) through (e) that (i) are included in any claim in the Patents and/or any item in the foregoing categories (b) through (e), (ii) are subject matter capable of being reduced to a patent claim in a reissue or reexamination proceeding brought on any of the Patents and/or any item in the foregoing categories (b) through (e), and/or (iii) could have been included as a claim in any of the Patents and/or any item in the foregoing categories (b) through (e);

(g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (f), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;

(h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

- (1) damages,
- (2) injunctive relief, and
- (3) any other remedies of any kind

for past, current, and future infringement; and

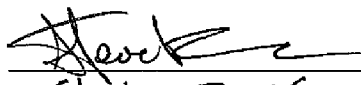
(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h).

The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Poway, CA  
on Apr. 25, 2011.

**ASSIGNOR:**

**SiGe Semiconductor Inc.**

By:   
Name: Stephen J. Kovacic  
Title: Director  
(Signature MUST be attested)

ATTESTATION OF SIGNATURE PURSUANT TO 28 U.S.C. § 1746

The undersigned witnessed the signature of Stephen J. Kovacic to the above Assignment of Patent Rights on behalf of SiGe Semiconductor Inc. and makes the following statements:

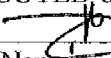
I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.

Stephen J. Kovacic is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me on April 25, 2011 to execute the above Assignment of Patent Rights on behalf of SiGe Semiconductor Inc.

Stephen J. Kovacic subscribed to the above Assignment of Patent Rights on behalf of SiGe Semiconductor Inc.

I declare under penalty of perjury under the laws of the United States of America that the statements made in the three (3) numbered paragraphs immediately above are true and correct.

EXECUTED on April 25, 2011 (date)

  
Print Name: TIMMY DO

