PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date	
SanDisk Corporation	04/04/2011	

RECEIVING PARTY DATA

Name:	SanDisk Technologies Inc.			
Street Address:	ernal Address: Two Legacy Town Center			
City:	Plano			
State/Country:	TEXAS			
Postal Code:	75024			

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	12482696

CORRESPONDENCE DATA

Fax Number: (408)801-9019

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 4088012863

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Correspondent Name: SanDisk Corporation

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Address Line 2: Patent Department/Simona Benjamin

Address Line 4: Milpitas, CALIFORNIA 95035

ATTORNEY DOCKET NUMBER: SDA-1109X-A-US

NAME OF SUBMITTER: Simona Benjamin

Total Attachments: 5

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PATENT REEL: 026255 FRAME: 0400 H \$40.00 12482

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ASSIGNMENT

WHEREAS, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as ASSIGNOR), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, THE ASSIGNED PATENT RIGHTS);

WHEREAS, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as ASSIGNEE), is desirous of acquiring the full and exclusive right, title and interest in THE ASSIGNED PATENT RIGHTS;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, ASSIGNOR hereby assigns, transfers and conveys unto the said ASSIGNEE, and ASSIGNEE accepts, all of ASSIGNOR's right, title and interest throughout the world in and to THE ASSIGNED PATENT RIGHTS and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for THE ASSIGNED PATENT RIGHTS, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and ASSIGNOR hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for THE ASSIGNED PATENT RIGHTS to ASSIGNEE in accordance with the terms of this ASSIGNMENT but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from THE ASSIGNED PATENT RIGHTS including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **Assignor** are granted or assigned to ASSIGNEE under this ASSIGNMENT, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

ASSIGNOR further covenants that ASSIGNEE will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to THE ASSIGNED PATENT RIGHTS and legal equivalents as may be known and reasonably accessible to ASSIGNOR and that ASSIGNOR will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to ASSIGNEE or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce THE ASSIGNED PATENT RIGHTS and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4th April 2011	gray Brune
	Name: They Bruner Title: EVP and CFO
STATE OF California) COUNTY OF Santa Clava) SS	
On this 4 day of 4 2011, be Public, personally appeared 14 branch	efore me Aren I Branzon Watan (notary public), the undersigned Notary
within instrument and acknowledged to me that	lence to be the person(s) whose name(s) is/are subscribed to the at he/she/they executed the same in his/her/their authorized on the instrument the person(s), or the entity upon behalf of which
I certify under PENALTY OF PERJURY under the and correct.	laws of the State of California that the foregoing paragraph is true
	WITNESS my hand and official seal.
AVERIL BRANWEN WATAN Commission # 1742706 Notary Public - California Santa Clara County	Signature: Signature of Notary Public
My Comm. Expires Apr 29, 2011	We accept the Assignment
	SANDISK TECHNOLOGIES INC.
Dated: 4/4/11	Name: James F. Brelsford Title: President and Exerctany
STATE OF California) ss	, and the second
On this 4 day of April, 2011, be Public, personally appeared AMES F. E	efore me L-Benjamin, the undersigned Notary
who proved to me on the basis of satisfactory evid within instrument and acknowledged to me that	ence to be the person(s) whose name(s) is/are subscribed to the at he/she/they executed the same in his/her/their authorized

capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

L. BENJAMIN Commission # 1893105 Notary Public - California Santa Clara County My Comm. Expires Jul 16, 2014 WITNESS my hand and official seal.

Signature:

Signature of Notary Public

Exhibit A

ILE#	INVENTORS	TITLE	COUNTRY	STATUS	SER#	FILING DATE	PAT#	ISSUE DATE
DA-1101Y-US	Feng Pan	Analog Voltage Generator with Self-Biased Capacitive Feedback Stage	US	Issued	11618918	1-Jan-07	7492214	17-Feb-09
DA-1102Y-I-US	Jonathan Hubert, Jason Patrick Hanlon, Kevin Conley	Expandable And Collapsible Peripheral Device	US	Issued	12103533	15-Apr-08	7762849	27 -J ul-10
DA-1102Y-US	Jonathan Hubert, Jason Patrick Hanlon, Kevin Conley	Expandable And Collapsible Peripheral Device	US	Issued	11620519	5-Jan-07	7798840	21-Sep-10
DA-1103-US		Connection between an I/O Region and the Core Region of an Integrated Circuit	US	Pending	11651614	8-Jan-07		
DA-1104X-US	Takashi Orimoto, James Kai, Henry Chien, George Matamis	Methods of Forming NAND Memory with Virtual Channel	US	Issued	11626784	24-Jan-07	7494870	24-Feb-09
)A-1104Y-US	Takashi Orimoto, James Kai, Henry Chien, George Matamis	NAND Memory with Virtual Channel	US	Issued	11626778	24-Jan-07	7495282	24-Feb-09
DA-1105X-US	James Kai, George Matamis, Tuan D. Pham, Masaaki Higashitani, Takashi Orimoto	Methods of Forming Spacer Patterns Using Assist Layer for High Density Semiconductor Devices	US	Issued	11623314	15-Jan-07	7592225	22-Sep-09
)A-1105Y-a-US	James Kai, George Matamis, Tuan D.	Spacer Patterns Using Assist Layer for High Density Semiconductor Devices	US	Allowed	12791103	1-Jun-10		
)A-1105Y-US	James Kai, George Matamis, Tuan D.	Spacer Patterns Using Assist Layer for High Density Semiconductor Devices	US	Issued	11623315	15-Jan-07	7773403	10-Aug-10
)A-1106-US	Takashi Orimoto, George Matamis, James Kai, Tuan D. Pham, Masaaki Higashitani, Henry Chien	Methods of Forming Integrated Circuit Devices using Composite Spacer Structures	US	Issued	12014689	15-Jan-08	7795080	14-Sep-10
DA-1107Y-US	Darmin Jin, Brian Cheung	De-Glitch Circuit	US	Issued	11686828	15-Mar-07	7557643	7-Jul-09
DA-1108X-US	Nima Mokhlesi	Multiple Pass Write Sequence for Non-Volatile Storage	US	Issued	11694987	31-Mar-07	7564711	21-Jul-09
DA-1108Y-US	Nima Mokhlesi	Non-Volatile Storage Apparatus with Multiple Pass Write Sequence	US	Issued	11694989	31-Mar-07	7616500	10-Nov-09
DA-1109X-a-US	Nima Mokhlesi	Variable Initial Program Voltage Magnitude for Non- Volatile Storage	US	Published	12482696	11-Jun-09		
DA-1109X-US		Variable Initial Program Voltage Magnitude for Non- Volatile Storage		Issued	11694990	31-Mar-07	7564715	21-Jul-09
DA-1109Y-US	Nima Mokhlesi	Non-Voltage Storage Apparatus with Variable Initial Program Voltage Magnitude	US	Issued	11694991	31-Mar-07	7616495	10-Nov-09
)A-1110X-US		Dynamic Verify Based on Threshold Voltage Distribution	US	Issued	11694992	31-Mar-07	7619930	17-Nov-09
)A-1111X-a-US	Kini	PCB Circuit Modification From Multiple to Individual Chio Enable Signals		Published	12857365	16-Aug-10		
)A-1111X-US	Michael James McCarthy, Ning Ye, Naveen Kini	Method of Making PCB Circuit Modification From Multiple to Individual Chip Enable Signals	US	Issued	11679153	26-Feb-07	7709278	4-May-10
DA-1111Y-US	Michael James McCarthy, Ning Ye, Naveen Kini	PCB Circuit Modification From Multiple to Individual Chio Enable Signals	US	Issued	11679157	26-Feb-07	7778057	17-Aug-10
DA-1112X-US	Yan Li	Method for Cache Page Copy in a Non-Volatile Memory	US	Issued	11683356	7-Mar-07	7502255	10-Mar-09
DA-1112Y-US	Yan Li	Non-Volatile Memory with Cache Page Copy	US	Issued	11683365	7-Mar-07	7499320	3-Mar-09

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