

PATENT ASSIGNMENT

Electronic Version v1.1
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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SanDisk Corporation	04/04/2011
RECEIVING PARTY DATA	
Name:	SanDisk Technologies Inc.
Street Address:	6900 North Dallas Parkway
Internal Address:	Two Legacy Town Center
City:	Plano
State/Country:	TEXAS
Postal Code:	75024
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	7630252
CORRESPONDENCE DATA	
Fax Number:	(408)801-9019
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	4088012863
Email:	simona.benjamin@sandisk.com
Correspondent Name:	SanDisk Corporation
Address Line 1:	601 Mccarthy Boulevard
Address Line 2:	Patent Department/Simona Benjamin
Address Line 4:	Milpitas, CALIFORNIA 95035
ATTORNEY DOCKET NUMBER:	SDA-1163Y-US
NAME OF SUBMITTER:	Simona Benjamin
Total Attachments: 5 source=page 12#page1.tif source=page 12#page2.tif source=page 12#page3.tif source=page 12#page4.tif source=page 12#page5.tif	

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ASSIGNMENT

WHEREAS, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

WHEREAS, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

ASSIGNOR further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4th April 2011

Judy Bruner
Name: Judy Bruner
Title: EVP 2nd CFO

STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4th day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner,

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: Averil Watan
Signature of Notary Public

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: 4/4/11

James F. Brelsford
Name: James F. Brelsford
Title: President and Secretary

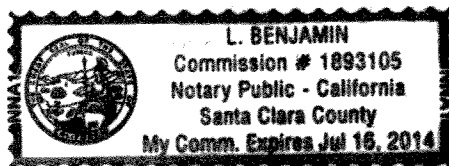
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: L. Benjamin
Signature of Notary Public

Exhibit A

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1154-1-US	Alan Welsh Sinclair	Method and Apparatus for Providing Data Type and Host File Information to a Mass Storage System	US	Published	12030018	12-Feb-08		
SDA-1154X-US	Alan Welsh Sinclair	Method of Interfacing a Host Operating Through a Logical Address Space with a Direct File Storage Medium	US	Published	11760469	8-Jun-07		
SDA-1155-US	Alan Welsh Sinclair, Barry Wright	Method and System for Storage Address Re-Mapping for a Memory Device	US	Published	12036014	22-Feb-08		
SDA-1156X-US	Robert C Chang, Po Yuan, Bahman Qawami, Farshid Sabet-Sharghi, Solomon Junzhi Wang, James Xian Jun Liu, Chieh-Hao (John) Yang, June Li, Mei Yan	Method of Storing and Accessing Header Data from Memory	US	Published	11771804	29-Jun-07		
SDA-1157X-US	Chih Chin Liao, Ning Ye, Cheeman Yu, Jack Chang Chien, Hem Takiar	Method of Mimizing Kerf Width on a Semiconductor Substrate Panel	US	Issued	11760385	8-Jun-07	7611927	3-Nov-09
SDA-1157Y-US	Chih Chin Liao, Ning Ye, Cheeman Yu, Jack Chang Chien, Hem Takiar	Two-Sided Substrate Lead Connection for Mimizing Kerf Width on a Semiconductor Substrate Panel	US	Published	11760405	8-Jun-07		
SDA-1158X-US	Loc Tu, Jian Chen, Alexander Mak, Tien-Chien Kuo, Long Pham	Programmable Chip Enable and Chip Address in Semiconductor Memory	US	Issued	11763287	14-Jun-07	7715255	11-May-10
SDA-1158Y-US	Loc Tu, Jian Chen, Alexander Mak, Tien-Chien Kuo, Long Pham	Systems for Programmable Chip Enable and Chip Address in Semiconductor Memory	US	Issued	11763292	14-Jun-07	7477545	13-Jan-09
SDA-1159X-US	Andrew Tomlin, Sergey Anatolevich Gorobets, Reuven Elhamias, Shai Traister, Alan Bennett	Method for Writing Data on an Atomic Transaction to a Memory Device	US	Pending	11820617	19-Jun-07		
SDA-1159Y-US	Andrew Tomlin, Sergey Anatolevich Gorobets, Reuven Elhamias, Shai Traister, Alan Bennett	Memory Device with Circuitry for Writing Data of an Atomic Transaction	US	Pending	11820670	19-Jun-07		
SDA-1161X-US	George Matamis, James Kai, Takashi Orimoto, Nima Mokhlesi	Methods of Forming High Density Semiconductor Devices Using Recursive Spacer Technique	US	Published	11765866	20-Jun-07		
SDA-1162X-a-US	Yupin Fong, Jun Wan	Intelligent Control of Program Pulse Duration for Non-Volatile Storage	US	Published	12607329	28-Oct-09		
SDA-1162X-US	Yupin Fong, Jun Wan	Intelligent Control of Program Pulse Duration	US	Issued	11766583	21-Jun-07	7630249	8-Dec-09
SDA-1162Y-US	Yupin Fong, Jun Wan	Non-Volatile Storage System with Intelligent Control of Program Pulse Duration	US	Issued	11766580	21-Jun-07	7580290	25-Aug-09
SDA-1163X-US	Jason Lin	Methods of Programming Multilevel Cell Non-Volatile Memory	US	Issued	11767605	25-Jun-07	7719889	18-May-10
SDA-1163Y-US	Jason Lin	Systems for Programming Multilevel Cell Non-Volatile Memory	US	Issued	11767611	25-Jun-07	7630252	8-Dec-09
SDA-1164X-US	Nima Mokhlesi, Jun Wan	Method for Forming Dual Bit Line Metal Layers for Non-Volatile Memory	US	Published	11768461	26-Jun-07		
SDA-1164Y-US	Nima Mokhlesi, Jun Wan	Dual Bit Line Metal Layers for Non-Volatile Memory	US	Published	11768468	26-Jun-07		
SDA-1165X-US	Haluk Kent Tanik	Method for Communicating with a Non-Volatile Memory Storage Device	US	Published	11771165	29-Jun-07		
SDA-1165Y-US	Haluk Kent Tanik	System for Communicating with a Non-Volatile Memory Storage Device	US	Published	11771203	29-Jun-07		
SDA-1166Y-US	Jason Lin	Systems and Methods For Reading Nonvolatile Memory Using Multiple Reading Schemes	US	Issued	11767587	25-Jun-07	7849383	7-Dec-10