

**PATENT ASSIGNMENT**

Electronic Version v1.1  
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SanDisk Corporation	04/04/2011
RECEIVING PARTY DATA	
Name:	SanDisk Technologies Inc.
Street Address:	6900 North Dallas Parkway
Internal Address:	Two Legacy Town Center
City:	Plano
State/Country:	TEXAS
Postal Code:	75024
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	7813172
CORRESPONDENCE DATA	
Fax Number:	(408)801-9019
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	4088012863
Email:	simona.benjamin@sandisk.com
Correspondent Name:	SanDisk Corporation
Address Line 1:	601 Mccarthy Boulevard
Address Line 2:	Patent Department/Simona Benjamin
Address Line 4:	Milpitas, CALIFORNIA 95035
ATTORNEY DOCKET NUMBER:	SDA-1217Y-US
NAME OF SUBMITTER:	Simona Benjamin
Total Attachments: 5 source=page 16#page1.tif source=page 16#page2.tif source=page 16#page3.tif source=page 16#page4.tif source=page 16#page5.tif	

**CH \$40.00 7813172**

## ASSIGNMENT

**WHEREAS**, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

**WHEREAS**, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

**NOW, THEREFORE**, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

**ASSIGNOR** hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

**ASSIGNOR** further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4<sup>th</sup> April 2011

Judy Bruner  
Name: Judy Bruner  
Title: EVP 2nd CFO

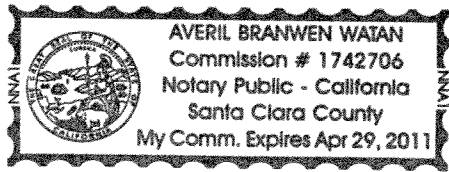
STATE OF California )  
COUNTY OF Santa Clara ) ss.

On this 4<sup>th</sup> day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner,

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: Averil Watan  
Signature of Notary Public

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: 4/4/11

James F. Brelsford  
Name: James F. Brelsford  
Title: President and Secretary

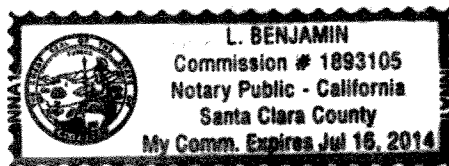
STATE OF California )  
COUNTY OF Santa Clara ) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: L. Benjamin  
Signature of Notary Public

**Exhibit A**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1217Y-US	Raul-Adrian Cernea	NONVOLATILE MEMORY WITH CORRELATED MULTIPLE PASS PROGRAMMING	US	Issued	12138387	12-Jun-08	7813172	12-Oct-10
SDA-1218X-US	Raul-Adrian Cernea	METHOD FOR INDEX PROGRAMMING AND REDUCED VERIFY IN NONVOLATILE MEMORY	US	Issued	12138371	12-Jun-08	7800945	21-Sep-10
SDA-1218Y-US	Raul-Adrian Cernea	NONVOLATILE MEMORY WITH INDEX PROGRAMMING AND REDUCED VERIFY	US	Issued	12138378	12-Jun-08	7826271	2-Nov-10
SDA-1219-US	Siu Lung Chan	Nonvolatile memory and method for smart program and precharge and verify	US	Unfiled				
SDA-1220-a-US	Yan Li, Yupin Fong, Siu Lung Chan	Nonvolatile Memory And Method With Reduced Program Verify By Ignoring Fastest And/Or Slowest Programming Bits	US	Issued	12407665	19-Mar-09	7894273	22-Feb-11
SDA-1220-b-US	Yan Li, Yupin Fong, Siu Lung Chan	Nonvolatile Memory And Method With Reduced Program Verify By Ignoring Fastest And/Or Slowest Programming Bits	US	Pending	13029848	17-Feb-11		
SDA-1220-US	Yan Li, Yupin Fong, Siu Lung Chan	Nonvolatile Memory And Method With Reduced Program Verify By Ignoring Fastest And/Or Slowest Programming Bits	US	Issued	12249678	10-Oct-08	7768836	3-Aug-10
SDA-1221-a-US	Jeffrey Lutze, Dana Lee	Offset Non-Volatile Storage	US	Published	12822546	24-Jun-10		
SDA-1221-II-US	Jeffrey Lutze, Dana Lee	Offset Non-Volatile Storage	US	Published	12822584	24-Jun-10		
SDA-1221-I-US	Jeffrey Lutze, Dana Lee	Offset Non-Volatile Storage	US	Published	12822568	24-Jun-10		
SDA-1221-US	Jeffrey Lutze, Dana Lee	Offset Non-Volatile Storage	US	Issued	11861135	25-Sep-07	7760547	20-Jul-10
SDA-1224-US	Paul McAvoy, Po Yuan, Alson Kemp	System and Methods for Digital Content Distribution	US	Published	11863714	28-Sep-07		
SDA-1225-US	Kevin Conley, Bill Thanos, Keith D. Washo, Ronald Chung	Portable, Digital Media Player and Associated Methods	US	Published	11863790	28-Sep-07		
SDA-1227-US	Shai Traister	Dynamic Meta-Blocks	US	Published	11864629	28-Sep-07		
SDA-1228-US	Deepak C. Sekar, Nima Mokhesi	Single-Layer Metal Conductors With Multiple Thicknesses	US	Published	11861994	26-Sep-07		
SDA-1229-US	Nima Mokhesi, Dengtao Zhao, Man Mui, Hao Thai Nguyen, Seungail Lee, Deepak C. Sekar, Tanan Samaddar	Multiple Bit Line Voltages Based On Distance	US	Issued	11861571	26-Sep-07	7551477	23-Jun-09
SDA-1230-US	Nima Mokhesi, Dengtao Zhao, Henry Chin, Tanan Samaddar	Compensation of Non-Volatile-Memory Chip Non-Idealities by Program Pulse Adjustment	US	Allowed	11862485	27-Sep-07		
SDA-1231-I-US	Dana Lee, Hock C So	High Voltage Generation and Control in Source-Side Injection Programming of Non-Volatile Memory	US	Pending	13028847	16-Feb-11		
SDA-1231-US	Dana Lee, Hock C So	High Voltage Generation and Control in Source-Side Injection Programming of Non-Volatile Memory	US	Issued	11864825	28-Sep-07	7894263	22-Feb-11
SDA-1232-US	Dana Lee, Jun Wan	Reducing Programming Voltage Differential Nonlinearity in Non-Volatile Storage	US	Issued	11861909	26-Sep-07	7577034	18-Aug-09
SDA-1233-I-US	Kong Hoo KH Ong, Java Zhu, Ning Ye, Hem Takiar	Method of Reducing Memory Card Edge Roughness by Edge Coating	US	Pending	12901861	11-Oct-10		
SDA-1233-US	Kong Hoo KH Ong, Java Zhu, Ning Ye, Hem Takiar	Method of Reducing Memory Card Edge Roughness by Edge Coating	US	Issued	11863809	28-Sep-07	7811859	12-Oct-10
SDA-1235-US	Steven Cheng	Nonvolatile Memory with Self Recovery	US	Issued	11861146	25-Sep-07	7873803	18-Jan-11
SDA-1236-US	Steven T. Sprouse, Dhaval Parikh, Michael James McCarthy	Multi-Host Interface Controller with USB PHY/Analog Functions Integrated in a Single Package	US	Issued	11906582	2-Oct-07	7898091	1-Mar-11