

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SanDisk Corporation	04/04/2011
RECEIVING PARTY DATA	
Name:	SanDisk Technologies Inc.
Street Address:	6900 North Dallas Parkway
Internal Address:	Two Legacy Town Center
City:	Plano
State/Country:	TEXAS
Postal Code:	75024
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	7751249
CORRESPONDENCE DATA	
Fax Number:	(408)801-9019
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	4088012863
Email:	simona.benjamin@sandisk.com
Correspondent Name:	SanDisk Corporation
Address Line 1:	601 Mccarthy Boulevard
Address Line 2:	Patent Department/Simona Benjamin
Address Line 4:	Milpitas, CALIFORNIA 95035
ATTORNEY DOCKET NUMBER:	SDA-1327X-US
NAME OF SUBMITTER:	Simona Benjamin
Total Attachments: 5 source=page 21#page1.tif source=page 21#page2.tif source=page 21#page3.tif source=page 21#page4.tif source=page 21#page5.tif	

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ASSIGNMENT

WHEREAS, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

WHEREAS, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

ASSIGNOR further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4th April 2011

Judy Bruner
Name: Judy Bruner
Title: EVP 2nd CFO

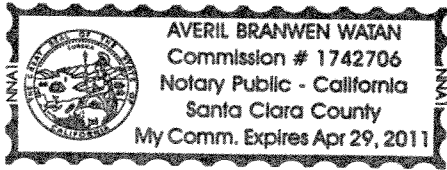
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4th day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner,

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: Averil Branwen Watan
Signature of Notary Public

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: 4/4/11

James F. Brelsford
Name: James F. Brelsford
Title: President and Secretary

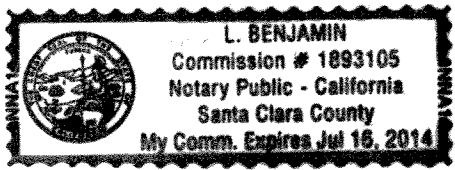
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: L. Benjamin
Signature of Notary Public

Exhibit A

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1318-US	Yaron Sheba, Derek Niizawa	Media Player	US	Issued	29301602	7-Mar-08	D584742	13-Jan-09
SDA-1319-US	Yaron Sheba, Derek Niizawa	Media Player	US	Issued	29301600	7-Mar-08	D584740	13-Jan-09
SDA-1320-US	Yaron Sheba, Derek Niizawa	Media Player	US	Issued	29301601	7-Mar-08	D584741	13-Jan-09
SDA-1323-US	Ahmet Altay, Haluk Kent Tanik, Robert C Chang, James Xian Jun Liu, Oktay Rasizade, Po Yuan	Managing Host Application Privileges	US	Pending	12342965	23-Dec-08		
SDA-1324-US	Rotem Sela, Michael Holtzman, Ron Barzilai, Donald Ray Bryant-Rich	Managing Access to an Address Range in a Storage Device	US	Pending	12338738	18-Dec-08		
SDA-1325X-US	Xingzhi Nemo Liang, Hai Bo Fang, Li Wang	METHOD OF FABRICATING WIRE ON WIRE STITCH BONDING IN A SEMICONDUCTOR DEVICE	US	Published	12165375	30-Jun-08		
SDA-1325Y-US	Xingzhi Nemo Liang, Hai Bo Fang, Li Wang	WIRE ON WIRE STITCH BONDING IN A SEMICONDUCTOR DEVICE	US	Published	12165391	30-Jun-08		
SDA-1326X-US	James Kai, Dana Lee, Takashi Orimoto, Vinod Robert Purayath, George Matamis, Henry Chin	METHOD OF FORMING DIELECTRIC LAYER ABOVE FLOATING GATE FOR REDUCING LEAKAGE CURRENT	US	Issued	12170321	9-Jul-08	7915124	29-Mar-11
SDA-1326Y-US	Dana Lee, Henry Chin, James Kai, Takashi Orimoto, Vinod Robert Purayath, George Matamis	DIELECTRIC LAYER ABOVE FLOATING GATE FOR REDUCING LEAKAGE CURRENT	US	Allowed	12170327	9-Jul-08		
SDA-1327X-US	Seungpil Lee, Hao Thai Nguyen, Man Mui	MINIMIZING POWER NOISE DURING SENSING IN MEMORY DEVICE	US	Issued	12163115	27-Jun-08	7751249	6-Jul-10
SDA-1327Y-US	Seungpil Lee, Hao Thai Nguyen, Man Mui	MEMORY DEVICE WITH POWER NOISE MINIMIZATION DURING SENSING	US	Issued	12163133	27-Jun-08	7751250	6-Jul-10
SDA-1328-US	Jonathan Huynh, Qui Nguyen, Feng Pan	Techniques of Ripple Reduction for Charge Pumps	US	Issued	12146243	25-Jun-08	7683700	23-Mar-10
SDA-1329-a-US	Nima Mokhlesi	All-Bit-Line Erase Verify and Soft Program Verify	US	Issued	12767660	26-Apr-10	7920422	5-Apr-11
SDA-1329-US	Nima Mokhlesi	All-Bit-Line Erase Verify and Soft Program Verify	US	Issued	12323413	25-Nov-08	7764544	27-Jul-10
SDA-1330-US	Yan Li, Pao-Ling Anne Koh	Adaptive Algorithm in Cache Operation with Dynamic Data Latch Requirements	US	Published	12051462	19-Mar-08		
SDA-1331-US	Yan Li	Different Combinations of Wordline Order and Look-Ahead Read to Improve Non-Volatile Memory Performance	US	Published	12051492	19-Mar-08		
SDA-1332-US	Vadzim Struk, Derrill Sturgeon	AVOIDANCE OF SELF EVICTION CAUSED BY DYNAMIC MEMORY ALLOCATION IN A FLASH MEMORY STORAGE DEVICE	US	Published	12177863	22-Jul-08		
SDA-1333-US	Fabrice Jogand-Coulomb, Robert C Chang, Po Yuan, Mei Yan, James Xian Jun Liu	CONTROLLED DATA ACCESS TO NON-VOLATILE MEMORY	US	Published	12336445	16-Dec-08		
SDA-1334-US	Oktay Rasizade, Haluk Kent Tanik, Fabrice Jogand-Coulomb	CONTENT BINDING AT FIRST ACCESS	US	Pending	12619112	16-Nov-09		
SDA-1335-US	Rotem Sela, Vijay Ahuja, Michael Holtzman, John Michael Podobnik, Avi Shmuel	Methods for Producing Products with Certificates and Keys	US	Published	12408308	20-Mar-09		
SDA-1336-US	Hyekyung (Sophia) Kim	Housing and Clip Assembly for Portable Electronics Device	US	Published	12134664	6-Jun-08		
SDA-1337-US	George Matamis, Takashi Orimoto, Masaaki Higashitani, James Kai, Tuan D. Pham	Methods of Fabricating Non-Volatile Memory With Integrated Peripheral Circuitry and Pre-Isolation Memory Cell Formation	US	Issued	12061641	2-Apr-08	7582529	1-Sep-09