

## PATENT ASSIGNMENT

Electronic Version v1.1

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|                                                                                                                                                                  |                                   |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------|
| SUBMISSION TYPE:                                                                                                                                                 | NEW ASSIGNMENT                    |
| NATURE OF CONVEYANCE:                                                                                                                                            | ASSIGNMENT                        |
| CONVEYING PARTY DATA                                                                                                                                             |                                   |
| Name                                                                                                                                                             | Execution Date                    |
| SanDisk Corporation                                                                                                                                              | 04/04/2011                        |
| RECEIVING PARTY DATA                                                                                                                                             |                                   |
| Name:                                                                                                                                                            | SanDisk Technologies Inc.         |
| Street Address:                                                                                                                                                  | 6900 North Dallas Parkway         |
| Internal Address:                                                                                                                                                | Two Legacy Town Center            |
| City:                                                                                                                                                            | Plano                             |
| State/Country:                                                                                                                                                   | TEXAS                             |
| Postal Code:                                                                                                                                                     | 75024                             |
| PROPERTY NUMBERS Total: 1                                                                                                                                        |                                   |
| Property Type                                                                                                                                                    | Number                            |
| Patent Number:                                                                                                                                                   | 7915664                           |
| CORRESPONDENCE DATA                                                                                                                                              |                                   |
| Fax Number:                                                                                                                                                      | (408)801-9019                     |
| <i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>                                                                             |                                   |
| Phone:                                                                                                                                                           | 4088012863                        |
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| Correspondent Name:                                                                                                                                              | SanDisk Corporation               |
| Address Line 1:                                                                                                                                                  | 601 Mccarthy Boulevard            |
| Address Line 2:                                                                                                                                                  | Patent Department/Simona Benjamin |
| Address Line 4:                                                                                                                                                  | Milpitas, CALIFORNIA 95035        |
| ATTORNEY DOCKET NUMBER:                                                                                                                                          | SDA-1343-US                       |
| NAME OF SUBMITTER:                                                                                                                                               | Simona Benjamin                   |
| Total Attachments: 5<br>source=page 22#page1.tif<br>source=page 22#page2.tif<br>source=page 22#page3.tif<br>source=page 22#page4.tif<br>source=page 22#page5.tif |                                   |

CH 7915664 \$40.00

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PATENT  
REEL: 026279 FRAME: 0909

## ASSIGNMENT

**WHEREAS**, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

**WHEREAS**, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

**NOW, THEREFORE**, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

**ASSIGNOR** hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

**ASSIGNOR** further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

**SANDISK CORPORATION**

Dated: 4<sup>th</sup> April 2011

Name: Judy Bruner  
Title: EVP and CFO

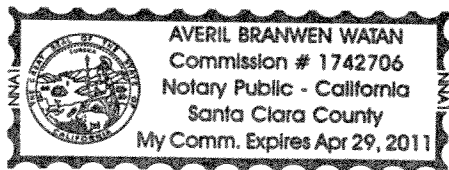
STATE OF California )  
COUNTY OF Santa Clara ) ss.

On this 4<sup>th</sup> day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: [Signature]  
Signature of Notary Public

**We accept the Assignment**

**SANDISK TECHNOLOGIES INC.**

Dated: 4/4/11

Name: James F. Brelsford  
Title: President and Secretary

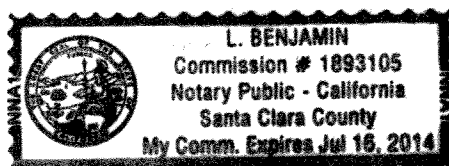
STATE OF California )  
COUNTY OF Santa Clara ) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: [Signature]  
Signature of Notary Public

## Exhibit A

| FILE #         | INVENTORS                                                                      | TITLE                                                                                                                               | COUNTRY | STATUS    | SER #    | FILING DATE | PAT #   | ISSUE DATE |
|----------------|--------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------|---------|-----------|----------|-------------|---------|------------|
| SDA-1338-US    | Tuan D. Pham, Takashi Orimoto, Masaaki Higashitani, James Kai, George Matamis  | Methods of Fabricating Non-Volatile Memory With Integrated Select And Peripheral Circuitry and Post-Isolation Memory Cell Formation | US      | Issued    | 12061642 | 2-Apr-08    | 7592223 | 22-Sep-09  |
| SDA-1339-US    | Masaaki Higashitani                                                            | Non-Volatile Storage Having A Connected Source and Well                                                                             | US      | Published | 12060956 | 2-Apr-08    |         |            |
| SDA-1340X-US   | Richard C Spence, Kevin Conley                                                 | Method for Playing Digital Media Files with a Digital Media Player Using a Plurality of Playlists                                   | US      | Pending   | 12157844 | 13-Jun-08   |         |            |
| SDA-1340Y-US   | Richard C Spence, Kevin Conley                                                 | Memory Device Storing a Plurality of Digital Media Files and Playlists                                                              | US      | Pending   | 12157846 | 13-Jun-08   |         |            |
| SDA-1341-US    | Henry R Hutton                                                                 | USB Memory Card Reader Having Memory Card Identification                                                                            | US      | Allowed   | 12273463 | 18-Nov-08   |         |            |
| SDA-1342-US    | Hao Thai Nguyen, Man Mui, Seungpil Lee, Fanglin Zhang, Chi-Ming Wang           | High Speed Sense Amplifier Array and Method for Nonvolatile Memory                                                                  | US      | Published | 12128535 | 28-May-08   |         |            |
| SDA-1343-US    | Henry Chien, Takashi Orimoto, George Matamis, James Kai, Vinod Robert Puravath | Non-Volatile Memory with Sidewall Channels and Raised Source/Drain Regions                                                          | US      | Issued    | 12105242 | 17-Apr-08   | 7915664 | 29-Mar-11  |
| SDA-1344-US    | Alan Welsh Sinclair                                                            | Method and System for Storage Address Re-Mapping for a Multi-Bank Memory Device                                                     | US      | Pending   | 12110050 | 25-Apr-08   |         |            |
| SDA-1345-US    | Nima Mokhlesi, Henry Chin                                                      | DYNAMIC AND ADAPTIVE OPTIMIZATION OF READ COMPARE LEVELS BASED ON MEMORY CELL THRESHOLD VOLTAGE DISTRIBUTION                        | US      | Published | 12338850 | 18-Dec-08   |         |            |
| SDA-1346-1-US  | Yingda Dong, Jeffrey Lutze                                                     | Source and Drain Side Early Boosting Using Local Self Boosting For Non-Volatile Storage                                             | US      | Issued    | 12060487 | 1-Apr-08    | 7577026 | 18-Aug-09  |
| SDA-1347-US    | Yingda Dong, Man Mui, Jeffrey Lutze, Shinji Sato, Gerrit Jan Hemink            | Enhanced Bitline Pre-charge Scheme for Increasing Channel Boosting in Non-Volatile Storage                                          | US      | Issued    | 12126375 | 23-May-08   | 7719902 | 18-May-10  |
| SDA-1348X-US   | Hem Takiar, Shrikar Bhagath                                                    | METHOD OF FABRICATING STACKED WIRE BONDED SEMICONDUCTOR PACKAGE WITH LOW PROFILE BOND LINE                                          | US      | Published | 12165340 | 30-Jun-08   |         |            |
| SDA-1348Y-US   | Hem Takiar, Shrikar Bhagath                                                    | STACKED WIRE BONDED SEMICONDUCTOR PACKAGE WITH LOW PROFILE BOND LINE                                                                | US      | Published | 12165362 | 30-Jun-08   |         |            |
| SDA-1349X-US   | Robert C Miller                                                                | METHOD FOR PREVENTING DAMAGE TO A MEMORY CARD                                                                                       | US      | Issued    | 12239093 | 26-Sep-08   | 7909611 | 22-Mar-11  |
| SDA-1349Y-a-US | Robert C Miller                                                                | CONNECTOR BLOCK FEATURE                                                                                                             | US      | Pending   | 12983752 | 3-Jan-11    |         |            |
| SDA-1349Y-US   | Robert C Miller                                                                | CONNECTOR BLOCK FEATURE                                                                                                             | US      | Issued    | 12239109 | 26-Sep-08   | 7862381 | 4-Jan-11   |
| SDA-1350-US    | Kong Hoo KH Ong, Robertito Piaduche, Ning Ye, Hem Takiar                       | Method For Reclaiming Semiconductor Package                                                                                         | US      | Issued    | 12165383 | 30-Jun-08   | 7745234 | 29-Jun-10  |
| SDA-1351-a-US  | Nima Mokhlesi                                                                  | Data Refresh for Non-Volatile Storage                                                                                               | US      | Published | 12903067 | 12-Oct-10   |         |            |
| SDA-1351-US    | Nima Mokhlesi                                                                  | Data Refresh for Non-Volatile Storage                                                                                               | US      | Issued    | 12338879 | 18-Dec-08   | 7859932 | 28-Dec-10  |
| SDA-1352-I-US  | Nima Mokhlesi, Klaus Schuegraf                                                 | READ DISTURB MITIGATION IN NON-VOLATILE MEMORY                                                                                      | US      | Issued    | 12832822 | 8-Jul-10    | 7867620 | 25-Jan-11  |
| SDA-1352-US    | Nima Mokhlesi, Klaus Schuegraf                                                 | READ DISTURB MITIGATION IN NON-VOLATILE MEMORY                                                                                      | US      | Issued    | 12165302 | 30-Jun-08   | 7808831 | 5-Oct-10   |
| SDA-1353-US    | Suresh Upadhyayula, Chih Chin Liao, Hem Takiar                                 | Use of BT Substrate as Interposer in SIP Cards                                                                                      | US      | Closed    |          |             |         |            |
| SDA-1354-US    | Benjamin Telya                                                                 | FLASH Memory Bootloader                                                                                                             | US      | Unfiled   |          |             |         |            |
| SDA-1355-US    | Geoffrey S Gongwer                                                             | Sloped IC Die Edge to Allow connection Between Stacked IC's                                                                         | US      | Unfiled   |          |             |         |            |