

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SanDisk Corporation	04/04/2011
RECEIVING PARTY DATA	
Name:	SanDisk Technologies Inc.
Street Address:	6900 North Dallas Parkway
Internal Address:	Two Legacy Town Center
City:	Plano
State/Country:	TEXAS
Postal Code:	75024
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	7898864
CORRESPONDENCE DATA	
Fax Number:	(408)801-9019
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	4088012863
Email:	simona.benjamin@sandisk.com
Correspondent Name:	SanDisk Corporation
Address Line 1:	601 Mccarthy Boulevard
Address Line 2:	Patent Department/Simona Benjamin
Address Line 4:	Milpitas, CALIFORNIA 95035
ATTORNEY DOCKET NUMBER:	SDA-1452-US
NAME OF SUBMITTER:	Simona Benjamin
Total Attachments: 5 source=page 27#page1.tif source=page 27#page2.tif source=page 27#page3.tif source=page 27#page4.tif source=page 27#page5.tif	

CH \$40.00 7898864

ASSIGNMENT

WHEREAS, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

WHEREAS, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

ASSIGNOR further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4th April 2011

Judy Bruner
Name: Judy Bruner
Title: EVP 2nd CFO

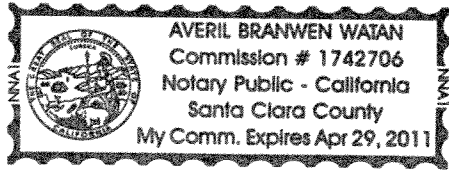
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4th day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner,

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: Averil Watan
Signature of Notary Public

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: 4/4/11

James F. Brelsford
Name: James F. Brelsford
Title: President and Secretary

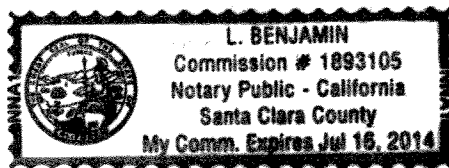
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: L. Benjamin
Signature of Notary Public

Exhibit A

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1449-US	Jason Lin, Fabrice Jogand-Coulomb	Method and System for Content Replication Control	US	Published	12478688	4-Jun-09		
SDA-1451-US	Tuan D. Pham, Sanghyun Lee, Masato Horiike, Klaus Schuegraf, Masaaki Higashitani, Keichi Isono	Integrated Circuits And Fabrication Using Sidewall Nitridation Processes	US	Published	12763963	20-Apr-10		
SDA-1452-US	Yingda Dong	Read Operation for Memory with Compensation for Coupling Based on Write-Erase Cycles	US	Issued	12490550	24-Jun-09	7898864	1-Mar-11
SDA-1453-US	Yingda Dong, Cynthia Hsu	Forecasting Program Disturb in Memory by Detecting Natural Threshold Voltage Distribution	US	Issued	12490557	24-Jun-09	7916533	29-Mar-11
SDA-1454-1-US	Yaron Sheba, Philip Lee Ly	Memory Card Holder and Organizer for Holding and Organizing a Plurality of Portable Memory Cables	US	Published	12493134	26-Jun-09		
SDA-1455-a-US	Shrikar Bhagath, Hem Takiar	Semiconductor device having under-filled die in a die stack	US	Pending	12906523	18-Oct-10		
SDA-1455-US	Shrikar Bhagath, Hem Takiar	Method of Under-Filling Semiconductor Die in a Die Stack and Semiconductor Device formed thereby	US	Issued	12494803	30-Jun-09	7816181	19-Oct-10
SDA-1456-US	Yingda Dong, Tien-Chien Kuo, Gerrit Jan Hemink	Selective Memory Cell Program and Erase	US	Published	12544113	19-Aug-09		
SDA-1457-US	Yaron Sheba, Philip Lee Ly	Media Player (1 of 4)	US	Issued	29337735	28-May-09	D605165	1-Dec-09
SDA-1458-US	Yaron Sheba, Philip Lee Ly	Memory Card Holder	US	Issued	29337965	2-Jun-09	D613744	13-Apr-10
SDA-1460-US	Steven Cheng	Memory System with Multi-Level Signaling and Method for Operation the Same	US	Published	12495717	30-Jun-09		
SDA-1461-US	Yaron Sheba, Philip Lee Ly	Media Player (2 of 4)	US	Issued	29337757	28-May-09	D607874	12-Jan-10
SDA-1462-US	Yaron Sheba, Philip Lee Ly	Media Player (3 of 4)	US	Issued	29337759	28-May-09	D605167	1-Dec-09
SDA-1463-US	Yaron Sheba, Philip Lee Ly	Media Player (4 of 4)	US	Issued	29337755	28-May-09	D605166	1-Dec-09
SDA-1464-US	Manuel Antonio D'Abreu, Steve Skala	SYSTEM AND METHOD OF TRACKING ERROR DATA WITHIN A STORAGE DEVICE	US	Published	12494186	29-Jun-09		
SDA-1465-US	Manuel Antonio D'Abreu, Steve Skala	METHOD AND DEVICE FOR SELECTIVELY REFRESHING A REGION OF A NON-VOLATILE MEMORY OF A DATA STORAGE DEVICE	US	Published	12494210	29-Jun-09		
SDA-1466-US	Manuel Antonio D'Abreu, Steve Skala, Dana Lee	SYSTEM AND METHOD RESPONSIVE TO A RATE OF CHANGE OF A PERFORMANCE PARAMETER OF A MEMORY	US	Published	12494222	29-Jun-09		
SDA-1467-US	Saranyan Rajagopalan, Charles Michael Schroter	Multi-Bank Non-Volatile Memory System with Satellite File System	US	Published	12485500	16-Jun-09		
SDA-1468-US	Yan Li, Kwang-ho Kim, Wanfang (Frank) Tsai, Aldo Bottelli	Bad Column Management with Bit Information in Non-Volatile Memory Systems	US	Published	12498220	6-Jul-09		
SDA-1469-US	Feng Pan	CHARGE PUMP WITH CURRENT BASED REGULATION	US	Pending	12506998	21-Jul-09		
SDA-1470-US	Jason Lin, Alexander Kanaris, Joe Halpern	Memory Device and Method for Embedding Host-Identification Information into Content	US	Published	12492751	26-Jun-09		
SDA-1471-US	Ron Barzilai, Michael Holtzman	Method for Performing Double Domain Encryption in a Memory Device	US	Published	12478691	4-Jun-09		
SDA-1474-US	Man Mui, Yingda Dong, Binh Le, Deeoanshu Dutta	Partial Speed and Full Speed Programming for Non-Volatile Memory Using Floating Bit Lines	US	Published	12547449	25-Aug-09		