

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

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| SUBMISSION TYPE: | NEW ASSIGNMENT |
| NATURE OF CONVEYANCE: | ASSIGNMENT |
| CONVEYING PARTY DATA | |
| Name | Execution Date |
| SanDisk Corporation | 04/04/2011 |
| RECEIVING PARTY DATA | |
| Name: | SanDisk Technologies Inc. |
| Street Address: | 6900 North Dallas Parkway |
| Internal Address: | Two Legacy Town Center |
| City: | Plano |
| State/Country: | TEXAS |
| Postal Code: | 75024 |
| PROPERTY NUMBERS Total: 1 | |
| Property Type | Number |
| Application Number: | 12882357 |
| CORRESPONDENCE DATA | |
| Fax Number: | (408)801-9019 |
| <i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i> | |
| Phone: | 4088012863 |
| Email: | simona.benjamin@sandisk.com |
| Correspondent Name: | SanDisk Corporation |
| Address Line 1: | 601 Mccarthy Boulevard |
| Address Line 2: | Patent Department/Simona Benjamin |
| Address Line 4: | Milpitas, CALIFORNIA 95035 |
| ATTORNEY DOCKET NUMBER: | SDA-1552-US |
| NAME OF SUBMITTER: | Simona Benjamin |
| Total Attachments: 5 source=page 31#page1.tif source=page 31#page2.tif source=page 31#page3.tif source=page 31#page4.tif source=page 31#page5.tif | |

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PATENT
REEL: 026294 FRAME: 0585

ASSIGNMENT

WHEREAS, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

WHEREAS, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

ASSIGNOR further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4th April 2011

Name: Judy Bruner
Title: EVP and CFO

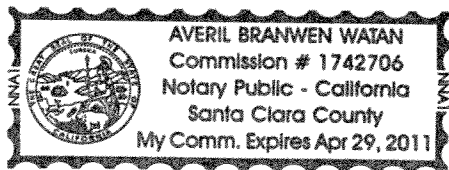
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4th day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: [Signature]
Signature of Notary Public

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: 4/4/11

Name: James F. Brelsford
Title: President and Secretary

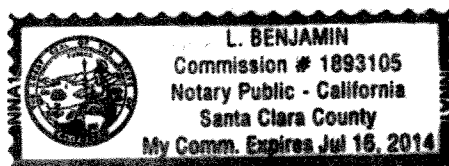
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: [Signature]
Signature of Notary Public

Exhibit A

| FILE # | INVENTORS | TITLE | COUNTRY | STATUS | SER # | FILING DATE | PAT # | ISSUE DATE |
|-------------|--|---|---------|---------|----------|-------------|---------|------------|
| SDA-1540-US | Deepak Pancholi, Bhavin Odedara, Naidu Prasad, Srikanth Bojja, Srinivasa Rao Sabbineni, Jayaprakash Naradasi | Self-Calibrating Relaxation Oscillator Based Clock Cycle | US | Pending | 12754836 | 6-Apr-10 | | |
| SDA-1541-US | Dhaval Parikh, Tony Ahwal | Auxiliary Interface for Non-Volatile Memory System | US | Pending | 12969167 | 15-Dec-10 | | |
| SDA-1542-US | Ken Oowada, Yingda Dong, Deepanshu Dutta | Extra dummy erase pulse(s) after shallow erase verify to avoid sensing deep erased voltage | US | Pending | 12751265 | 31-Mar-10 | | |
| SDA-1544-US | Yingda Dong, Yupin Fong, Gerrit Jan Hemink | Saw-Shaped Multi-Pulse Program Noise Reduction in Memory | US | Pending | 12757399 | 9-Apr-10 | | |
| SDA-1545-US | Yan Li | Fast Random Access to Non-Volatile Storage | US | Pending | 12835315 | 13-Jul-10 | | |
| SDA-1546-US | Matthew Davidson, Ralph Heron, Lakhdar Iguelmame, Rony (Pc) Tal, Asher Druck | Enhancement of Input/Output for non source-synchronous interfaces | US | Issued | 12731504 | 25-Mar-10 | 7888966 | 15-Feb-11 |
| SDA-1547-US | George Samachisa, Johann Alsmeyer | ULTRAHIGH DENSITY VERTICAL NAND MEMORY DEVICE AND METHOD OF MAKING THEREOF | US | Pending | 12827761 | 30-Jun-10 | | |
| SDA-1548-US | Jarrett Liang, Vinod Robert Purayath, Takashi Oimoto | Metal Control Gate Formation in Non-Volatile Storage | US | Pending | 12845329 | 28-Jul-10 | | |
| SDA-1549-US | Jayavel Pachamuthu, Vinod Robert Puravath | Process for Fabricating Non-Volatile Storage | US | Pending | 12762341 | 18-Apr-10 | | |
| SDA-1550-US | Yaron Sheba, Philip Lee Ly, Derek Niizawa | Media Player | US | Issued | 29360076 | 20-Apr-10 | D627758 | 23-Nov-10 |
| SDA-1551-US | Yuval Weiss, Daniel Weinfeld | Efficient Electrical Hibernate Entry and Recovery | US | Pending | 12727073 | 18-Mar-10 | | |
| SDA-1552-US | Jayaprakash Naradasi, Anand Venkitachalam | System and Method of Distributive ECC Processing | US | Pending | 12882357 | 15-Sep-10 | | |
| SDA-1553-US | Yan Li, Dana Lee, Jonathan Huynh | Method and Apparatus to Detect Wordline at high Voltage with built in self-calibration | US | Pending | 12833146 | 9-Jul-10 | | |
| SDA-1554-US | Grishma Shailesh Shah, Yan Li | Detection of Broken Word-Lines in Memory Arrays | US | Pending | 12833167 | 9-Jul-10 | | |
| SDA-1555-US | Manuel Antonio D'Abreu, Steve Skala, Jayaprakash Naradasi, Anand Venkitachalam | System and Method of Interleaving Data According to an Adjustable Parameter | US | Pending | 13031531 | 21-Feb-11 | | |
| SDA-1557-US | Takashi Oimoto, Atsushi Suyama, Ming Tian, Henry Chin, Henry Chien, Vinod Robert Puravath, Dana Lee | P-Type Control Gate in Non-Volatile Storage and Methods for Forming Same | US | Pending | 12887328 | 21-Sep-10 | | |
| SDA-1558-US | George Samachisa, Johann Alsmeyer, Andrei Mihnea | Junctionless TFT NAND Flash Memory | US | Pending | 12848458 | 2-Aug-10 | | |
| SDA-1559-US | Yosuke Kato | Non-Volatile Memory and Method for Even/Odd Combined Row Decoding | US | Unfiled | | | | |
| SDA-1560-US | Chih Chin Liao, Cheeman Yu | Corrugated Die Edge for Stacked Die Semiconductor Package | US | Pending | 12825617 | 29-Jun-10 | | |
| SDA-1561-US | Deepanshu Dutta, Jeffrey Lutze | Programming Non-Volatile Memory with Bit Line Voltage Set Up | US | Pending | 12838902 | 19-Jul-10 | | |
| SDA-1562-US | Sergey Anatolievich Gorobets, Robert Young | Techniques for Maintaining Logical to Physical Mapping Information in Non-Volatile Memory Systems | US | Pending | 12821423 | 23-Jun-10 | | |
| SDA-1563-US | Eran Erez, Steven Cheng | Adaptive flash interface | US | Pending | 12835292 | 13-Jul-10 | | |