

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SanDisk Corporation	04/04/2011
RECEIVING PARTY DATA	
Name:	SanDisk Technologies Inc.
Street Address:	6900 North Dallas Parkway
Internal Address:	Two Legacy Town Center
City:	Plano
State/Country:	TEXAS
Postal Code:	75024
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	12894922
CORRESPONDENCE DATA	
Fax Number:	(408)801-9019
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	4088012863
Email:	simona.benjamin@sandisk.com
Correspondent Name:	SanDisk Corporation
Address Line 1:	601 Mccarthy Boulevard
Address Line 2:	Patent Department/Simona Benjamin
Address Line 4:	Milpitas, CALIFORNIA 95035
ATTORNEY DOCKET NUMBER:	SDA-1588-US
NAME OF SUBMITTER:	Simona Benjamin
Total Attachments: 5 source=page 32#page1.tif source=page 32#page2.tif source=page 32#page3.tif source=page 32#page4.tif source=page 32#page5.tif	

CH \$40.00 12894922

ASSIGNMENT

WHEREAS, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

WHEREAS, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

ASSIGNOR further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4th April 2011

Judy Bruner
Name: Judy Bruner
Title: EVP 2nd CFO

STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4th day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner,

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: Averil Watan
Signature of Notary Public

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: 4/4/11

James F. Brelsford
Name: James F. Brelsford
Title: President and Secretary

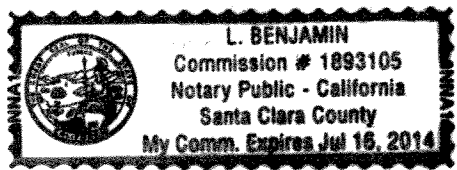
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: L. Benjamin
Signature of Notary Public

Exhibit A

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1564-a-US	Krishnamurthy Dhakshinamurthy, Damian Yurzola, Rajeev Nagabhirava, Oren Sotrasberg	Systems and Methods for Implementing a Programming Sequence to Enhance Die Interleave	US	Pending	12979686	22-Dec-10		
SDA-1565-US	Ben (Chenche) Huang, Albert Chun-Ming Wang, Masaaki Higashitani	Method of Patterning NAND Strings Using Perpendicular SRAF	US	Pending	12897257	4-Oct-10		
SDA-1566-US	Deepanshu Dutta, Jeffrey Lutze	Natural Threshold Voltage Distribution Compaction in Non-Volatile Memory	US	Pending	12849510	3-Aug-10		
SDA-1567-US	Steven Cheng, Dennis Ea, Jianmin Huang, Alexander Mak, Farookh Moogat	Phased NAND Power on Reset	US	Pending	12770358	29-Apr-10		
SDA-1568-US	Sergey Anatolevich Gorobets, Robert Young, Alan Bennett	USE OF GUARD BANDS AND PHASED MAINTENANCE OPERATIONS TO AVOID EXCEEDING MAXIMUM LATENCY REQUIREMENTS IN NON-VOLATILE MEMORY SYSTEMS	US	Pending	12821759	23-Jun-10		
SDA-1570-US	William Wu, Steven T. Sprouse, Sergey Anatolevich Gorobets, Alan Bennett, Ameen Aslam	Method And System For Binary Cache Cleanup	US	Pending	12790960	31-May-10		
SDA-1574-US	Deepanshu Dutta, Jeffrey Lutze, Henry Chin	Multi-Step Channel Boosting to Reduce Channel to Floating Gate Coupling Memory	US	Pending	12894889	30-Sep-10		
SDA-1575-US	Jong Hak Yuh	Wordline Kicking Method when sensing Non-Volatile Storage	US	Pending	12947693	16-Nov-10		
SDA-1576-US	Manuel Antonio D'Abreu, Steve Skala	HYBRID ERROR CORRECTION CODING TO ADDRESS UNCORRECTABLE ERRORS	US	Pending	12913120	27-Oct-10		
SDA-1578P-US	Eliyahou Harari, Tuan D. Pham, Yupin Fong, Vinod Robert Purayath	Non-Volatile Memory with Air Gaps	US	Pending	61354094	11-Jun-10		
SDA-1579P-US	Vinod Robert Purayath, George Matamis, Henry Chien, James Kai, Yuan Zhang	Flat Non-Volatile Memory Cells With Air Gap Isolation	US	Pending	61356600	19-Jun-10		
SDA-1581P-US	Vinod Robert Purayath, Tuan D. Pham, Hiroyuki Kinoshita, Sanghyun Lee, Masaaki Higashitani, Mohan Vamsi Dunga	Air Gap Isolation in Semiconductor Devices	US	Pending	61356630	20-Jun-10		
SDA-1582P-US	Vinod Robert Purayath, George Matamis, Eliyahou Harari	Air Gap Isolation in Non-Volatile Memory	US	Pending	61656603	19-Jun-10		
SDA-1583-US	Jonathan Huynh, Feng Pan, Qui Nguyen, Trung Pham	Level Shifter with Shoot-Through Current Isolation	US	Pending	12895457	30-Sep-10		
SDA-1584-US	Jonathan Huynh, Feng Pan	High Voltage Switch Suitable For Use In Flash Memory	US	Pending	12895476	30-Sep-10		
SDA-1585-US	Jong Hak Yuh	Techniques for the Fast Settling of Word Lines in NAND flash memory	US	Pending	12893611	29-Sep-10		
SDA-1586-US	Chih Chin Liao, Cheeman Yu, Ya Huei Lee	Semiconductor Device with Die Stack Arrangement Including Staggered Die and Efficient Wire Bonding	US	Pending	12844959	28-Jul-10		
SDA-1587-US	Johann Alsmeyer, Vinod Robert Purayath, James Kai, George Matamis	Stacked Metal Fin Cell	US	Pending	12974098	21-Dec-10		
SDA-1588-US	Haibo Li	Sensing for NAND Memory Based on Word Line Position	US	Pending	12894922	30-Sep-10		
SDA-1589-US	Deepanshu Dutta, Jeffrey Lutze	Alternate bit line bias during programming to reduce channel-to-floating gate coupling in memory	US	Pending	12976893	22-Dec-10		