

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SanDisk Corporation	04/04/2011
RECEIVING PARTY DATA	
Name:	SanDisk Technologies Inc.
Street Address:	6900 North Dallas Parkway
Internal Address:	Two Legacy Town Center
City:	Plano
State/Country:	TEXAS
Postal Code:	75024
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	5847425
CORRESPONDENCE DATA	
Fax Number:	(408)801-9019
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	4088012863
Email:	simona.benjamin@sandisk.com
Correspondent Name:	SanDisk Corporation
Address Line 1:	601 Mccarthy Boulevard
Address Line 2:	Patent Department/Simona Benjamin
Address Line 4:	Milpitas, CALIFORNIA 95035
ATTORNEY DOCKET NUMBER:	SDK0011.004US
NAME OF SUBMITTER:	Simona Benjamin
Total Attachments: 5 source=page 34#page1.tif source=page 34#page2.tif source=page 34#page3.tif source=page 34#page4.tif source=page 34#page5.tif	

CH \$40.00 5847425

501534569

PATENT
REEL: 026295 FRAME: 0402

ASSIGNMENT

WHEREAS, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

WHEREAS, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

ASSIGNOR further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4th April 2011

Judy Bruner
Name: Judy Bruner
Title: EVP and CFO

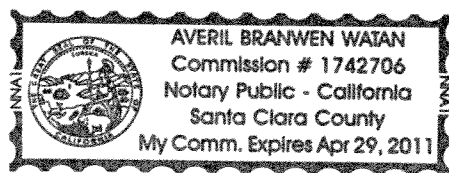
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4th day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: Averil Branwen Watan
Signature of Notary Public

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: 4/4/11

James F. Brelsford
Name: James F. Brelsford
Title: President and Secretary

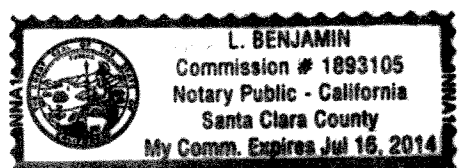
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: L. Benjamin
Signature of Notary Public

Exhibit A

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0001.018US	Eliyahou Harari	Flash EEPROM System Cell Array With Defect Management Including An Error Correction Scheme	US	Issued	8400034	7-Mar-95	5544118	6-Aug-96
SDK0001.019US	Eliyahou Harari	Flash EEPROM System Which Maintains Individual Memory Block Cycle Counts	US	Issued	8468061	6-Jun-95	5568439	22-Oct-96
SDK0001.026US	Eliyahou Harari	Highly Compact EPROM and Flash EEPROM Devices	US	Issued	9190648	12-Nov-98	5963480	5-Oct-99
SDK0004.001US	Sanjay Mehrotra, Eliyahou Harari, Winston Lee	Multi-State EEprom Read and Write Circuits and Techniques	US	Issued	7508273	11-Apr-90	5172338	15-Dec-92
SDK0004.003US	Sanjay Mehrotra, Eliyahou Harari, Winston Lee	Multi-State EEprom Read and Write Circuits and Techniques	US	Issued	7734221	22-Jul-91	5163021	10-Nov-92
SDK0008.000US	Eliyahou Harari, Sanjay Mehrotra	Segmented Column Memory Array	US	Issued	7919715	24-Jul-92	5315541	24-May-94
SDK0009.000US	Stephen J. Gross, Robert Norman	Device and Method for Defect Handling in Semi-Conductor Memory	US	Issued	7422949	17-Oct-89	5200959	6-Apr-93
SDK0011.000US	Jack H. Yuan, George Samachisa, Daniel Guterman, Eliyahou Harari	Dense Vertical Programmable Read Only Memory Cell Structure and Processes for Making Them	US	Issued	7629250	18-Dec-90	5343063	30-Aug-94
SDK0011.003US	Jack H. Yuan, George Samachisa, Daniel Guterman, Eliyahou Harari	Dense Vertical Programmable Read Only Memory Cell Structures and Processes for Making Them	US	Issued	8117219	3-Sep-93	5380672	10-Jan-95
SDK0011.004US	Jack H. Yuan, George Samachisa, Daniel Guterman, Eliyahou Harari	Dense Vertical Programmable Read Only Memory Cell Structures and Processes for Making Them	US	Issued	8341411	17-Nov-94	5847425	8-Dec-98
SDK0011.005US	Jack H. Yuan, George Samachisa, Daniel Guterman, Eliyahou Harari	Method of Making Dense Vertical Programmable Read Only Memory Cell Structure	US	Issued	8423218	17-Apr-95	5512505	30-Apr-96
SDK0011.006US	Jack H. Yuan, George Samachisa, Daniel Guterman, Eliyahou Harari	Dense Vertical Programmable Read Only Memory Cell Structures and Processes for Making Them	US	Issued	8844002	18-Apr-97	5965913	12-Oct-99
SDK0013.000US	Stephen J. Gross, Eliyahou Harari, Daniel Guterman, Sanjay Mehrotra	Method for Optimum Erasing of EEprom	US	Issued	7670246	15-Mar-91	5270979	14-Dec-93
SDK0013.001US	Eliyahou Harari, Daniel Guterman, Sanjay Mehrotra, Stephen J. Gross, John S Mangan	Streamlined Write Operation for EEprom System	US	Issued	8148932	8-Nov-93	5396468	7-Mar-95
SDK0013.002US	Stephen J. Gross, Eliyahou Harari, Daniel Guterman, Sanjay Mehrotra, Robert Norman	Mixed Data Encoding EEprom System	US	Issued	8148930	8-Nov-93	5504760	2-Apr-96
SDK0013.003US	Stephen J. Gross, Eliyahou Harari, Daniel Guterman, Sanjay Mehrotra	Method for Optimum Erasing of EEprom	US	Issued	8149602	8-Nov-93	5369615	29-Nov-94
SDK0014.000US	George Samachisa, Stephen J. Gross, Sanjay Mehrotra, Winston Lee	Latent Defect Handling in EEprom Devices	US	Issued	7948175	21-Sep-92	5428621	27-Jun-95
SDK0014.003US	George Samachisa, Stephen J. Gross, Sanjay Mehrotra, Winston Lee	Latent Defect Handling in EEprom Devices	US	Issued	8494090	23-Jun-95	5659550	19-Aug-97
SDK0015.000US	Jeffrey Donald Stai, Robert Norman, Karl M Lofgren, Anil Gupta, Sanjay Mehrotra	Solid-State Memory System Including Plural Memory Chips And A Serialized Bus	US	Issued	7736733	26-Jul-91	5430859	4-Jul-95
SDK0015.002US	Jeffrey Donald Stai, Robert Norman, Karl M Lofgren, Anil Gupta, Sanjay Mehrotra	Device and Method for Controlling Solid-State Memory System	US	Issued	8931193	16-Sep-97	5806070	8-Sep-98