

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SanDisk Corporation	04/04/2011
RECEIVING PARTY DATA	
Name:	SanDisk Technologies Inc.
Street Address:	6900 North Dallas Parkway
Internal Address:	Two Legacy Town Center
City:	Plano
State/Country:	TEXAS
Postal Code:	75024
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	7491999
CORRESPONDENCE DATA	
Fax Number:	(408)801-9019
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	4088012863
Email:	simona.benjamin@sandisk.com
Correspondent Name:	SanDisk Corporation
Address Line 1:	601 Mccarthy Boulevard
Address Line 2:	Patent Department/Simona Benjamin
Address Line 4:	Milpitas, CALIFORNIA 95035
ATTORNEY DOCKET NUMBER:	SDK0111.003US
NAME OF SUBMITTER:	Simona Benjamin
Total Attachments: 5 source=page 38#page1.tif source=page 38#page2.tif source=page 38#page3.tif source=page 38#page4.tif source=page 38#page5.tif	

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PATENT
REEL: 026301 FRAME: 0983

ASSIGNMENT

WHEREAS, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

WHEREAS, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

ASSIGNOR further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4th April 2011

Judy Bruner
Name: Judy Bruner
Title: EVP and CFO

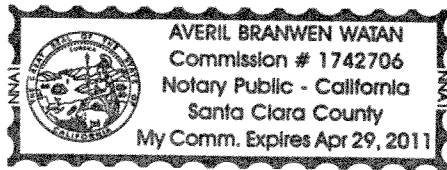
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4th day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: Averil Branwen Watan
Signature of Notary Public

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: 4/4/11

James F. Brelsford
Name: James F. Brelsford
Title: President and Secretary

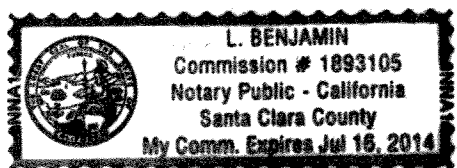
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: L. Benjamin
Signature of Notary Public

Exhibit A

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0106.004US	Robert Wallace, Robert C Miller	Removable Memory Card For Use With Portable Electronic Devices	US	Issued	29141421	4-May-01	D453934	26-Feb-02
SDK0111.000US	Jack H. Yuan, George Samachisa, Elyahou Harari	Floating Gate Memory Cells Utilizing Substrate Trenches To Scale Down Their Size	US	Issued	9860704	18-May-01	6894343	17-May-05
SDK0111.001US	Jack H. Yuan, George Samachisa, Henry Chien, Elyahou Harari	Non-Volatile Memory Cells Utilizing Substrate Trenches	US	Issued	9925134	8-Aug-01	6936887	30-Aug-05
SDK0111.002US	Elyahou Harari, Jack H. Yuan, George Samachisa, Henry Chien	Non-volatile memory cells utilizing substrate trenches	US	Issued	10848242	17-May-04	7087951	8-Aug-06
SDK0111.003US	Elyahou Harari, Jack H. Yuan, George Samachisa, Henry Chien	Non-Volatile Memory Cells Utilizing Substrate Trenches	US	Issued	11423121	8-Jun-06	7491999	17-Feb-09
SDK0112.000US	Robert Wallace	Semiconductor Package Using Terminals Formed on a Conductive Layer of a Circuit Board	US	Issued	9096140	11-Jun-98	6040622	21-Mar-00
SDK0112.001US	Robert Wallace	Semiconductor Package Using Terminals Formed On A Conductive Layer Of A Circuit Board	US	Issued	9487106	19-Jan-00	6410355	25-Jun-02
SDK0112.002US	Robert Wallace	Semiconductor Package Using Terminals Formed On A Conductive Layer Of A Circuit Board	US	Issued	9866022	24-May-01	6867485	15-Mar-05
SDK0112.003US	Robert Wallace	Semiconductor Package Using Terminals Formed On A Conductive Layer Of A Circuit Board	US	Issued	11021169	21-Dec-04	7053483	30-May-06
SDK0123.000US	Robert Wallace, Elyahou Harari	Compact Mechanism for Removable Insertion of Multiple Integrated Circuit Cards into Portable and Other Electronic Devices	US	Issued	9123888	28-Jul-98	5933328	3-Aug-99
SDK0130.000US	Robert Wallace, Robert C Miller	Portable Memory Card for Storage of Personal Information	US	Issued	29109687	19-Aug-99	D442598	22-May-01
SDK0130.001US	Robert Wallace, Robert C Miller	Portable Memory Card For Storage Of Personal Information	US	Issued	29141416	4-May-01	D452246	18-Dec-01
SDK0130.002US	Robert Wallace, Robert C Miller	Portable Memory Card For Storage Of Personal Information	US	Issued	29141407	4-May-01	D452245	18-Dec-01
SDK0130.003US	Robert Wallace, Robert C Miller	Portable Memory Card For Storage Of Personal Information	US	Issued	29141365	4-May-01	D452244	18-Dec-01
SDK0134.000US	Michael Holtzman, Yosi Pinto	Removable Computer With Mass Storage	US	Issued	9924185	2-Aug-01	7418344	26-Aug-08
SDK0134.001US	Michael Holtzman, Yosi Pinto	Removable Computer With Mass Storage	US	Published	12215575	26-Jun-08		
SDK0134.002US	Michael Holtzman, Yosi Pinto	Removable Computer With Mass Storage	US	Published	12215573	26-Jun-08		
SDK0136.000US	George Samachisa, Jack H. Yuan, Elyahou Harari, Daniel Guterman	Dual Floating Gate EEPROM Cell Array with Steering Gates Shared by Adjacent Cells	US	Issued	9343328	30-Jun-99	6151248	21-Nov-00
SDK0136.001US	George Samachisa, Jack H. Yuan, Elyahou Harari, Daniel Guterman	Dual Floating Gate EEPROM Cell Array with Steering Gates Shared by Adjacent Cells	US	Issued	9634694	8-Aug-00	6266278	24-Jul-01
SDK0137.000US	George Samachisa, Yupin Fong	Method and Structure for Programming Floating Gate Memory Cells	US	Issued	7658098	20-Feb-91	5272669	21-Dec-93
SDK0138.000US	George Samachisa, Daniel Guterman, Yupin Fong, Elyahou Harari	Eeprom With Split Gate Source Side Injection	US	Issued	7820364	14-Jan-92	5313421	17-May-94
SDK0138.001US	Daniel Guterman, George Samachisa, Yupin Fong, Elyahou Harari	Method for forming EEPROM with split gate source side injection	US	Issued	8193707	9-Feb-94	5776810	7-Jul-98
SDK0138.003US	George Samachisa, Daniel Guterman, Elyahou Harari, Yupin Fong	Eeprom With Split Gate Source Side Injection	US	Issued	8639128	26-Apr-96	5847996	8-Dec-98