

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SanDisk Corporation	04/04/2011
RECEIVING PARTY DATA	
Name:	SanDisk Technologies Inc.
Street Address:	6900 North Dallas Parkway
Internal Address:	Two Legacy Town Center
City:	Plano
State/Country:	TEXAS
Postal Code:	75024
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	6747892
CORRESPONDENCE DATA	
Fax Number:	(408)801-8512
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	4088012863
Email:	simona.benjamin@sandisk.com
Correspondent Name:	SanDisk Corporation
Address Line 1:	601 Mccarthy Boulevard
Address Line 2:	Patent Department/Simona Benjamin
Address Line 4:	Milpitas, CALIFORNIA 95035
ATTORNEY DOCKET NUMBER:	SDK0210.000US
NAME OF SUBMITTER:	Simona Benjamin
Total Attachments: 5 source=page 40#page1.tif source=page 40#page2.tif source=page 40#page3.tif source=page 40#page4.tif source=page 40#page5.tif	

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PATENT
REEL: 026311 FRAME: 0534

ASSIGNMENT

WHEREAS, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

WHEREAS, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

ASSIGNOR further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4th April 2011

Judy Bruner
Name: Judy Bruner
Title: EVP and CFO

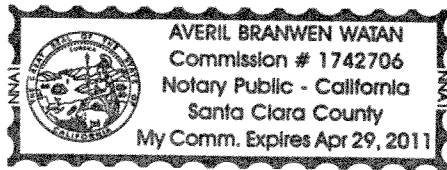
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4th day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: Averil Branwen Watan
Signature of Notary Public

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: 4/4/11

James F. Brelsford
Name: James F. Brelsford
Title: President and Secretary

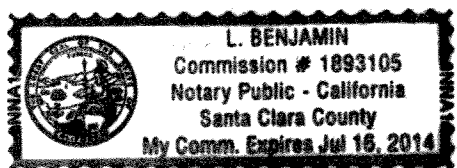
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: L. Benjamin
Signature of Notary Public

Exhibit A

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0149.000US	Robert Wallace, Wesley G. Brewer, Yosi Pinto	Use Of Small Electronic Circuit Cards With Different Interfaces In An Electronic System	US	Issued	9633089	4-Aug-00	6824063	30-Nov-04
SDK0149.001US	Robert Wallace, Wesley G. Brewer, Yosi Pinto	Use of small electronic circuit cards with different interfaces in an electronic system	US	Issued	10886309	6-Jul-04	7090124	15-Aug-06
SDK0149.002US	Robert Wallace, Wesley G. Brewer, Yosi Pinto	Use Of Small Electronic Circuit Cards With Different Interfaces In An Electronic System	US	Issued	11383928	17-May-06	7350714	1-Apr-08
SDK0154.001US	Robert Wallace	Integrated Circuit Memory Card	US	Issued	29124606	7-Jun-00	D452243	18-Dec-01
SDK0154.002US	Robert Wallace	Memory Card Electrical Contact Structure	US	Issued	9590029	7-Jun-00	6634561	21-Oct-03
SDK0154.003US	Robert Wallace	Memory Card Electrical Contact Structure	US	Issued	10639051	11-Aug-03	7547234	16-Jun-09
SDK0155.000US	Raul-Adrian Cernea, George Samachisa	Memory Array Architecture Utilizing Global BIT Lines Shared by Multiple Cells	US	Issued	9370775	9-Aug-99	6091633	18-Jul-00
SDK0157.000US	Raul-Adrian Cernea	Source Controlled Operation Of Non-Volatile Memories	US	Issued	10600988	20-Jun-03	6950348	27-Sep-05
SDK0157.001US	Raul-Adrian Cernea	Source Controlled Operation Of Non-Volatile Memories	US	Issued	11201000	9-Aug-05	7173860	6-Feb-07
SDK0164.000US	Leon Malned, Anthony J. Knights, Johnny H. Chen, Daniel Auclair	Updated ImageMate Design	US	Issued	29114371	22-Nov-99	D439579	27-Mar-01
SDK0207.000US	Khandker N Quader, Sharon Huynh	Multiple Output Current Mirror with Improved Accuracy	US	Issued	9592469	9-Jun-00	6285615	4-Sep-01
SDK0207.001US	Khandker N Quader, Sharon Huynh	Multiple Output Current Mirror With Improved Accuracy	US	Issued	9912136	24-Jul-01	6396757	28-May-02
SDK0208.000US	Raul-Adrian Cernea, Khandker N Quader, Sanjay Mehrotra	EEPROM Memory Chip with Multiple Use Pinouts	US	Issued	9591090	9-Jun-00	6282130	28-Aug-01
SDK0209.000US	Geoffrey S Gongwer, Shahzad Khalid, Daniel Guterman, Kevin Conley, Richard T. Simko	Writable Tracking Cells	US	Issued	9671793	27-Sep-00	6538922	25-Mar-03
SDK0209.001US	Richard T. Simko, Kevin Conley, Shahzad Khalid, Daniel Guterman, Geoffrey S Gongwer	Sense Amplifier Suitable For Analogue Voltage Levels	US	Issued	10350874	24-Jan-03	6714449	30-Mar-04
SDK0209.002US	Richard T. Simko, Kevin Conley, Shahzad Khalid, Daniel Guterman, Geoffrey S Gongwer	Writable Tracking Cells	US	Issued	10692922	23-Oct-03	6873549	29-Mar-05
SDK0209.003US	Richard T. Simko, Kevin Conley, Shahzad Khalid, Daniel Guterman, Geoffrey S Gongwer	Writable Tracking Cells	US	Issued	11064529	22-Feb-05	7301807	27-Nov-07
SDK0210.000US	Shahzad Khalid	Sense Amplifier For Multilevel Non-Volatile Integrated Memory Devices	US	Issued	9989996	20-Nov-01	6747892	8-Jun-04
SDK0211.000US	Geoffrey S Gongwer, Daniel Guterman	Non-Volatile Memory With Improved Programming And Method Therefor	US	Issued	9793370	26-Feb-01	6738289	18-May-04
SDK0211.002US	Geoffrey S Gongwer, Daniel Guterman	Non-Volatile Memory With Improved Programming And Method Therefor	US	Issued	11126044	9-May-05	7139465	21-Nov-06
SDK0212.000US	Raul-Adrian Cernea	Background Erase For Memory Cells	US	Issued	9956201	17-Sep-01	6741502	25-May-04
SDK0212.001US	Raul-Adrian Cernea	Background Operation for Memory Cells	US	Issued	10677349	1-Oct-03	6845045	18-Jan-05
SDK0212.002US	Raul-Adrian Cernea	Latched Programming of Memory and Method	US	Issued	10842941	10-May-04	7177197	13-Feb-07
SDK0212.003US	Raul-Adrian Cernea	NAND Flash Memory With A Programming Voltage Held Dynamically In A NAND Chain Channel Region	US	Issued	11621190	9-Jan-07	7660156	9-Feb-10