PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
SanDisk Corporation	04/04/2011

RECEIVING PARTY DATA

Name:	SanDisk Technologies Inc.
Street Address:	6900 North Dallas Parkway
Internal Address:	Two Legacy Town Center
City:	Plano
State/Country:	TEXAS
Postal Code:	75024

PROPERTY NUMBERS Total: 1

Property Type	Number
Patent Number:	7234036

CORRESPONDENCE DATA

Fax Number: (408)801-8512

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 4088012863
Email: simona.benjamin@sandisk.com
Correspondent Name: SanDisk Corporation
Address Line 1: 601 Mccarthy Boulevard
Address Line 2: Patent Department/Simona Benjamin
Address Line 4: Milpitas, CALIFORNIA 95035

ATTORNEY DOCKET NUMBER:	SDK0416.000US
NAME OF SUBMITTER:	Simona Benjamin

Total Attachments: 5
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ASSIGNMENT

WHEREAS, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached <u>Exhibit A</u>, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

WHEREAS, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, ASSIGNOR hereby assigns, transfers and conveys unto the said ASSIGNEE, and ASSIGNEE accepts, all of ASSIGNOR's right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and ASSIGNOR hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to ASSIGNEE under this ASSIGNMENT, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

ASSIGNOR further covenants that ASSIGNEE will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to ASSIGNOR and that ASSIGNOR will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to ASSIGNEE or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

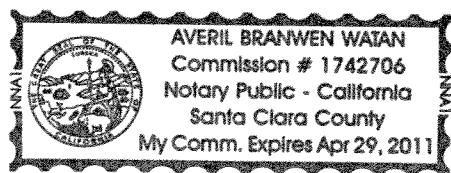Dated: _4ᵗʰ April 2011_

Name: _Judy Bruner_
Title: _EVP and CFO_

STATE OF _California_)
) ss.
COUNTY OF _Santa Clara_)

On this _4ᵗʰ_ day of _April_, 2011, before me _Averil Branwen Watan (notary public)_, the undersigned Notary Public, personally appeared _Judy Bruner_,

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.

Signature: _____
Signature of Notary Public

AVERIL BRANWEN WATAN
Commission # 1742706
Notary Public - California
Santa Clara County
My Comm. Expires Apr 29, 2011

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: _4/4/11_

Name: _James F. Brelsford_
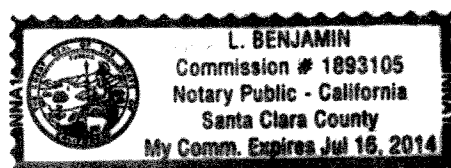Title: _President and Secretary_

STATE OF _California_)
) ss.
COUNTY OF _Santa Clara_)

On this _4_ day of _April_, 2011, before me _L. Benjamin_, the undersigned Notary Public, personally appeared _JAMES F. BRELSFORD_,

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.

Signature: _____
Signature of Notary Public

L. BENJAMIN
Commission # 1893105
Notary Public - California
Santa Clara County
My Comm. Expires Jul 16, 2014

Exhibit A

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0411.001US	Jian Chen, Masaaki Higashitani	Use of Voids Between Elements in Semiconductor Structures for Isolation	US	Issued	11210218	22-Aug-05	7569465	4-Aug-09
SDK0413.000US	Raul-Adrian Cernea	Highly Compact Non-Volatile Memory and Method Therefor with Space Efficient	US	Issued	10254221	24-Sep-02	6940753	6-Sep-05
SDK0414.000US	Raul-Adrian Cernea, Yan Li	Non-Volatile Memory And Method With Reduced Bit Line Crosstalk Errors	US	Issued	10254898	24-Sep-02	7443757	28-Oct-08
SDK0415.000US	Raul-Adrian Cernea, Yan Li	Non-Volatile Memory And Method With Reduced Neighboring Field Errors	US	Issued	10254290	24-Sep-02	6987693	17-Jan-06
SDK0415.001US	Raul-Adrian Cernea, Yan Li	Non-Volatile Memory And Method With Reduced Neighboring Field Errors	US	Issued	11235985	26-Sep-05	7239551	3-Jul-07
SDK0415.003US	Raul-Adrian Cernea, Yan Li	Non-Volatile Memory And Method With Reduced Neighboring Field Errors	US	Published	12749292	29-Mar-10		
SDK0416.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Method and Apparatus for Resolving Physical Blocks Associated with a Common Logical Block	US	Issued	10281762	28-Oct-02	7234036	19-Jun-07
SDK0417.000US	Yosi Pinto, Aviad Zer, Amir Tsuri, Asher Druck	Efficient Connection Between Modules Of Removable Electronic Circuit Cards	US	Issued	10732149	9-Dec-03	7209995	24-Apr-07
SDK0417.001US	Yosi Pinto, Aviad Zer, Amir Tsuri, Asher Druck	Efficient Connection Between Modules Of Removable Electronic Circuit Cards	US	Issued	11303220	15-Dec-05	7467249	16-Dec-08
SDK0420.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Maintaining Erase Counts in Non-Volatile Storage Systems	US	Issued	10281696	28-Oct-02	6831865	14-Dec-04
SDK0422.000US	Feng Pan	High Voltage Ripple Reduction	US	Issued	10328686	23-Dec-02	6734718	11-May-04
SDK0422.001US	Feng Pan	High boltage ripple reduction and substrate protection	US	Issued	10811074	26-Mar-04	7116155	3-Oct-06
SDK0428.000US	Eliyahou Harari, George Samachisa	Memory cell arrays having dual control gtates per memory cell charge storage element	US	Allowed	11279725	13-Apr-06		
SDK0431.000US	Khandker N Quader, Raul-Adrian Cernea	Erase Inhibit In Non-Volatile Memories	US	Issued	10671847	25-Sep-03	6958936	25-Oct-05
SDK0431.001US	Khandker N Quader, Raul-Adrian Cernea	Erase Inhibit In Non-Volatile Memories	US	Issued	11223055	8-Sep-05	7379346	27-May-08
SDK0432.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi, Ping Li	Method and Apparatus for Performing Block Caching in a Non-Volatile Memory System	US	Issued	10678672	2-Oct-03	7174440	6-Feb-07
SDK0434.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Hybrid Mapping Implementation with a Non-Volatile Memory System	US	Issued	10676652	1-Oct-03	7188228	6-Mar-07
SDK0434.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Hybrid Mapping Implementation with a Non-Volatile Memory System	US	Issued	11617441	28-Dec-06	7702880	20-Apr-10
SDK0435.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Method and Apparatus for Efficiently Enabling and Out of Sequence Write Process within a Non-Volatile Memory System	US	Issued	10679008	3-Oct-03	7526599	28-Apr-09
SDK0436.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Unusable Block Management within a Non-Volatile Memory System	US	Issued	10682641	9-Oct-03	7171536	30-Jan-07
SDK0437.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi, Sergy Yudin	Power Management Block for Use in a Non-Volatile Memory System	US	Issued	10685624	14-Oct-03	7181611	20-Feb-07
SDK0437.002US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi, Sergy Yudin	Power Management Block for Use in a Non-Volatile Memory System	US	Issued	11616687	27-Dec-06	7809962	5-Oct-10
SDK0438.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Method and Apparatus for Managing the Integrity of Data in Non-Volatile Memory	US	Pending	10679000	2-Oct-03		
SDK0439.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Hybrid Implementation for Error Correction Codes with a Non-Volatile Memory System	US	Pending	10678893	2-Oct-03		

PATENT