

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SanDisk Corporation	04/04/2011
RECEIVING PARTY DATA	
Name:	SanDisk Technologies Inc.
Street Address:	6900 North Dallas Parkway
Internal Address:	Two Legacy Town Center
City:	Plano
State/Country:	TEXAS
Postal Code:	75024
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	12607444
CORRESPONDENCE DATA	
Fax Number:	(408)801-8512
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	4088012863
Email:	simona.benjamin@sandisk.com
Correspondent Name:	SanDisk Corporation
Address Line 1:	601 Mccarthy Boulevard
Address Line 2:	Patent Department/Simona Benjamin
Address Line 4:	Milpitas, CALIFORNIA 95035
ATTORNEY DOCKET NUMBER:	SDK0453.006US
NAME OF SUBMITTER:	Simona Benjamin
Total Attachments: 5 source=page 52#page1.tif source=page 52#page2.tif source=page 52#page3.tif source=page 52#page4.tif source=page 52#page5.tif	

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PATENT
REEL: 026336 FRAME: 0317

ASSIGNMENT

WHEREAS, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

WHEREAS, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

ASSIGNOR further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4th April 2011

Judy Bruner
Name: Judy Bruner
Title: EVP and CFO

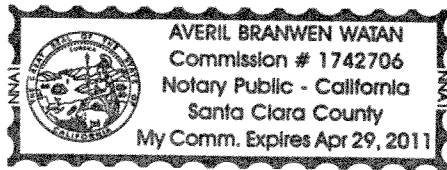
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4th day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: Averil Branwen Watan
Signature of Notary Public

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: 4/4/11

James F. Brelsford
Name: James F. Brelsford
Title: President and Secretary

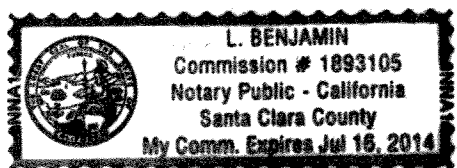
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD.

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: L. Benjamin
Signature of Notary Public

Exhibit A

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0442.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Dynamic Redundant Area Configuration in a Non-Volatile Memory System	US	Issued	10676407	1-Oct-03	7559004	7-Jul-09
SDK0443.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Adaptive Internal Table Back-up for Non-Volatile Memory System	US	Pending	10696685	28-Oct-03		
SDK0444.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Erase Count Differential Table within a Non-Volatile Memory System	US	Issued	10696228	28-Oct-03	7032087	18-Apr-06
SDK0445.000US	Robert C Chang, Farshid Sabet-Sharghi, Bahman Qawami	Internal Maintenance Schedule Request for Non-Volatile Memory System	US	Issued	10696229	28-Oct-03	7089349	8-Aug-06
SDK0446.000US	Soochok Kee	Integrated Circuit Package having Stacked Integrated Circuits and Method Therefor	US	Issued	10463742	16-Jun-03	7309923	18-Dec-07
SDK0449.000US	Jeffrey Lutze, Tuan D. Pham, Henry Chien, George Matamis	Self-aligned non-volatile memory cell and process for fabrication	US	Issued	10600259	30-Jun-03	7105406	12-Sep-06
SDK0449.001US	Jeffrey Lutze, Tuan D. Pham, Henry Chien, George Matamis	Self-aligned non-volatile memory cell	US	Issued	11469727	1-Sep-06	7504686	17-Mar-09
SDK0450.000US	Ron Barzilai, Reuven Elhamias	Multi-Purpose Non-Volatile Memory Card	US	Issued	10886302	6-Jul-04	7554842	30-Jun-09
SDK0450.001US	Ron Barzilai, Reuven Elhamias	Multi-Purpose Non-Volatile Memory Card	US	Pending	11821564	21-Jun-07		
SDK0453.000US	Eliyahou Harari	Flash Memory Cell Arrays Having Dual Control Gates Per Memory Cell Charge Storage Element	US	Issued	10282747	28-Oct-02	6888755	3-May-05
SDK0453.001US	Eliyahou Harari	Flash Memory Cell Arrays Having Dual Control Gates	US	Issued	10791486	1-Mar-04	7075823	11-Jul-06
SDK0453.002US	Eliyahou Harari	Flash memory cell arrays having dual control gates per memory cell charge storage element	US	Issued	11379004	17-Apr-06	7502261	10-Mar-09
SDK0453.003US	Eliyahou Harari	Flash memory cell arrays having dual control gates per memory cell charge storage element	US	Issued	11379012	17-Apr-06	7303956	4-Dec-07
SDK0453.004US	Eliyahou Harari	Flash memory cell arrays having dual control gates per memory cell charge storage element	US	Issued	11379019	17-Apr-06	7486555	3-Feb-09
SDK0453.005US	Eliyahou Harari	Flash Memory Cell Arrays Having Dual Control Gates Per Memory Cell Charge Storage Element	US	Issued	11422016	2-Jun-06	7638834	29-Dec-09
SDK0453.006US	Eliyahou Harari	Flash Memory Cell Arrays Having Dual Control Gates Per Memory Cell Charge Storage Element	US	Published	12607444	28-Oct-09		
SDK0454.000US	Kevin Conley, Yoram Cedar	System and Method For Use of On-Chip Non-Volatile Memory Write Cache	US	Issued	11021200	21-Dec-04	7882299	1-Feb-11
SDK0455.000US	Sergey Anatolievich Gorobets, Alan Bennett, Peter John Smith, Alan Welsh Sinclair, Kevin Conley, Philip D. Royall	Cyclic Flash Memory Wear Leveling	US	Issued	10990189	15-Nov-04	7441067	21-Oct-08
SDK0456.000US	Raul-Adrian Cernea	Highly Compact Non-Volatile Memory And Method Therefor With Internal Serial Buses	US	Issued	10254919	24-Sep-02	6891753	10-May-05
SDK0456.001US	Raul-Adrian Cernea	Highly Compact Non-Volatile Memory and Method Therefor with Internal Serial Buses	US	Issued	11122738	5-May-05	7085159	1-Aug-06
SDK0456.002US	Raul-Adrian Cernea	Highly Compact Non-Volatile Memory And Method Therefor With Internal Serial Buses	US	Issued	11422719	7-Jun-06	7447070	4-Nov-08
SDK0457.000US	Jeffrey Lutze, Jian Chen, Yan Li, Masaaki Higashitani	SOURCE SIDE SELF BOOSTING TECHNIQUE FOR NON-VOLATILE MEMORY	US	Issued	10379608	5-Mar-03	6859397	22-Feb-05
SDK0457.001US	Jeffrey Lutze, Jian Chen, Yan Li, Masaaki Higashitani	SELF BOOSTING TECHNIQUE	US	Issued	11049802	3-Feb-05	6975537	13-Dec-05
SDK0458.000US	Yan Li, Mehrdad Mofidi, Shahzad Khalid, Raul-Adrian Cernea	Non-Volatile Memory and Method with Bit Line to Bit Line Coupled Compensation	US	Issued	10667222	17-Sep-03	7064980	20-Jun-06
SDK0458.001US	Raul-Adrian Cernea, Mehrdad Mofidi, Yan Li, Shahzad Khalid	Non-Volatile Memory And Method With Bit Line To Bit Line Coupled Compensation	US	Issued	11422034	2-Jun-06	7269069	11-Sep-07