

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SanDisk Corporation	04/04/2011
RECEIVING PARTY DATA	
Name:	SanDisk Technologies Inc.
Street Address:	6900 North Dallas Parkway
Internal Address:	Two Legacy Town Center
City:	Plano
State/Country:	TEXAS
Postal Code:	75024
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	7447821
CORRESPONDENCE DATA	
Fax Number:	(408)801-8512
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	4088012863
Email:	simona.benjamin@sandisk.com
Correspondent Name:	SanDisk Corporation
Address Line 1:	601 Mccarthy Boulevard
Address Line 2:	Patent Department/Simona Benjamin
Address Line 4:	Milpitas, CALIFORNIA 95035
ATTORNEY DOCKET NUMBER:	SDK0755.000US
NAME OF SUBMITTER:	Simona Benjamin
Total Attachments: 5 source=Assignment 72#page1.tif source=Assignment 72#page2.tif source=Assignment 72#page3.tif source=Assignment 72#page4.tif source=Assignment 72#page5.tif	

CH \$40.00 7447821

ASSIGNMENT

WHEREAS, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

WHEREAS, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

ASSIGNOR further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4th April 2011

Judy Bruner
Name: Judy Bruner
Title: EVP 2nd CFO

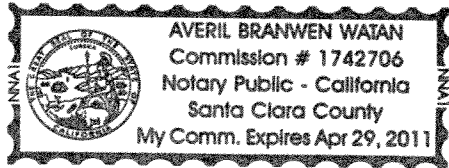
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4th day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner,

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: Averil Watan
Signature of Notary Public

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: 4/4/11

James F. Brelsford
Name: James F. Brelsford
Title: President and Secretary

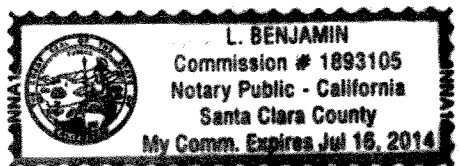
STATE OF California)
COUNTY OF Santa Clara) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: L. Benjamin
Signature of Notary Public

Exhibit A

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0747.000US	Yupin Fong, Jun Wan, Jeffrey Lutze	REDUCING READ DISTURB FOR NON-VOLATILE STORAGE	US	Issued	11295776	6-Dec-05	7349258	25-Mar-08
SDK0747.001US	Yupin Fong, Jun Wan, Jeffrey Lutze	SYSTEM FOR REDUCING READ DISTURB FOR NON-VOLATILE STORAGE	US	Issued	11296087	6-Dec-05	7262994	28-Aug-07
SDK0747.002US	Yupin Fong, Jun Wan, Jeffrey Lutze	SYSTEM FOR REDUCING READ DISTURB FOR NON-VOLATILE STORAGE	US	Issued	11681188	2-Mar-07	7295473	13-Nov-07
SDK0747.003US	Yupin Fong, Jun Wan, Jeffrey Lutze	REDUCING READ DISTURB FOR NON-VOLATILE STORAGE	US	Issued	12021729	29-Jan-08	7440318	21-Oct-08
SDK0747.004US	Yupin Fong, Jun Wan, Jeffrey Lutze	REDUCING READ DISTURB FOR NON-VOLATILE STORAGE	US	Issued	12021741	29-Jan-08	7447065	4-Nov-08
SDK0747.005US	Yupin Fong, Jun Wan, Jeffrey Lutze	REDUCING READ DISTURB FOR NON-VOLATILE STORAGE	US	Issued	12021761	29-Jan-08	7495956	24-Feb-09
SDK0750.000US	Loc Tu, Jeffrey Lutze, Jun Wan, Jian Chen	Timing of Analog Voltage in Flash Memory Devices	US	Issued	11331479	12-Jan-06	7457178	25-Nov-08
SDK0750.001US	Loc Tu, Jeffrey Lutze, Jun Wan, Jian Chen	Flash Memory Devices with Trimmed Analog Voltages	US	Issued	11332567	12-Jan-06	7254071	7-Aug-07
SDK0752.000US	Farookh Moogat, Raul-Adrian Cernea, Shouchang Tsao, Tomoharu Tanaka	Method for column redundancy using data latches in solid-state memories	US	Issued	11389655	24-Mar-06	7394690	1-Jul-08
SDK0752.001US	Farookh Moogat, Raul-Adrian Cernea, Shouchang Tsao, Tomoharu Tanaka	Non-Volatile Memory with Redundancy Data Buffered in Data Latches for Defective Locations	US	Issued	11388408	24-Mar-06	7224605	29-May-07
SDK0752.002US	Farookh Moogat, Raul-Adrian Cernea, Shouchang Tsao, Tomoharu Tanaka	Method for column redundancy using data latches in solid-state memories	US	Issued	12163017	27-Jun-08	7663950	16-Feb-10
SDK0753.000US	Yan Li, Fanglin Zhang, Toru Miwa, Farookh Moogat	Variable program voltage increment values in non-volatile memory program operations	US	Issued	11548264	10-Oct-06	7474561	6-Jan-09
SDK0753.001US	Yan Li, Fanglin Zhang, Toru Miwa, Farookh Moogat	Systems utilizing variable program voltage increment values in non-volatile memory program operations	US	Issued	11548267	10-Oct-06	7450426	11-Nov-08
SDK0754.000US	Vincent (Ming Hsun) Lee, Cheeman Yu, Hem Takiar	COL-TSOP WITH NON-CONDUCTIVE MATERIAL FOR REDUCING PACKAGE CAPACITANCE	US	Issued	11354441	15-Feb-06	7728411	1-Jun-10
SDK0755.000US	Joerg Ferchau, Carlos J. Gonzalez	U3 Adapter	US	Issued	11408714	21-Apr-06	7447821	4-Nov-08
SDK0755.001US	Joerg Ferchau, Carlos J. Gonzalez	Method for U3 Adapter	US	Issued	11408553	21-Apr-06	7516261	7-Apr-09
SDK0756.000US	Hem Takiar, Warren Middlekauf, Robert C Miller	SIP Module with a Single Sided Lid	US	Issued	11356279	15-Feb-06	7193161	20-Mar-07
SDK0757.000US	Fabrice Jogand-Coulomb	Personal License Server and Methods for Use Thereof	US	Published	12070689	19-Feb-08		
SDK0758.000US	Henry Chien, George Matamis, Tuan D. Pham, Masaaki Higashitani, Hidetaka Horiuchi, Jeffrey Lutze, Nima Mokhlesji, Yupin Fong	Methods of reducing coupling between floating gates in non-volatile memory	US	Issued	11534135	21-Sep-06	7615445	10-Nov-09
SDK0758.002US	Henry Chien, George Matamis, Tuan D. Pham, Masaaki Higashitani, Hidetaka Horiuchi, Jeffrey Lutze, Nima Mokhlesji, Yupin Fong	Method of reducing coupling between floating gates in non-volatile memory	US	Issued	12567257	25-Sep-09	7910434	22-Mar-11
SDK0758.003US	Henry Chien, George Matamis, Tuan D. Pham, Masaaki Higashitani, Hidetaka Horiuchi, Jeffrey Lutze, Nima Mokhlesji, Yupin Fong	Method of reducing coupling between floating gates in non-volatile memory	US	Pending	13010563	20-Jan-11		