

PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
FAIRCHILD SEMICONDUCTOR CORPORATION	06/24/2011
RECEIVING PARTY DATA	
Name:	RPX CORPORATION
Street Address:	ONE MARKET PLAZA, STEUART TOWER, SUITE 700
City:	SAN FRANCISCO
State/Country:	CALIFORNIA
Postal Code:	94105
PROPERTY NUMBERS Total: 28	
Property Type	Number
Patent Number:	6208644
Patent Number:	6249528
Patent Number:	5884101
Patent Number:	6151644
Patent Number:	6507581
Patent Number:	6212194
Patent Number:	6510138
Patent Number:	6501761
Patent Number:	5465056
Patent Number:	5790048
Patent Number:	5717871
Patent Number:	5710550
Patent Number:	5754791
Patent Number:	5940596
Patent Number:	5784003

501592958

PATENT
REEL: 026582 FRAME: 0631

CH \$1120.00 6208644

Patent Number:	5781717
Patent Number:	6356111
Patent Number:	6816057
Patent Number:	5428800
Patent Number:	5282271
Patent Number:	5428750
Patent Number:	5426738
Patent Number:	5530814
Patent Number:	5559971
Patent Number:	5625780
Patent Number:	5734334
Patent Number:	6636483
Patent Number:	6771162

CORRESPONDENCE DATA

Fax Number: (972)479-0464

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 9724790462

Email: BETH@DALPAT.COM

Correspondent Name: GREGORY M. HOWISON

Address Line 1: P.O. BOX 741715

Address Line 4: DALLAS, TEXAS 75374-1715

ATTORNEY DOCKET NUMBER:	RPXC-29189-FAIRCHILD
-------------------------	----------------------

NAME OF SUBMITTER:	GREGORY M. HOWISON
--------------------	--------------------

Total Attachments: 9

source=RPX29189 EXE FAIRCHILD ASSMT#page1.tif
source=RPX29189 EXE FAIRCHILD ASSMT#page2.tif
source=RPX29189 EXE FAIRCHILD ASSMT#page3.tif
source=RPX29189 EXE FAIRCHILD ASSMT#page4.tif
source=RPX29189 EXE FAIRCHILD ASSMT#page5.tif
source=RPX29189 EXE FAIRCHILD ASSMT#page6.tif
source=RPX29189 EXE FAIRCHILD ASSMT#page7.tif
source=RPX29189 EXE FAIRCHILD ASSMT#page8.tif
source=RPX29189 EXE FAIRCHILD ASSMT#page9.tif

EXHIBIT A

Patent Assignment

This patent assignment ("Assignment") is entered into as of June 22, 2011 by and between Fairchild Semiconductor Corporation, a Delaware corporation, with principal place of business at 82 Running Hill Road, South Portland, ME 04106 ("Seller"), and RPX Corporation, a Delaware corporation, with principal place of business at One Market Plaza, Steuart Tower, Suite 700, San Francisco, CA 94105 ("Buyer")

For good and valuable consideration, the receipt of which is hereby acknowledged, Seller hereby:

(A) irrevocably assigns to Buyer:

(1) the entire right, title, and interest, everywhere in the world, to

(a) the issued patents and pending patent applications set forth on Schedule 1,

(b) any patent applications and/or patents that may claim priority of the items in (a) (including but not limited to continuations, divisions, substitutes, reissues, reexaminations, or extensions thereof, together with all priority rights and foreign counterpart applications under any existing or future international patent conventions, agreements, or treaties),

(c) any patent that may be granted on any patent application in (a) or (b), and

(d) any other rights in the inventions described in any of (a), (b) or (c) including rights to future patent applications and all rights of cooperation assigned or granted by a third party;

(all of the foregoing in (1), collectively, the "Assigned Patents");

(2) the right to sue third parties for infringement (including but not limited to past, present and future infringement, damages and injunctive relief) of any of the Assigned Patents based on activities occurring prior to the execution date hereof or hereafter; and

(3) any current or future right to receive royalties based on any of the foregoing in (1) or (2);
and

(B) agrees upon request (and at the expense) of Buyer to, and if Seller is unable or unwilling to do so authorizes Buyer to act in Seller's name to: execute all oaths, assignments, powers, and any other papers necessary to perform Seller's obligations hereunder, testify in any proceeding, and otherwise take any action, and fully cooperate with Buyer to perform Seller's obligations hereunder, in each case, related to securing and enforcing Buyer's rights related to this Assignment.

The terms and conditions of this Assignment will inure to the benefit of Buyer, its successors, assigns, and other legal representatives and will be binding upon Seller, its successors, assigns, and other legal representatives.

Seller:

Fairchild Semiconductor Corporation

By: 

Name: PAUL D. DELVA

Title: SR. V.P. + GENERAL COUNSEL

Date: 6/24/11

Notary Seal:



X Known to me and signed,
Sealed this 24th Day of June
2011.



KIMBERLEY A. GROVER

Notary Public, Maine

My Commission Expires June 28, 2018

SCHEDULE 1

PATENTS AND PATENT APPLICATIONS

Patents

Patent No.	Issue Date	Application Serial No.	Filing Date	Title
US6208644	03/27/2001	09/041,823	03/12/1998	Network switch providing dynamic load balancing
US6249528	06/19/2001	09/041,826	03/12/1998	Network switch providing per virtual channel queuing for segmentation and reassembly
US5884101	03/16/1999	09/062,247	04/17/1998	Apparatus for detecting data buffer faults
US6151644	11/21/2000	09/062,446	04/17/1998	Dynamically configurable buffer for a computer network
US6507581	01/14/2003	09/097,177	06/12/1998	Dynamic port mode selection for crosspoint switch
US6212194	04/03/2001	09/129,982	08/05/1998	Network routing switch with non-blocking arbitration system
US6510138	01/21/2003	09/258,181	02/25/1999	Network switch with head of line input buffer queue clearing
US6501761	12/31/2002	09/258,183	02/25/1999	Modular network switch with peer-to-peer address mapping communication
US5465056	11/07/1995	08/333,524	11/02/1994	Apparatus for programmable circuit and signal switching
US5790048	08/04/1998	08/961,545	10/30/1997	Crosspoint switch with bank-switched memory
EP0789950 B1	01/16/2002	95939027.9	10/31/1995	Apparatus for programmable circuit and signal switching
GB0789950	01/16/2002	EP95939027.9	10/31/1995	Apparatus for programmable circuit and signal switching
US5717871	02/10/1998	08/516,319	08/17/1995	Crossbar switch with input/output buffers having multiplexed control inputs
US5710550	01/20/1998	08/516,322	08/17/1995	Apparatus for programmable signal switching
US5754791	05/19/1998	08/622,764	03/25/1996	Hierarchical address translation system for a network switch
US5940596	08/17/1999	08/905,440	08/04/1997	Clustered address caching system for a network switch
	10/02/1997	PCT/US97/04258	03/18/1997	Hierarchical address translation system for a network switch
US5784003	07/21/1998	08/624,686	03/25/1996	Network switch with broadcast support
EP0885498 B1	05/09/2007	97916040.5	03/18/1997	Network switch with broadcast support
FR0885498	05/09/2007	EP97916040.5	03/18/1997	Network switch with broadcast

				support
GB0885498	05/09/2007	EP97916040.5	03/18/1997	Network switch with broadcast support
US5781717	07/14/1998	08/710,571	09/19/1996	Dynamic spare column replacement memory system
US6356111	03/12/2002	09/735,430	12/12/2000	Crosspoint switch array with broadcast and implied disconnect operating modes
US6816057	11/09/2004	09/766,010	01/19/2001	Segmented crosspoint switch array
US5428800	06/27/1995	07/960,965	10/13/1992	Input/output (i/o) bidirectional buffer for interfacing i/o ports of a field programmable interconnection device with array ports of a cross-point switch
US5282271	01/25/1994	07/912,975	07/06/1992	I/o buffering system to a programmable switching apparatus
DE0610426	08/25/1999	19926029873T	10/29/1992	Sampling buffer for field programmable interconnect device
EP0610426 B1	08/25/1999	92924152.9	10/29/1992	Sampling buffer for field programmable interconnect device
FR0610426	08/25/1999	EP92924152.9	10/29/1992	Sampling buffer for field programmable interconnect device
GB0610426	08/25/1999	EP92924152.9	10/29/1992	Sampling buffer for field programmable interconnect device
NL0610426	08/25/1999	EP92924152.9	10/29/1992	Sampling buffer for field programmable interconnect device
SE0610426	08/25/1999	EP92924152.9	10/29/1992	Sampling buffer for field programmable interconnect device
US5428750	06/27/1995	08/171,751	12/21/1993	Bi-directional buffers for mounting a plurality of integrated circuit devices
US5426738	06/20/1995	08/171,752	12/21/1993	Apparatus for flexibly routing signals between pins of electronic devices
US5530814	06/25/1996	08/333,290	11/02/1994	Bi-directional crossbar switch with control memory for selectively routing signals between pairs of signal ports
US5559971	09/24/1996	08/333,371	11/02/1991	Folded hierarchical crosspoint array
US5625780	04/29/1997	08/333,484	11/02/1994	Programmable backplane for buffering and routing bi-directional signals between terminals of printed circuit boards
US5734334	03/31/1998	08/516,320	08/17/1995	Programmable port for crossbar switch
US6636483	10/21/2003	09/258,182	02/25/1999	Network switch with zero latency flow control
US6771162	08/03/2004	09/687,724	10/12/2000	Active cell crosspoint switch

Patent Applications

Application Serial No.	Filing Date	Title/Description	Status
US08/269,794	06/30/1994	Apparatus for programmable circuit and signal switching	Abandoned
US08/516,318	08/17/1995	Crosspoint switch with bank-switched memory	Abandoned
WO PCT/US95/14202	10/31/1995	Apparatus for programmable circuit and signal switching	A1 Publ.
JP08-515408T	10/31/1995	Apparatus for programmable circuit and signal switching	Lapsed
EP97915138.8	03/18/1997	Hierarchical address translation system for a network switch	Withdrawn
JP09-534466T	03/18/1997	Hierarchical address translation system for a network switch	Lapsed
WOPCT/US98/10529	05/22/1998	Clustered address caching system for a network switch	A1 Publ.
WOPCT/US97/04257	03/18/1997	Network switch with broadcast support	A1 Publ.
DE19976037712T	03/18/1997	Network switch with broadcast support	Lapsed
JP09-534465T	03/18/1997	Network switch with broadcast support	Lapsed
WOPCT/US97/17186	09/19/1997	Dynamic spare column replacement memory system	A1 Publ.
US07/784,901	10/30/1991	Field programmable interconnect device	Abandoned
WOPCT/US92/09292	10/29/1992	Sampling buffer for field programmable interconnect device	A1 Publ.
JP04-508602	10/29/1992	Sampling buffer for field programmable interconnect device	Lapsed
US07/785,082	10/30/1991	Apparatus for flexibly interconnecting integrated circuits	Abandoned
US07/846,511	03/04/1992	Apparatus for mounting electronic circuit devices	Abandoned
WOPCT/US92/09284	10/29/1992	Field programmable logic module	A1 Publ.
WOPCT/US92/09363	10/29/1992	Field programmable circuit board	A1 Publ.
WOPCT/US95/14201	10/31/1995	Hierarchical crossbar switch	A1 Publ.
EP95939704.3	10/31/1995	Hierarchical crossbar switch	Withdrawn

JP08-515407T	10/31/1995	Hierarchical crossbar switch	Lapsed
--------------	------------	------------------------------	--------

EXHIBIT C

Patent Encumbrances – Licenses

<u>Party and Date</u>	<u>Beneficiaries</u>	<u>Patents</u>	<u>Term</u>	<u>Exclusivity</u>	<u>Fields of Use</u>	<u>Details</u>
<u>IBM Corporation</u> <u>North Castle</u> <u>Drive</u> <u>Armonk, NY</u> <u>10504</u> <u>June 24, 2009</u> <u>(L095749)</u>	<u>N/A</u>	<u>All</u> <u>Assigned</u> <u>Patents</u>	<u>Life</u>	<u>Non-</u> <u>Exclusive</u>	<u>All</u>	<u>Royalty-Free</u> <u>Non-</u> <u>Sublicensable</u> <u>Non-</u> <u>Transferable</u>
<u>Infineon</u> <u>Technologies</u> <u>AG (Am</u> <u>Campeon 1-12,</u> <u>85579</u> <u>Neubiberg,</u> <u>Germany)</u> <u>December 23,</u> <u>2009</u>	<u>N/A</u>	<u>All</u> <u>Assigned</u> <u>Patents</u>	<u>Life</u>	<u>Non-</u> <u>Exclusive</u>	<u>All</u>	<u>Royalty-Free</u> <u>Non-</u> <u>Sublicensable</u> <u>Non-</u> <u>Transferable</u>
<u>National</u> <u>Semiconductor</u> <u>Corporation</u> <u>Draft LOI dated</u> <u>January 7, 2004</u>	<u>N/A</u>	<u>All</u> <u>Assigned</u> <u>Patents</u>	<u>Life</u>	<u>Non-</u> <u>Exclusive</u>	<u>In Defined</u> <u>CrossPoint</u> <u>Switch</u> <u>Products</u>	<u>Royalty-Free</u> <u>Non-</u> <u>Sublicensable</u> <u>Non-</u> <u>Transferable</u>

EXHIBIT D

Licensed Patents

1. "Licensed Patents" means (a) the issued patents and pending patent applications identified on Schedule 1 to this Exhibit D, and (b) all issued patents, rights to inventions and pending and future applications for patents under U.S. law or regulation or of any foreign country with respect to the patentable inventions from which such patents in the foregoing (a) arise, including without limitation utility patents, utility models, design patents, invention certificates, continuations, divisionals, continuations-in-part, reexaminations, reissues, extensions and renewals, in all countries of the world.
2. Seller hereby grants to Purchaser a royalty-free, fully paid-up, irrevocable, perpetual, non-exclusive, transferable, fully sublicensable (through multiple tiers), worldwide license under the Licensed Patents to make, have made, purchase, use, sell, lease, import, offer for sale, lease, or import any product or service.
3. Seller hereby for itself and its respective legal successors, affiliates, heirs and assigns, releases and absolutely discharges Purchaser (together with any and all sublicensees under the license set forth in Section 2 of this Exhibit D) from and against all claims, demands, actions, liabilities, damages, losses, causes of action, and all other claims of every kind and nature in law or equity, whether arising under state, federal, international or other law, which arise from or relate to in any way the Licensed Patents (excluding claims for any breach of the Agreement to which this Exhibit D is attached). The foregoing release will extend to all sublicensees under the license set forth in Section 2 of this Exhibit D and the employees, representatives, agents, officers, directors, parents, subsidiaries, past, present and future, of such sublicensees.
4. Seller, on behalf of itself and any successors-in-interest to any and all of the Licensed Patents, hereby unconditionally and irrevocably covenants that Seller will not sue or commence any legal proceeding, nor will Seller join any lawsuit or legal proceeding, with respect to any and all of the Licensed Patents.

SCHEDULE 1

PATENTS AND PATENT APPLICATIONSPatents

Patent No.	Issue Date	Application Serial No.	Filing Date	Title
US5202593	04/13/1993	07/785,299	10/30/1991	Bi-directional bus repeater
DE0610452	08/13/1997	19926021632T	10/29/1992	Bi-directional bus repeater
EP0610452 B1	08/13/1997	93909342.3	10/29/1992	Bidirectional bus repeater
FR0610452	08/13/1997	EP93909342.3	10/29/1992	Bidirectional bus repeater
GB0610452	08/13/1997	EP93909342.3	10/29/1992	Bidirectional bus repeater

Patent Applications

Application Serial No.	Filing Date	Title/Description	Status
WOPCT/US92/09285	10/29/1992	Bidirectional bus repeater	A1 Publ.
JP04-508596	10/29/1992	Bi-directional bus repeater	Lapsed