PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Bruce John Chamberlin	07/11/2011
Chang-Min Chu	07/05/2011
Gao-Bin Hu	07/01/2011
Joseph Kuczynski	06/30/2011
Kaspar Ka Chung Tsang	07/11/2011

RECEIVING PARTY DATA

Name:	International Business Machines Corporation	
Street Address:	New Orchard Road	
City:	Armonk	
State/Country:	NEW YORK	
Postal Code:	10504	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	13182906

CORRESPONDENCE DATA

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Email: shrodrig@us.ibm.com Correspondent Name: Jeffrey S. LaBaw Address Line 1: 11400 Burnet Rd.

Address Line 2: IBM-Intellectual Property Law

Address Line 4: Austin, TEXAS 78758

ATTORNEY DOCKET NUMBER: AUS920110200US1 NAME OF SUBMITTER: Jeffrey S. LaBaw

Total Attachments: 9

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Whereas, the undersigned Inventor(s) has/have made certain inventions, improvements, and discoveries (herein referred to as the "Invention") in:

Title of the Invention: Plated Through Hole Void Detection in Printed Circuit Boards by Detecting Material Coupling to Exposed Luminate

and further identified by the IBM Docket Number provided above in the header of this Assignment, for which an application for a United States Patent was executed concurrently herewith or was filed having:

Application Number:

(insert series code and serial number here if/when available)

Whereas, International Business Machines Corporation, a corporation of New York having a place of business at Armonk, New York (herein referred to as "IBM"), desires to acquire, and each undersigned Inventor desires to grant to IBM, the entire worldwide right, title, and interest in and to the Invention and in and to any and all patent applications and patents directed thereto:

Now, therefore, for good and valuable consideration, the receipt and sufficiency theresis being hereby acknowledged, each undersigned Inventor ("ASSIGNOR") hereby sells, assigns, and otherwise transfers to IBM (the "ASSIGNEE"), its successors, legal representatives, and assigns, the entire worldwide right. title, and interest in and to the Invention, the above-identified United States patent application, and any and all other patent applications and patents for the invention which may be applied for or granted therefor in the United States and in all foreign countries and jurisdictions, including all divisions, continuations, reissues, reexaminations, renewals, extensions, counterparts, substitutes, and extensions thereof, and all rights of priority resulting from the filling of such applications and granting of such patents. In addition, each undersigned Inventor hereby authorizes and requests the Director of the United States Patent and Trademark Office to issue any United States Patent, and foreign patent authorities to issue any forcign patent, granted for the Invention, to IRM, its successors, legal representatives, and assigns, the entire worldwide right, title, and interest in and to the same to be held and enjoyed by IBM. its successors, legal representatives, and assigns to the full end of the terms for which any and all such patents may be granted, as fully and entirely as would have been held and enjoyed by the undersigned had this Assignment not been made; and each undersigned Inventor agrees to execute any and all documents and instruments and perform all lawful acts reasonably related to recording this Assignment or perfecting title in the Invention and all related patents and applications, in BPM, its successors, legal representatives, and assigns, whenever requested by HBM, its successors, legal representatives, or assigns.

Each undersigned Inventor acknowledges their prior and ongoing obligations to sell, assign, and transfer the rights under this Assignment to IBM and is unaware of any reason why they may not have the full and unencumbered right to sell, assign, and transfer the rights bereby sold, assigned, and transferred, and has not executed, and will not execute, any document or instrument in conflict herewith. Each undersigned havenfor also hereby grants IBM, its successors, legal representatives, and assigns, the right to insert in this Assignment any further identification (including, but not limited to, patent application Serial Number) which may be necessary or desirable for recordation of this Assignment. This Assignment is governed by the substantive laws of the State of New York, and any disputes will be resolved in a New York state court or federal court sited in New York.

(Inventor Signature Pages Follow)

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Decks: AUS920110201150

File: 1921;

IBM DOCKET NUMBER: AUS928110281US1

		Executed by inventor 1 of 5	
Signaturet	Bruce John Chambertin	<u>alac</u>	Date: <u>// JGZ 4 2 © //</u>
		Executed by inventor 2 of 5	
Signature:	Chang-Min Chu	and the second s	Date:
Maritime and a second		Executed by Inventor 3 of 5	
Signature:	Gause Bu	i,	Uate:
·		Executed by Inventor 4 of 5	
Signature:	Joseph Kuezyaski		Date:
		Executed by Inventor 5 of 5	
Signatore:	Kaspar Na Chung Tsang		Date:

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Whereas, the undersigned Inventor(s) has/have made certain inventions, improvements, and discoveries (herein referred to as the "Invention") in:

Title of the Invention: Plated Through Hole Void Detection in Printed Circuit Boards by Detecting Material Coupling to Exposed Laminate

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[Inventor Signature Pages Follow]

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IBM DOCKET NUMBER: AUS928118281USI

		Executed by Inventor 1 of 5	***************************************	
Signature:	Bruce John Chamberlin			
		Executed by Inventor 2 of 5	00000000000000000000000000000000000000	
Signature:	Chang - Min Chang-Min Chu	<u>Chu</u>	Date:	07/05.
***************************************		Executed by Inventor 3 of 5		
Signature:	Gause Hu		Date: _	
***************************************		Executed by Inventor 4 of 5	***************************************	***************************************
Signature;	Joseph Kuczynski			
		Executed by Inventor 5 of 5		
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Signature:	Kaspar Ka Chung Tsang		Date:	***************************************

2 of 2 Docket: AUS920110201US1

File: 1021

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	Executed by investor 2 of 5	
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mannaman	Executed by Inventor 3 of 5	
Signature	GAQBO HU GAO-BW HU	one: <u>Tuly 15t, 2011</u>
	Exercised by Inventor 4 of 5	
Signature:	Joseph Kucayaski	Vaix)
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Signature:		Dates
· Mariana Maria	Kaspar Ka Chung Tsang	and the second of the second o

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Whereas, the undersigned Inventor(s) has/have made certain inventions, improvements, and discoveries (herein referred to as the "Invention") in:

Title of the Invention: Plated Through Hole Void Detection in Printed Circuit Boards by Detecting Material Coupling to Exposed Laminate

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[Inventor Signature Pages Follow]

1 of 2

Docket: AUS920110201US1 File: 1021

IBM DOCKET NUMBER: AUS920110201US1

	Executed by Inver	ntor I of 5
Signature:	Bruce John Chamberlin	Date:
	Executed by Inver	ator 2 of 5
Signature:	Chang-Min Chu	Date:
	Executed by Inves	ator 3 of 5
Signature:	Gause Hu	Date:
***************************************	/ Executed by Inver	ntor 4 of 5
Signature:	Joseph Knczynski	Date: <u>4/30/11</u>
	Executed by Inver	ntor 5 of 5
Signature:	Kaspar Ka Chung Tsang	Date:

2 of 2

Docket: AUS920110201US1

File: 1021

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[Inventor Signature Pages Follow]

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File: 102.1

IBM BOCKET NUMBER: AUS920110201USI

		Executed by Inventor 1 of 5		
		Executed by Inventor 2 of S		
Signature	Chang-Min Chu		AN 10 P. 1	
		Executed by Investor 3 of 5		
Signature:			Date:	
		Executed by Inventor 4 of 5		
Signature:	Joseph Kurzynski		Date:	
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Signature:	Karpar K. Chung Tsang		Date:	11-Jul-2

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RECORDED: 07/15/2011

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