

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SONICblue Incorporated	11/24/2006
RECEIVING PARTY DATA	
Name:	S3 Graphics Co., Ltd.
Street Address:	Charles Adams, Ritchie & Duckworth, Zephyr House, Mary Street, P.O. Box 709
City:	Grand Cayman
State/Country:	CAYMAN ISLANDS
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	6766281
CORRESPONDENCE DATA	
Fax Number:	(650)812-3444
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	6508123400
Email:	bbathurst@carferrell.com, lmolera@carferrell.com
Correspondent Name:	K. Brian Bathurst
Address Line 1:	120 Constitution Drive
Address Line 4:	Menlo Park, CALIFORNIA 94025
ATTORNEY DOCKET NUMBER:	6,766,281
NAME OF SUBMITTER:	K. Brian Bathurst
Total Attachments: 10 source=PA1742_Sonicblue_to_S3Graphics#page1.tif source=PA1742_Sonicblue_to_S3Graphics#page2.tif source=PA1742_Sonicblue_to_S3Graphics#page3.tif source=PA1742_Sonicblue_to_S3Graphics#page4.tif source=PA1742_Sonicblue_to_S3Graphics#page5.tif source=PA1742_Sonicblue_to_S3Graphics#page6.tif source=PA1742_Sonicblue_to_S3Graphics#page7.tif source=PA1742_Sonicblue_to_S3Graphics#page8.tif source=PA1742_Sonicblue_to_S3Graphics#page9.tif source=PA1742_Sonicblue_to_S3Graphics#page10.tif	

CH \$40.00 6766281

**ASSIGNMENT OF PATENTS AND PATENT APPLICATIONS AND
DISCLOSURES**

WHEREAS, SONICblue Incorporated (SONICblue Inc.), a corporation organized and existing under the laws of the State of Delaware, having changed its name from S3 Incorporated (S3 Inc.) to SONICblue Incorporated (“Assignor”), and having an office and place of business at 2841 Mission College Boulevard, Santa Clara, CA 95054, is the owner of the inventions, patents, patent applications and disclosures listed in Schedule 1 annexed hereto and made a part hereof; and

WHEREAS, S3 Graphics Co., Ltd., a corporation organized and existing under the laws of the Cayman Islands, having a registered office at Charles Adams, Ritchie & Duckworth, Zephyr House, Mary Street, P.O. Box 709, Grand Cayman, British West Indies (“Assignee”), is desirous of acquiring the entire right, title, and interest in and to the inventions, patents, patent applications and disclosures listed in Schedule 1 annexed hereto, in the United States of America, and in its colonies, territories, and dependencies, and also in all countries foreign to the United States of America.

NOW, THEREFORE, TO ALL WHOM IT MAY CONCERN:

Be it known that for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, the aforesaid Assignor, has sold, assigned, transferred and set over, and by these presents does hereby sell, assign, transfer and set over to said Assignee, the entire right, title and interest in and to said inventions, patents, patent applications and disclosures listed in Schedule 1, and any and all continuations, divisions and renewals of and substitutes for said applications, and in, to and under any and all Letters Patent which may be granted on or as a result thereof in the United States and any and all other countries, and any reissue or reissues or extension or extensions of said Letters Patent; and does hereby assign to and authorize said Assignee, to file applications for Letters Patent in all countries, the same to be held and enjoyed by said Assignee, its successors, assigns, nominees or legal representatives, to the full end of the term or terms for which said Letters Patent respectively may be granted, reissued or extended, as fully

and entirely as the same would have been held and enjoyed by Assignor had this assignment, sale and transfer not been made.

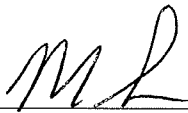
AND Assignor covenants to execute all additional instruments and to do all things necessary for carrying out the purpose of this instrument, at the expense of said Assignee and its successors.

AND Assignor hereby authorizes and requests the Commissioner of Patents and Trademarks of the United States and any official of any country or countries foreign to the United States whose duty it is to issue patents on applications as aforesaid, to issue to said Assignee, as assignee of the entire right, title and interest, any and all Letters Patent for said inventions, patents, patent applications, and disclosures listed in Schedule I, including any and all Letters Patent of the United States which may be issued and granted on or as a result of the applications aforesaid, in accordance with the terms of this assignment.

This assignment is effective November 14, 2006.

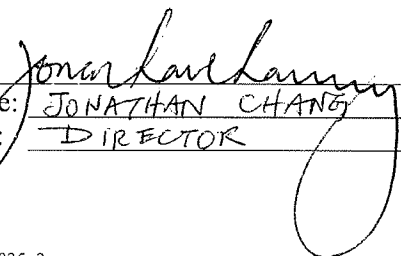
SONICblue Incorporated

Signed at San Jose
This 21st day of November, 2006

By: 
Name: Marcus Smith
Title: CFO

S3 Graphics Co., Ltd.

Signed at FREMONT, CA
This 5 day of DECEMBER, 2006

By: 
Name: JONATHAN CHANG
Title: DIRECTOR

Schedule 1

Agreement No.	C&F No. (Complete)	Country	Application Title	Application Date	Application No.	Patent No.	Patent No.	Note
CR0001	PA1846US	US	Method and Apparatus for Processing Perception of High Quality Grayscale Images on Digitally Commanded Displays	1982/4/6	07865,046	U.S. Patent No. 5,283,159		
CR0002	PA1847US	US	Image Memory Controller for Controlling Multiple Memories and Method of Operation	1980/9/28	07144,139	U.S. Patent No. 5,241,642		
CR0003	PA1848US	US	Video Window Generator with Scorable	1984/6/27	08295,164	U.S. Patent No. 5,402,513		
CR0004	PA1849US	US	Video Window Generator with Scorable	1982/7/15	08177,456	U.S. Patent No. 5,581,278		
CR0005	PA1850US	US	Video Window Generator with Scorable	1984/10/29	08293,382	U.S. Patent No. 5,440,889		
CR0006	PA1850JP	JP	Digital Video Editing Processing Unit	1982/2/25	5941,793	JP Patent No. 3088990		
CR0007	PA1850EP	EP	Digital Video Editing Processing Unit	1982/2/25	5941,793	EP Patent No. 3088990		
CR0008	PA1850DE	DE	Digital Video Editing Processing Unit	1982/2/25	5941,793	DE Patent No. 3088990		
CR0009	PA1851US	US	Integrated Video Scaling and Sharpening Filter	1982/4/12	09017,696	U.S. Patent No. 5,422,827		
CR0010	PA1852US	US	Process for Producing Shaded Colored Images Using Differing Techniques	1982/5/17	09061,802	U.S. Patent No. 5,757,347		
CR0011	PA1853US	US	Graphics and Video Signals	1986/7/16	06853,102	U.S. Patent No. 5,889,499		
CR0012	PA1854US	US	Video Processing Apparatus, System and Methods	1985/9/13	08412,935	U.S. Patent No. 5,625,379		
CR0013	PA1855US	US	Shared Memory for Split-Panel LCD Display System	1983/6/8	08101,770	U.S. Patent No. 5,537,128		
CR0014	PA1856US	US	Variable Pixel Depth and Format for Video Windows	1984/4/29	08235,784	U.S. Patent No. 5,608,864		
CR0015	PA1856KR	KR	Variable Pixel Depth and Format for Video Windows	1984/4/29	96-79697	KR Patent No. 245309		
CR0016	PA1856JP	JP	Variable Pixel Depth and Format for Video Windows	1984/4/29	7-52830	JP Patent No. 268779		
CR0017	PA1856EP	EP	Variable Pixel Depth and Format for Video Windows	1984/4/29	96317,94.3	EP Patent No. 398179		
CR0018	PA1857US	US	Method and Apparatus for Expanding and Contracting VCA Text and Graphics	1984/4/29	09233,827	U.S. Patent No. 5,521,614		
CR0019	PA1858US	US	Video processing Apparatus System and Methods	1985/9/13	08412,932	U.S. Patent No. 5,821,918		
CR0020	PA1859US	US	Brightness Control for Liquid Crystal Displays	1985/6/7	08474,416	U.S. Patent No. 5,734,352		
CR0021	PA1860US	US	Optimum Implementation of X-Y Clipping on Pixel Boundary	1985/6/22	08493,965	U.S. Patent No. 5,668,941		
CR0022	PA1876US	US	Method and Apparatus for Upscaling Video Images in a Graphics Computer Chip	1986/3/12	09076,670	U.S. Patent No. 6,115,507		
CR0023	PA1861US	US	Method and Apparatus for Upscaling Video Images When Pixel Data Used for Upscaling is Source Video Image are Unavailable	1985/11/22	08551,997	U.S. Patent No. 5,703,519		
CR0024	PA1861TW	TW	臺灣發明一種顯示器之影像放大方法	1985/11/22	8511442	TW Patent No. 08276		
CR0025	PA1861EP	EP	Apparatus for Image Scaling Using Interpolation	1986/11/21	96309452	EP Patent No. 08276		
CR0026	PA1861JP	JP	Method and Apparatus for Upscaling Video Images When Pixel Data Used for Upscaling is Source Video Image are Unavailable	1986/11/21	96309452	JP Patent No. 0775975		
CR0027	PA1861UK	UK	Apparatus for Image Scaling Using Interpolation	1986/11/21	96309452	UK Patent No. 0775975		
CR0028	PA1861NL	NL	Apparatus for Image Scaling Using Interpolation	1986/11/21	96309452	NL Patent No. 0775975		
CR0029	PA1861DE	DE	Apparatus for Image Scaling Using Interpolation	1986/11/21	96309452	DE Patent No. 6952219.2		
CR0030	PA1862US	US	Controller for Processing Different Pixel Data Types Stored in the Same Display Memory by Use of Tag Bits	1985/7/21	08576,870	U.S. Patent No. 5,828,383		
CR0031	PA1862US	US	Hardware Assisted for YUV Data Format Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
CR0032	PA1863US	US	Hardware Assisted for YUV Data Format Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
CR0033	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
CR0034	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
CR0035	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
CR0036	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
CR0037	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
CR0038	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
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CR0044	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
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CR0065	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
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CR0070	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
CR0071	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
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CR0075	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
CR0076	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
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CR0090	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
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CR0100	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
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CR0103	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
CR0104	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
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CR0107	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
CR0108	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
CR0109	PA1863US	US	Conversion to Software MPEG Decoder	1989/10/29	08430,370	U.S. Patent No. 6,355,440		
CR0110	PA1863US	US	Conversion					

Schedule 1

CR0033	1796293C	1796	US	Parallel Architecture For Graphics Primitive Decomposition	1985/1025	08/740,244	U.S. Patent No. 5,846,264
CR0034	1797293C	1987	US	Graphics Primitives Decomposition Using Edge Function and Recursive Tile Substitution	1987/1120	2727287	CA Patent No. 2,272,387
CR0034	1797293C	1987	US	Graphics Primitives Decomposition Using Edge Function and Recursive Tile Substitution	1989/1122	08/755,545	U.S. Patent No. 5,935,198
EX0028	PA18344US	1844	US	Debug and Video Queue for Multi-Processor Chip	1987/1120	2727287	CA Patent No. 2,272,387
EX0032	PA1838CA	1839	CA	Multiplier for Performing 3D Graphics Interpolations	1987/1120	19782085.7	U.S. Patent No. 5,935,198
EX0032	PA1839US	1839	US	Multiplier with Selectable Booth Encoders for Performing 3D Graphics Interpolations with Multiple Passes in a Single Pass Through BV Multiplier	1987/1120	19782085.7	U.S. Patent No. 5,935,198
EX0032	PA1839DE	1839	DE	Multiplier for Performing 3D Graphics Interpolations			
EX0032	1839LK	1839	GB	Multiplier for Performing 3D Graphics Interpolations			
EX0032	1839JP	1839	JP	Multiplier for Performing 3D Graphics Interpolations			
S30001	PA1753US	1733	US	Open High Speed Bus for Microcomputer System	1989/0729	06/011,449	U.S. Patent No. 5,917,626
S30008	PA1820US	1820	US	Improved Data Retrieval From Securely-Accessed Memory Devices	1989/0729	06/165,035	U.S. Patent No. 5,416,749
S30009	PA1824US	1824	US	Method and Apparatus for Connection of Video Training Associated with a Video on a Graphics Monitor	1986/0909	06/629,784	U.S. Patent No. 5,764,240
S30014	PA1728US	1728	US	Graphic Shading for Liquid Crystal Display Panels	1985/0625	09/519,690	U.S. Patent No. 5,777,580
S30014	PA1728JP	1728	JP	Graphic Shading for Liquid Crystal Display Panels	1985/0625	9-510349	JP Patent No. 3,818,255
S30015	PA1734US	1734	US	Decomposition of MPEG Compressed Data in a Computer System	1985/0612	09/489,888	U.S. Patent No. 5,776,086
S30015 ?	PA1759US	1735	US	Method and Apparatus for Decomposition of MPEG Compressed Data in a Computer System	1985/103	06/538,887	U.S. Patent No. 5,774,676
S30016	PA1728US	1729	US	Correction of Flicker Associated with Noninterlaced-to-Interlaced Video Conversion	1986/0225	08/621,567	U.S. Patent No. 5,910,820
S30016	PA1728JP	1729	JP	Correction of Flicker Associated with Noninterlaced-to-Interlaced Video Conversion	1987/0424	8-487786	
S30019	PA1738US	1738	US	Video Decoder Engine	1985/0812	09/400,322	U.S. Patent No. 5,818,867
S30020	PA1721US	1738	US	Frame Reconstruction for Video Data Compression	1985/0728	09/506,696	U.S. Patent No. 5,757,670
S30020 ?	PA1821US	1821	US	Multi-Function Controller and Method for a Multi-Function Display System	2000/0110	09/451,971	U.S. Patent No. 6,480,198
S30022	PA1822TW	1822	TW	Method and Apparatus for a Multi-Function Display System	1987/0627	08/684,361	U.S. Patent No. 6,052,133
S30022	PA1822US	1822	US	Method and Apparatus for a Multi-Function Display System	1987/0627	08/684,361	TW Patent No. 100111
S30022	PA1822JP	1822	JP	Method and Apparatus for a Multi-Function Display System	1987/0627	85187413	
S30023	PA1823US	1823	US	Close Captioning Processing Architecture for Video Data During Multiple Fields of a Video Frame	1986/0718	06/676,527	U.S. Patent No. 5,883,675
S30023	PA1823JP	1823	JP	Close Captioning Processing Architecture for Video Data During Multiple Fields of a Video Frame	1987/0718	HS-184224	
S30023	PA1823DE	1823	DE	Index and Storage System for Data Provided in the Vertical Blanking Interval	1986/1213	06/753,923	U.S. Patent No. 5,914,719
S30024	PA1730US	1737	US	Index and Storage System for Data Provided in the Vertical Blanking Interval	1987/121	19759286.9	
S30024	PA1731TW	1737	TW	Index and Storage System for Data Provided in the Vertical Blanking Interval	1987/121	09/595,135	U.S. Patent No. 5,977,833
S30027	PA1826US	1826	US	Direct Image Computer Display Controller	1986/0713	05/188226	TW Patent No. 84432
S30027	PA1826JP	1826	JP	Direct Image Computer Display Controller	1986/0713	08/694,573	U.S. Patent No. 5,990,812
S30027	PA1826CA	1826	CA	Virtual Address Access to Text Surfaces	1986/0718	98511339	U.S. Patent No. 5,990,812
S30028	PA1825US	1825	US	Virtual Address Access to Text Surfaces	1986/0718	2,284,884	
S30028	PA1825US	1825	US	Method for Rendering for Improved Texture Mapping	1987/119	08/760,787	U.S. Patent No. 5,952,451

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S30028	PA1825JP	1825	JP	Pixel Rendering for Improved Texture Mapping	1997/12/19	H10-530693			
S30028	PA1825EP	1825	EP	Pixel Rendering for Improved Texture Mapping	1997/12/19	9795438.1			
S30028	PA1825CA	1825	CA	Pixel Rendering for Improved Texture Mapping	1997/12/19	2275237	CA Patent No. 2,275,237		CA Patent No. 2,275,237
S30031	PA1826US	1826	US	System and Method for a Fast Carry/Sum Select Adder	1997/11/27	08/6788391		U.S. Patent No. 5,852,568	5,852,568
S30031	PA1828JP	1828	JP	System and Method for a Fast Carry/Sum Select Adder	1998/1/20	H10-534360		JP Patent No. 3183521	
S30031	PA1828EP	1828	EP	System and Method for a Fast Carry/Sum Select Adder	1998/1/20	58902817.4			
S30031	PA1828CA	1828	CA	System and Method for a Fast Carry/Sum Select Adder	1998/1/20	2,275,188	CA Patent No. 2,275,188		CA Patent No. 2,275,188
S30032	PA1827US	1827	US	System and Method for Simultaneous Flicker Filtering and Overscan Compensation	1997/8/29	08/642,143		U.S. Patent No. 5,990,965	U.S. Patent No. 5,990,965
S30032	PA1827JP	1827	JP	Flicker Filtering and Overscan Compensation	1998/9/29	2000-514468			
S30032	PA1827EP	1827	EP	Flicker Filtering and Overscan Compensation	1998/9/29	98950951.0-2202			U.S. Patent No. 5,852,568
S30032	PA1827CA	1827	CA	Flicker Filtering and Overscan Compensation	1998/9/29	2303905			JP Patent No. 3,183,921
S30032	?	?	JP	System and Method for Simultaneous Flicker Filtering and Overscan Compensation					
S30032	1827KR	1827	KR	Flicker Filtering and Overscan Compensation					
S30033	PA1831US	1831	US	Block and Band-Oriented Traversal in Three-Dimensional Triangle Rendering	1997/6/26	08/685,538		U.S. Patent No. 5,945,987	U.S. Patent No. 5,945,987
S30033	PA1831JP	1831	JP	Block and Band-Oriented Traversal in Three-Dimensional Triangle Rendering	1998/6/18	H11-509557			
S30033	PA1831CA	1831	CA	Block and Band-Oriented Traversal in Three-Dimensional Triangle Rendering	1998/6/18	2284323			
S30033	1831PCT	1831	PCT	Block and Band-Oriented Traversal in Three-Dimensional Triangle Rendering					
S30033 PCT	PA1831EP	1831	EP	Block and Band-Oriented Traversal in Three-Dimensional Triangle Rendering	1998/6/18	98931506.6			
S30034	PA1830US	1830	US	System and Method for 2-Tap/3-Tap Flicker Filtering of Non-Interlaced Computer Graphics to Interlaced Lines for Television Display	1997/1/13	08/959,837		U.S. Patent No. 5,884,568	U.S. Patent No. 5,884,568
S30034	PA1830JP	1830	JP	System and Method for 2-Tap/3-Tap Flicker Filtering	1998/1/12	2000-521610			
S30034	PA1830EP	1830	EP	System and Method for 2-Tap/3-Tap Flicker Filtering	1998/1/12	99595462.7			
S30034	PA1830CA	1830	CA	System and Method for 2-Tap/3-Tap Flicker Filtering	1998/1/12	2308805			
S30035 ?	PA2473US	2473	US	Block Image Compression with Inferred Pixel Values	1999/1/17	08/642,114		U.S. Patent No. 5,983,978	U.S. Patent No. 5,983,978
S30035	PA1730US	1730	US	Block Image Compression with Inferred Pixel Values	1997/1/02	08/642,860		U.S. Patent No. 5,956,431	U.S. Patent No. 5,956,431
S30035	PA1730KR	1730	KR	Block Image Compression with Inferred Pixel Values	1998/9/28	70036142/000			
S30035	PA1730US	1731	US	Fixed-Rate Block-Based Image Compression with Inferred Pixel Values	1999/7/12	08/925,930		U.S. Patent No. 6,658,146	U.S. Patent No. 6,658,146
S30035	PA1732US	1732	US	System and Method for Fixed-Rate Block-Based Image Compression with Inferred Pixel Values	2002/1/17	10052,813		U.S. Patent No. 6,775,417	U.S. Patent No. 6,775,417
S30035	?	?	US	System and Method for Fixed-Rate Block-Based Image 3-D Compression with Inferred Pixel Values					
S30035	1730PCT	?	PCT	System and Method for Fixed-Rate Block-Based Image Compression with Inferred Pixel Values					
S30035 PCT	PA1730JP	1730	JP	System and Method for Fixed-Rate Block-Based Image Compression with Inferred Pixel Values	1998/9/28	2000-515285			

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Schedule I

S30051 PCT	PA1877EP	1877	EP	Double Buffered Graphics and Video Accelerator Having a Write Bleeding Memory Buffer and a Method of Doing the Same	1989/059	95922776.8		
S30052	1720US	1720	US	Communicating the Status of a Peripheral Device Controller to a Host Processor				
S30053	1720PCT	1720	PCT	Communicating the Status of a Peripheral Device Controller to a Host Processor				
S30054	?	?	US	Computer System and Method for Efficiently Communicating Peripheral Device Status	2001/0219	95942081.3		
S30055 PCT	PA1720EP	1720	EP	Communicating the Status of a Peripheral Device Controller to a Host Processor				
S30056	PA1776US	1776	US	Device and method for Blending True Colors and Fog Colors to Generate Display Colors	1989/063	09/066,187	U.S. Patent No. 6,417,862	U.S. Patent No. 6,417,862
S30057	1776US	1776	US	Device and Method for Blending True Colors and Fog Colors to Generate Display Colors				
S30058	PA1765US	1765	US	System and Method for mapping Textures onto Surfaces of Computer-Generated Objects	2000/1177	09/708,787		
S30059	PA1746US	1746	US	System and Method for Mapping Textures onto Surfaces of Computer-Generated Objects				
S30060	PA1747US	1747	US	System and Method for Mapping Textures onto Surfaces of Computer-Generated Objects	2000/1111	09/708,775	U.S. Patent No. 6,837,250	U.S. Patent No. 6,837,250
S30061	PA1785US	1785	US	System and Method for Mapping Textures onto Surfaces of Computer-Generated Objects	1997/627	08/854,044	U.S. Patent No. 6,236,405	U.S. Patent No. 6,236,405
S30062	?	?	US	Out of Order Depth Unit for Graphics Primitives				
S30063	PA1835US	1835	US	Real Time DRAM Eliminating a Performance Penalty for Crossing a Page Boundary	1988/025	09/018,345	U.S. Patent No. 5,005,019	U.S. Patent No. 5,005,019
S30064	1835PCT	1835	PCT	Real time DRAM Page Boundary Adjustment				
S30065	PA1718US	1718	US	System and Method for Copy Protecting Computer Graphics	1989/522	09/053,923	U.S. Patent No. 6,345,059	U.S. Patent No. 6,345,059
S30066	PA1837US	1837	US	Adaptive Dynamic Aperture Correction	1988/447	09/061,725	U.S. Patent No. 6,172,718	U.S. Patent No. 6,172,718
S30067	PA1826US	1826	US	File-Panel Display Controller with Improved Dithering and Frame Rate Control	1988/210	09/021,718	U.S. Patent No. 6,008,794	U.S. Patent No. 6,008,794
S30068	PA1838US	1838	US	Programmable Delay Timing Calibrator for High Speed Data Interface	1988/927	09/045,984	U.S. Patent No. 6,041,419	U.S. Patent No. 6,041,419
S30069	PA1839CA	1839	CA	Programmable Delay Timing Calibrator for High Speed Data Interface	1988/524	2/286,149		
S30070	PA1840US	1840	US	AGP/DDB Interfaces for Full Swing and Reduced Swing (SSTL) Signals on an Integrated Circuit Chip	1988/48	09/057,047	U.S. Patent No. 6,005,412	U.S. Patent No. 6,005,412
S30071	PA1840KR	1840	KR	Reducing SS(TL) Signals on an Integrated Circuit Chip	1989/919	10-1959-7011515		
S30072	PA1840JP	1840	JP	Reducing SS(TL) Signals on an Integrated Circuit Chip	1989/919	H11-560498		
S30073	1840PCT	1840	PCT	AGP/DDB Interfaces for Full Swing and Reduced Swing (SS(TL) Signals on an Integrated Circuit Chip				
S30074	PA1841US	1841	US	Non-Stalled Requesting Texture Cache System and Method	1988/549	09/057,628	U.S. Patent No. 6,011,565	U.S. Patent No. 6,011,565
S30075	PA1841JP	1841	JP	Non-Stalled Requesting Texture Cache System and Method	1989/514	H11-551582		
S30076	PA1841EP	1841	EP	Non-Stalled Requesting Texture Cache System and Method	1989/514	9891111.5	EP Patent No. 0598709	EP Patent No. 0598709
S30077	PA1841CA	1841	CA	Non-Stalled Requesting Texture Cache System and Method	1989/514	2,293,634		
S30078 EP	PA1841UK	1841	UK	Non-Stalled Requesting Texture Cache System and Method	1989/514	98 911 111.5	UK Patent No. 0598709	UK Patent No. 0598709
S30079 EP	PA1841DE	1841	DE	Non-Stalled Requesting Texture Cache System and Method	1989/514	98 811 111.5	DE Patent No. 69818022	DE Patent No. 69818022
S30080	PA1842US	1842	US	System and Method for Performing Dithering with a Graphics Unit Having an Overlapping Buffer	1988/514	09/078,973	U.S. Patent No. 6,154,195	U.S. Patent No. 6,154,195
S30081	PA1843US	1843	US	System and Method for Performing Blending Using an Oversampling Buffer	1988/445	09/060,933	U.S. Patent No. 6,144,365	U.S. Patent No. 6,144,365

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