# PATENT ASSIGNMENT

# Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	MERGER
EFFECTIVE DATE:	05/19/2011

## **CONVEYING PARTY DATA**

Name	Execution Date	
BridgeCo, Inc.	05/19/2011	

#### RECEIVING PARTY DATA

Name:	SMSC Holdings S.a.r.l.						
Street Address:	6 rue Philippe II						
City:	Luxembourg						
State/Country:	LUXEMBOURG						
Postal Code:	L-2340						

# PROPERTY NUMBERS Total: 1

Property Type	Number			
Application Number:	09785598			

## CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	6428-02003
NAME OF SUBMITTER:	Jeffrey C. Hood

# Total Attachments: 5

501607670

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#### ASSIGNMENT OF PATENTS

WHEREAS pursuant to that certain Agreement and Plan of Merger, dated as of May 19, 2011 (as amended, modified, supplemented or restated from time to time in accordance with its terms, the "Merger Agreement"), by and among, among others, BridgeCo, Inc., a Delaware corporation, with its principal place of business at 400 Continental Blvd, 6th Floor El Segundo, CA 90245 ("Assignor") and Standard Microsystems Corporation and that certain Intellectual Property Assignment Agreement, dated as of May 19, 2011 (as amended, modified, supplemented or restated from time to time in accordance with its terms, the "IP Assignment Agreement"), between Assignor and SMSC Holdings S.à.r.l., a private limited liability company (société à responsabilitée limitée) existing under the laws of Luxembourg and having its corporate office at 6 rue Philippe II, L-2340 Luxembourg, Grand-Duchy of Luxembourg ("Assignee"), Assignor agreed to assign certain intellectual property rights to Assignee.

NOW THEREFORE, for valuable consideration, the receipt and sufficiency of which is hereby acknowledged, pursuant to the Merger Agreement and IP Assignment Agreement, Assignor hereby sells, assigns, transfers, and conveys to Assignee all of Assignor's right, title, and interest in and to any and all patents and patent applications owned by Assignor, including any and all continuations, divisions, continuations-in-part, reissues, re-examinations thereof and any patents and patent applications subsequently developed by Assignor; including without limitation the patents and patent applications (and all rights associated therewith and all drawings, designs, models and other documents evidencing or embodying the inventions which are the subject of such patents) that are set forth in Schedule 1 hereto.

The rights granted herein shall include the right to file, prosecute and enforce any foreign corresponding patent application in any jurisdiction throughout the world. In addition, Assignee is hereby assigned the full right, title and interest to enforce the rights granted hereunder against any past, present and future infringer of any of the rights transferred hereunder and to collect all recovery from such enforcement.

IN WITNESS WHEREOF, the Assignor and Assignee have caused this Assignment of
Patents to be executed by their authorized representatives.

Title: \_\_\_\_\_

Effective:	May19, 2011	
BridgeCo, Inc	•	
		By:
		Name:

Signature Page to Assignment of Patents

**ASSIGNMENT ACCEPTED:** 

SMSC Holdings S.à.r.l..

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Name: 1/4

Title:

[Signature Page to Assignment of Patents]

# **SCHEDULE 1**

1. The following patents and patent applications:

BRIDGECO, INC D	1Rle	Gobdwin Bocket Mumber		appi. Number	patent patent	statue	(effective) filting date	grant date	upcoming due date		Occasion
BRIDGECO, INC D	Jafaranaa Tima									comments	Description of Capability
BRIDGECO, INC D	Poloronoo Timo	BCO-001A	US	09/785.598		pending	2/16/2001			Not used in any current product	Method to distribute time stamps, heart basts, and do clock recovery across a network consisting of both synchronous and packet-based network segments. Relevant for 1394.1 based network bridges.
N	BRIDGECO, INC Distribution over a Network	BCO-001AEP	EPO	01910090.8	1256197	granted	2/16/2001	4/8/2009			
		BCO-001ADE	DE	01910090.8	60138266.8	granted	2/16/2001	4/8/2009			
		BCO-001AGB	GB FR	01910090.8	1256197	granted	2/16/2001	4/8/2009			
		BCO-001AFR		01910090.8	1256197	granted	2/16/2001	4/8/2009			
		BCO-001B	US	09/784.831	6772267	granted	2/16/2001	8/3/2004		_	1394 specific - connects a synchronus networks to an a-synchronus network. A business alliance (HANA - Samsung)
	Multi-Portal Bridge for	BCO-001BEP	EPO	01912070.8	1256207	granted	2/16/2001	9/5/2007		Not used in any	
	Providing Network Connectivity	BCO-001BDE	DE	01912070.8	60130319.9	granted	2/16/2001	9/5/2007		current product wants to use a s	wants to use a similar technology for home networking. So if they canot find a way around out patent there may be a
		BCO-001BGB	GB	01912070.8	1256207	granted	2/16/2001	9/5/2007			
		BCO-001BFR	FR	01912070.8	1256207	granted	2/16/2001	9/5/2007			
		BCO-002	US	10/496.367	7716375	granted	10/25/2002	5/11/2010			
		BCO-002C1	US	12/732654		awaiting first office action	10/25/2002				Algorithm to synchronize multiple
BRIDGECO, INC	Synchronization over	BGO-002EP	EPO	02774228.7	1449317	granted	10/25/2002	4/26/2006		future SW	nodes on a packet-based network to much better accuracy than can be reached with the network's regular
Pi	acket Networks	BGO-002DE	DE	2774228.7	50206599	granted	10/25/2002	4/26/2006		releases (and HW)	synchronization resources. Combination of hardware and software required for use in 802.3-type networks
		BCO-002GB	GB	2774228.7	1449317	granted	10/25/2002	4/26/2006			(Ethernet).
		BCO-002CH	СН	2188/01		abandoned in favor of BCO-002EP	11/28/2001				
		BCO-003	US	10/518.874	7132895	granted	6/23/2003	11/7/2006			Digital CMOS-based low-area clock
	RIDGECO, INC Digital Controlled Oscillator	BCO-003EP	EPO	03729767.8		Allowed	6/23/2003			Used in Checkov and Sulu	recovery and clock synthesis method. Allows both low-jitter clock synthesis based on a stable clock (from external crystal or on-chip PLL) or low-jitter clock recovery from a range of inputs. Obsoletes the need for off-chip VC(X)Os or more expensive on-chip PLLs.
		BCO-003CH	СН	111/02		abandoned in favor of BGO-003EP	6/27/2002				
		BCO-004	US	10/547560		Abandoned	3/1/2004				Advanced architecture for DSP-type algorithms with multiple diverse sequencer-based processing elements that are use in different slave-mode operations from a master processor. It bridges the divide between the computational performance of a VLIW architecture and the code-density of a general purpose RISC architecture.
		BGO-004EP	EPO	04715865.4	1599794	Granted	3/1/2004	2/18/2009			
BRIDGECO, INC S	Processor using diverse Sequencers for common Resources	BGO-004DE	DE	04715865.4	502004009010.9	Granted	3/1/2004	2/18/2009		Used in Sulu	
		BCO-004GB	GB	04715865.4	1599794	Granted	3/1/2004	2/18/2009			
		BCO-004FR	FR	04715865.4	1599794	Granted	3/1/2004	2/18/2009			
	Devices	BGO-005PR	US	61/095,204		Expired	9/8/2008			<u> </u>	
		BCO-006PR BCO-006	US	61/095,062 12/555,146		Expired Pending- await	9/9/2008				
In A	mproved Very-Long nstruction Word Architecure with Mutiple	BCO-006PC	EPO	09747916.6		examination Pending	9/8/2009				
BRIDGECO AG	ompreession for VLIVV	BCO-008PR	US	61/098081		Expired	9/18/2008				
BRIDGECO, INC U	Jser Preferences	BCO-009PR BCO-009PR BCO-009PC	US US	61/319,604 13/076,661 US/11/30657		Expired Pending	3/31/2010 3/31/2010 3/31/2010				
	Vi-Fi Setup Configuration	BCO-010PR BCO-010	PCT US US	61/331459 13/098,779		Pending Filed Filed	5/5/2010 5/5/2010				
	Remote Wireless Control	BCO-010PC BCO-011PR	PCT	US/11/34775 61/333896		Filed Expired	5/5/2010 5/12/2010				
Assigned to of		BCO-011 BCO-011PC	US	13/106,135 US11/036219		Filed Filed	5/12/2010 5/12/2010				
BRIDGECO, INC S		BCO-012PR	US	61/348927	_	Filed	5/27/2010		U.S. & Foreign filing due 27 May 2011		
M A No Assignment	Method for Sharing an	BCP-013PR	US	61/415955		Filed	11/22/2010		U.S. & Foreign filing due 22 November 2011		
FI No Assignment C	lovible Wireless	BCP-014PR	US	61/481,933		Filed	5/4/2011				



PATENT
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