

## PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Single Chip Systems Corp.	07/29/2011
RECEIVING PARTY DATA	
Name:	Neology, Inc.
Street Address:	12760 Danielson Ct.
Internal Address:	Suite A
City:	Poway
State/Country:	CALIFORNIA
Postal Code:	92064
PROPERTY NUMBERS Total: 4	
Property Type	Number
Patent Number:	5856788
Patent Number:	6229443
Patent Number:	6690264
Patent Number:	7064653
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ATTORNEY DOCKET NUMBER:	116546-000005
NAME OF SUBMITTER:	Noel C. Gillespie
Total Attachments: 2 source=SCS_Assignment_Neology#page1.tif source=SCS_Assignment_Neology#page2.tif	

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PATENT  
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## ASSIGNMENT

This Patent Assignment (the "Assignment") is dated as of July 14, 2011 (the "Effective Date").

WHEREAS, the undersigned, **Single Chip Systems Corp.** (the "Assignor") is the owner of the entire right, title and interest in certain Patents as set forth below; and

WHEREAS, **Neology, Inc.**, (the "Assignee"),

with an address of: 12760 Danielson Ct., Suite A  
Poway, CA 92064  
United States of America

desires to acquire the entire right, title and interest in and to the inventions, and in and to the said Patents;

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, Assignor assigns to Assignee, all right, title and interest in and to said inventions and in and to said application and all patents which may be granted therefor, and all provisional applications related thereto and any divisions, reissues, continuations, continuations-in-part and extensions thereof; and Assignor authorizes and requests the Commissioner of Patents and Trademarks to issue all patents for said inventions, or patents resulting therefrom, insofar as our interest is concerned, to Assignee.

Assignor also assigns to Assignee, all right, title and interest in and to the inventions disclosed in said application throughout the world, including the right to file applications and obtain patents, utility models, industrial models and designs for said inventions in its own name throughout the world, including all rights to publish cautionary notices reserving ownership of said inventions and all rights to register said inventions in appropriate registries; and Assignor further agrees to execute any and all powers of attorney, applications, assignments, declarations, affidavits, and any other papers in connection therewith necessary to perfect such right, title and interest in Assignee.

Assignor will communicate to Assignee any facts known to us respecting any improvements; and, at the expense of Assignee, Assignor will testify in any legal proceedings, sign all lawful papers, execute all divisional, continuation, continuation-in-part, reissue and substitute applications, make lawful oaths and declarations, and generally do everything possible

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to vest title in Assignee and to aid Assignee to obtain and enforce proper protection for said inventions in all countries.

This Assignment shall be binding on the parties' successors, assigns and legal representatives.

	Patent No.	Issue Date	Title
1	5,856,788	January 5, 1999	METHOD AND APPARATUS FOR RADIOFREQUENCY IDENTIFICATION TAGS
2	6,229,443	May 8, 2001	APPARATUS AND METHOD FOR DETUNING OF RFID TAG TO REGULATE VOLTAGE
3	6,890,264	February 10, 2004	SELECTIVE CLOAKING CIRCUIT FOR USE IN A RADIOFREQUENCY IDENTIFICATION AND METHOD OF CLOAKING RFID TAGS
4	7,064,653	June 20, 2006	SELECTIVE CLOAKING CIRCUIT FOR USE IN RADIO FREQUENCY IDENTIFICATION AND METHOD OF CLOAKING RFID TAGS

Assignor hereby grants NOEL C. GILLESPIE, Registration No. 47,596 and all practitioners with the firm of Procopio, Cory, Hargreaves & Savitch LLP (Customer Number 27189) with an office at 525 B Street, Suite 2200, San Diego, CA 92101, the power to insert on this Assignment any further identification which may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office for recordation of this document.

The undersigned further declare that all statements made herein of his/her own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful and false statements may jeopardize the validity of the application or any patent issued thereon.

Assignor:

Single Chip Systems Corp.

By:

Name and Title:

Francisco Martínez de Velasco Cortina, CEO

Date:

JULY 29 2011