

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Edward R. Vokoun	02/23/1994
Miguel A. Delgado	02/24/1994
Gregory N. Carter	02/23/1994
Brian D. Richardson	03/17/1994
Rajive Dhar	03/11/1994
Elizabeth A. Chambers	03/14/1994
RECEIVING PARTY DATA	
Name:	VLSI TECHNOLOGY INC.
Street Address:	1109 MCKAY DRIVE
City:	SAN JOSE
State/Country:	CALIFORNIA
Postal Code:	95131
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	5943591
CORRESPONDENCE DATA	
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Correspondent Name:	NXP SEMICONDUCTORS IP&L DEPT.
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ATTORNEY DOCKET NUMBER:	5943591
NAME OF SUBMITTER:	Vilimaina Naga
Total Attachments: 1 source=5943591-Assignmentstofile#page1.tif	

CH \$40.00 5943591

Assignment

Whereas, we, Edward R. Vokoun; Miguel A. Delgado; Gregory N. Carter;
Brian D. Richardson; Rajive Dhar; Elizabeth A. Chambers
 executed employment agreements with VLSI Technology, Inc. (hereinafter called VLSI), a Delaware Corporation having its principal place of business at 1109 McKay Drive, San Jose, California, 95131, whereby we have agreed to assign to VLSI all inventions (except as otherwise limited by law) which relate to VLSI's business and which were first conceived or actually reduced to practice during our employment by VLSI;

Now, therefor, pursuant to the terms of said employment agreements and for good and valuable consideration, receipt of which is hereby acknowledged, we do hereby sell, assign and transfer unto VLSI, its successors and assigns, the entire right, title and interest, including the right of priority in, to and under an application for Letters Patent of the United States entitled:

-INTEGRATED CIRCUIT SCRIBE LINE STRUCTURES AND METHODS FOR MAKING SAME

_____ filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; or

x Serial No.: 08/176,353 filed on 12/30/93 and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor;

And for the above consideration, we agree promptly upon request of VLSI, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to VLSI, its successors and assigns, the inventions described in said application and all patent rights therein, in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me in lending such cooperation and assistance are paid by VLSI.

In witness whereof, we hereunto set our hands and seal:

Inventor's Signature: <u>Edward R. Vokoun</u>	Date: <u>2/23/94</u>
Inventor's Signature: <u>Miguel A. Delgado</u>	Date: <u>2/24/94</u>
Inventor's Signature: <u>Gregory N. Carter</u>	Date: <u>2/23/94</u>
Inventor's Signature: <u>Brian D. Richardson</u>	Date: <u>3/17/94</u>
Inventor's Signature: <u>Rajive Dhar</u>	Date: <u>3/11/94</u>
Inventor's Signature: <u>Elizabeth A. Chambers</u>	Date: <u>3/14/94</u>
Inventor's Signature: _____	Date: _____
Inventor's Signature: _____	Date: _____

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