

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Dialog Semiconductor GmbH	02/13/2006
RECEIVING PARTY DATA	
Name:	Dialog Imaging System GmbH
Street Address:	Neue Strasse 95
City:	Kirchheim/Teck-Nabern
State/Country:	GERMANY
PROPERTY NUMBERS Total: 2	
Property Type	Number
Patent Number:	7256828
Application Number:	11891823
CORRESPONDENCE DATA	
Fax Number:	(845)471-2064
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	845-452-5863
Email:	cathys@saileackerman.com
Correspondent Name:	Saile Ackerman LLC
Address Line 1:	28 Davis Avenue
Address Line 4:	Poughkeepsie, NEW YORK 12603
ATTORNEY DOCKET NUMBER:	DIS ADDITIONAL RECORDING
NAME OF SUBMITTER:	Stephen B Ackerman
Total Attachments: 24 source=Dialog Imaging Systems - Redacted shareholder agreement 42.17.06#page1.tif source=Dialog Imaging Systems - Redacted shareholder agreement 42.17.06#page2.tif source=Dialog Imaging Systems - Redacted shareholder agreement 42.17.06#page3.tif source=Dialog Imaging Systems - Redacted shareholder agreement 42.17.06#page4.tif source=Dialog Imaging Systems - Redacted shareholder agreement 42.17.06#page5.tif	

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Protocol n. 284/K/2006

DIALOG IMAGING SYSTEMS GMBH

INVESTMENT AND SHAREHOLDERS' AGREEMENT

DECHERT LLP

PATENT
REEL: 026726 FRAME: 0003

acting for

- (a) Roland Pudelko,
date of birth 11.11.1952,
Ettwiesenweg 10, 72585 Riederich,
- (b) Hubert Utz,
date of birth 06.06.1953,
Agnes-Bernauer-Strasse 6, 82239 Alling,
- (c) Martin Sallenhag,
date of birth 28.11.1968,
In den Gassenäckern 7, 73249 Wernau,

as well as on behalf of

- (d) Dialog Imaging Systems GmbH, seated in Kirchheim,
Neue Strasse 95, 73230 Kirchheim / Teck-Nabern

registered with the commercial register at the Local Court of Kirchheim / Teck-Nabern under HRB 2076,

and

- (e) Hektor Vierte GmbH & Co. KG to be renamed into DIS Management Beteiligungs GmbH & KG, Neue Strasse 95, 7320 Kirchheim / Teck-Nabern

registered with the commercial register at the Local Court of Munich under HRA 86966,

[REDACTED]

[REDACTED]

The persons appearing requested to notarize this agreement in the English language. The notary has the capacity to comprehend and speak English. He convinced himself that the persons appearing comprehend and speak English, too.

The notary public pointed out that, before the recording, he has to ask the persons appearing whether he or any of his partners had already been or was presently active, outside of an official capacity, in the following recorded matter. The persons appearing declared that this is not the case.

Upon the request of those in appearance, I certify the orally expressed declarations of those concurrently present made before me according to the following:

INVESTMENT AND SHAREHOLDERS' AGREEMENT

between

1. **Dialog Semiconductor GmbH**
Neue Strasse 95, 73230, Kirchheim / Teck-Nabern, Germany

- hereinafter "Dialog" -

[REDACTED]

RECITALS

Dialog holds a share in the nominal amount of EUR 25,000 in the Company, which shall be defined as "Ordinary Share A".

The Company was incorporated as a shell company on 2 June 2005 and then bought by Dialog on 17 October 2005. By way of an Asset Contribution Agreement and with economic effect as of 1 January 2006, Dialog has transferred its imaging business including without limitation the image sensors semiconductors and camera modules business ("Imaging Business") on 13 February 2006 into the Company. The assets of the Imaging Business consisted of but were not limited to IP rights, test and laboratory equipment, employees, supply agreements, customer agreements and office equipment. No capital increase was made in connection with the contribution of the Imaging Business into the Company but the transferred assets were booked in the free capital reserves of the Company according to § 272 Abs. 2 Nr. 4 HGB. All outstanding shares in Dialog Imaging Systems Inc., a company existing and duly incorporated under the laws of the State of Delaware ("DIS Inc.") were part of the assets transferred by Dialog to the Company. The Company and its wholly owned subsidiary DIS Inc. hereinafter collectively ("DIS Group").

[REDACTED]

2. Patents

a) Patents acquired from **Sarnoff** including Patents/Patent Applications of Dialog outside the US regarding Sarnoff Patents acquired (limitations see column Status and No. 7. of this schedule F.1 Contracts No. 1-4)

Docket No.	Title	Status	Application No.	Filing Date/dd-mm-yy	Patent No.	Issue Date	Country	Expiration	Remarks
DSR10015B	X-Y Addressable Imager With Variable Integration	Granted	07/635452	28-Dec-90	5134488	28-Jul-92	US	28-Dec-10	
DSR10015A	X-Y Addressable Solid State Imager For Low Noise Operation	Granted	07/643893	28-Dec-90	5134489	28-Jul-92	US	28-Dec-10	
DSR11994(EU)	CMOS Image Sensor With Reduced Fixed Pattern Noise	Abandoned	98925089.9	02-Jun-98			EP		
DSR11994A(EU)	CMOS Image Sensor With Improved Fill Factor	Granted	98926174.8	01-Jun-98	0986898	03-Aug-05	EP		
DSR11994(JP)	CMOS Image Sensor With Reduced Fixed Pattern Noise	Pending	11-502656	02-Jun-98			JP		DE,FR,UK
DSR11994A(JP)	CMOS Image Sensor With Improved Fill Factor	Pending	11-501011	01-Jun-98			JP		
DSR11994A(KR)	CMOS Image Sensor With Improved Fill Factor	Pending	99-7011301	01-Jun-98			KR		
DSR11994(KR)	CMOS Image Sensor With Reduced Fixed Pattern Noise	Pending	99-7011298	02-Jun-98			KR		
DSR11994(TW)	CMOS Image Sensor With Reduced Fixed Pattern Noise	Granted	87108231	27-May-98	NI-108567	11-Jan-99	TW	11-Jan-17	
DSR11994	CMOS Image Sensor With	Granted	08/867653	02-Jun-97	6320616	20-Nov-01	US	02-Jun-17	

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	Reduced Fixed Pattern Noise							
DSR11994A	CMOS Image Sensor With Improved Fill Factor	Granted	08/867577	02-Jun-98	5926345	07-Jun-99	US	02-Jun-18
DSR12192	Active Pixel Sensor With Ultra Wide Dynamic Range	Closed						
DSR12346(EU)	DC Offset And Gain Correction For CMOS Image Sensor	Granted	98925088.1	02-Jun-98	0986900	03-Aug-05	EP	
DSR12346(JP)	DC Offset And Gain Correction For CMOS Image Sensor	Pending	11-502655	02-Jun-98			JP	DE,FR,UK
DSR12346(KR)	DC Offset And Gain Correction For CMOS Image Sensor	Pending	99-7011300	02-Jun-98			KR	
DSR12346(TW)	DC Offset And Gain Correction For CMOS Image Sensor	Granted	87108079	25-May-98	NI-123111	11-Nov-00	TW	11-Nov-18
DSR12346	DC Offset And Gain Correction For CMOS Image Sensor	Granted	08/867654	02-Jun-97	5969758	19-Okt-99	US	02-Jun-17
DSR12708(EU)	Extended Dynamic Range Image Sensor System	Granted	99927117.4	28-May-99	1082849	29-Sep-2004	EP	
DSR12708(JP)	Extended Dynamic Range Image Sensor System	Pending	00-551532	28-May-99			JP	DE,FR,UK
DSR12708(KR)	Extended Dynamic Range Image Sensor System	Allowed	00-7013393	28-May-99			KR	
DSR12708	Extended Dynamic Range Image Sensor System	Granted	09/087087	29-May-98	6040570	21-Marz-00	US	29-May-18
DSR12708A	Extended Dynamic Range Image Sensor System	Granted	09/497898	04-Feb-00	6441852	27-Aug-02	US	04-Feb-20

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DSR12955	Multiplexing Prism CMOS Imagers To A Single Data Bus	Pending	10/766648	27-Jan-04			US
DSR12955(EU)	Multiplexing Prism CMOS Imagers To A Single Data Bus	Pending	04392005.7	24-Feb-04			EP
DSR12955(JP)	Multiplexing Prism CMOS Imagers To A Single Data Bus	Pending	2004-49326	25-Feb-04			JP
DSR12955(KR)	Multiplexing Prism CMOS Imagers To A Single Data Bus	Pending	10-2004-0013071	26-Feb-04			KR
DSR13451	Method And Apparatus For Dual Resolution Foveating Display Output	Pending	09/510812	23-Feb-00			US
DSR13451(EU)	Method And Apparatus For Dual Resolution Foveating Display Output	Published	00915857.7	24-Feb-00			EP
DSR13451(JP)	Method And Apparatus For Dual Resolution Foveating Display Output	Pending	00-601837	24-Feb-00			JP
DSR13531	Simultaneous Multiple Line Readout of CMOS APS Imagers	Pending	10/744566	27-Jan-04			US
DSR13531(EU)	Simultaneous Multiple Line Readout of CMOS APS Imagers	Pending	04392006.5	24-Feb-04			EP
DSR13531(JP)	Simultaneous Multiple Line Readout of CMOS APS Imagers	Pending	2004-49359	25-Feb-04			JP
DSR13531(KR)	Simultaneous Multiple Line Readout of CMOS APS Imagers	Pending	10-2004-0013062	26-Feb-04			KR

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DSR13829	Fully Integrated Solid State Imager	Pending	10/744565	23-Dec-03			US	
DSR13829(EU)	Fully Integrated Solid State Imager	Pending	04368040.4	13-May-04			EU	
DSR13829(JP)	Fully Integrated Solid State Imager	Pending	2004-143502	13-May-04			JP	
DSR13829(KR)	Fully Integrated Solid State Imager	Pending	10-2004-0033955	13-May-04			KR	
DSR13829(CH)	Fully Integrated Solid State Imager	Pending	200410044541.0	12-May-04			CH	
DSR13977	Fully Integrated Solid State Imager And Cam And Circuitry	Granted	10/226383	23-Aug-02	6992713	31-Jan-06	US	23-Aug-22
DSR13977(EU)	Fully Integrated Solid State Imager And Cam And Circuitry	Pending	02768668.2				EU	Nat'l Stage App. of PCT/US02/26818
DSR13977(JP)	Fully Integrated Solid State Imager And Cam And Circuitry	Pending	2003-524161	23-Aug-02			JP	Nat'l Stage App. of PCT/US02/26818
DSR13977(KR)	Fully Integrated Solid State Imager And Cam And Circuitry	Pending	10-2003-7005708	24-Apr-03			KR	Nat'l Stage App. of PCT/US02/26818
DSR13977(CH)	Fully Integrated Solid State Imager And Cam And Circuitry	Pending	02802741.8	23-Aug-02			CH	Nat'l Stage App. of PCT/US02/26818
DSR14056	Floating Gate Amplifier CMOS APS Pixel Which Eliminates Reset Noise At The Pixel Level	Closed						
DSR14195	APS Pixel With Reset Noise Suppression And Programmable Binning Capability	Granted	10/339189	09-Jan-03	6878918	12-Apr-05	US	09-Jan-23
DSR14195(EU)	APS Pixel With Reset	Pending	04368002.4	08-Jan-04			EU	

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	Noise Suppression And Programmable Binning Capability							
DSR14195(JP)	APS Pixel With Reset Noise Suppression And Programmable Binning Capability	Pending	2004-3819	09-Jan-04			JP	
DSR14195(KR)	APS Pixel With Reset Noise Suppression And Programmable Binning Capability	Pending	10-2004-0001696	09-Jan-04			KR	
DSR14198/14199	Cmos Pixel Using Vertical Structure And Sub-Micron CMOS Process	Granted	10/439925	16-May-03	6911712	28-Jun-05	US	16-May-23
DSR14198/14199 (EU)	Cmos Pixel Using Vertical Structure And Sub-Micron CMOS Process	Pending	04368027.1	14-Apr-04			EU	
DSR14198/14199 (JP)	Cmos Pixel Using Vertical Structure And Sub-Micron CMOS Process	Pending	2004-118056	13-Apr-04			JP	
DSR14198/14199 (KR)	Cmos Pixel Using Vertical Structure And Sub-Micron CMOS Process	Pending	10-2004-0025699	14-Apr-04			KR	
DSR14198/14199 (CH)	Cmos Pixel Using Vertical Structure And Sub-Micron CMOS Process	Pending	200410034368.6	14-Apr-04			CH	
DSR14200	CMOS Pixel With Dual Gate PMOS Transistor	Granted	10/339190	09-Jan-03	6870209	22-Mrz-05	US	09-Jan-23
DSR14200B	CMOS Pixel With Dual Gate PMOS	Pending	11/068283	28-Feb-05			US	

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	Transistor						
DSR14200D	CMOS Pixel With Dual Gate PMOS Transistor	Pending	11/068365	28-Feb-05			US
DSR14200(EU)	CMOS Pixel With Dual Gate PMOS Transistor	Pending	04368001.5	08-Jan-04			EU
DSR14200(JP)	CMOS Pixel With Dual Gate PMOS Transistor	Pending	2004-118056	14-Apr-04			JP
DSR14200(KR)	CMOS Pixel With Dual Gate PMOS Transistor	Pending	10-2004-0025699	14-Apr-04			CH
DSR14304	Auto Registered Component RGB Imaging Method	Issued	10/647719	25-Aug-03	6984812	10-Jan-06	US
DSR14304(EU)	Auto Registered Component RGB Imaging Method	Pending	04392007.3	24-Feb-04			EU
DSR14304(JP)	Auto Registered Component RGB Imaging Method	Pending	2004-50715	26-Feb-04			JP
DSR14304(KR)	Auto Registered Component RGB Imaging Method	Pending	10-2004-0013063	26-Feb-04			KR
DSR14332	Scan-Line Based Pixel Offset Corrector Hardware Algorithm For Noise Reduction In Imaging	Closed					
DSR14446	Vertical Charge Transfer Active Sensor	Pending	10/706839	12-Nov-03			US
DSR14446(EU)	Vertical Charge Transfer Active Sensor	Pending	04192008.1	24-Feb-04			EU
DSR14446(JP)	Vertical Charge Transfer Active Sensor	Pending	2004-50727	26-Feb-04			JP
DSR14446(KR)	Vertical Charge Transfer	Pending	10-2004-0013064	26-Feb-04			KR

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	Active Sensor					
DSR14452	RED/GREEN Pixel With Simultaneous Exposure And Improved MTF	Pending	10/813864	31-Mar-04		US
DSR14452(EU)	RED/GREEN Pixel With Simultaneous Exposure And Improved MTF	Pending	04368028.9	14-Apr-04		EU
DSR14452(JP)	RED/GREEN Pixel With Simultaneous Exposure And Improved MTF	Pending	2004-118585	14-Apr-04		JP
DSR14452(KR)	RED/GREEN Pixel With Simultaneous Exposure And Improved MTF	Pending	10-2004-0025665	14-Apr-04		KR
DSR14452(CH)	RED/GREEN Pixel With Simultaneous Exposure And Improved MTF	Pending	200410079428.6	26-Feb-04		CH
DSR14460	CMOS APS Readout Scheme That Combines Reset Drain Current And The Source Follower Output	Pending	10753638	08-Jan-04		US
DSR14460(EU)	CMOS APS Readout Scheme That Combines Reset Drain Current And The Source Follower Output	Pending	04392003.2	24-Feb-04		EU
DSR14460(JP)	CMOS APS Readout Scheme That Combines Reset Drain Current And The Source Follower Output	Pending	2004-49330	25-Feb-04		JP
DSR14460(KR)	CMOS APS Readout Scheme That Combines Reset Drain	Pending	10-2004-0013041	26-Feb-04		KR

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Application

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DSR14725

DSR13736

Current And The Source Follower Output							
Tunneling Floating Gate APS Pixel	Granted	10/627796	25-Jul-03	6897519	24-Mai-05	US	25-Jul-23
High Resolution 3-D Imaging Range Finder (High Resolution Imager)	Granted	09/772361	30-Jun-01	6535275	18-Mrz-03	US	30-Jan-21

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b) Dialog's Initiated Patents:

Docket No.	Title	Status	Application No.	Filing Date/dd-mm-yy	Patent No.	Issue Date	Country	Expiration	Remarks
DS02-029	Weighted Gradient Based And Color Corrected Interpolation	Pending	10/347484	21-Jan-03			US		
DS02-029(EU)	Weighted Gradient Based And Color Corrected Interpolation	Pending	03392002.6	16-Jan-03			EU		
DS02-029(JP)	Weighted Gradient Based And Color Corrected Interpolation	Pending	2004-9679	16-Jan-04			JP		
DS02-029(KR)	Weighted Gradient Based And Color Corrected Interpolation	Pending	10-2004-0003433	16-Jan-04			KR		
DS02-030	Pixel With Variable Resolution During Exposure	Pending	10/619240	14-Jul-03			US		
DS02-030(EU)	Pixel With Variable Resolution During Exposure	Pending	04368048.7	13-Jul-04			EU		
DS02-030(JP)	Pixel With Variable Resolution During Exposure	Pending	2004-202843	09-Jul-04			JP		
DS02-030(KR)	Pixel With Variable Resolution During Exposure	Pending	10-2004-0054825	14-Jul-04			KR		
DS02-030(CH)	Pixel With Variable Resolution During Exposure	Pending	00410069840.X				CH		
DS03-008	Miniature Camera Module	Pending	10/417317	16-Apr-03			US		
DS03-008(EU)	Miniature Camera Module	Withdrawn	03368025.7	01-Apr-03			EU		
DS03-012	Test System For Camera Modules	Pending	10/434743	08-May-03			US		
DS03-012(EU)	Test System For Camera Modules	Pending	03392003.4	27-Mar-03			EU		
DS03-021	Extended Dynamic Range In Color Imagers	Pending	10/859797	03-Jun-04			US		
DS03-021(EU)	Extended Dynamic Range In Color Imagers	Pending	04392024.8	07-May-04			EU		
DS03-025	Image Resolution Conversion	Pending	10/830329	22-Apr-04			US		
DS03-025(EU)	Image Resolution Conversion	Pending	04392013.1	30-Mar-04			EU		
DS03-026	Zoom Algorithm	Pending	10/830154	22-Apr-04			US		
DS2003026(EU)	Zoom Algorithm	Pending	04392015.6	30-Mar-04			EU		
DS04-003	Imaging Sensors	Pending	10/874505	22-Apr-04			US		
DS04-003(EU)	Imaging Sensors	Pending	04392027.1	14-Jun-04			EU		
DS04-009	Self Adjusting Transfer Gate APS	Pending	11/028772	04-Jan-05			US		

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DS04-009(EU)	Self Adjusting Transfer Gate APS	Pending	05392014.6	29-Dec-05			EU
DS04-010	Suppression Of Noise In Pixel VDD Supply	Pending	11/028745	04-Jan-05			US
DS04-010(EU)	Suppression Of Noise In Pixel VDD Supply	Pending	05392013.8	29-Dec-05			EU
DS04-011	CMOS Active Pixel Sensor With Improved Dark Current And Sensitivity	Pending	11/036937	14-Jan-05			US
DS04-011(EU)	CMOS Active Pixel Sensor With Improved Dark Current And Sensitivity	Pending	06392001.0	10-Jan-06			EU
DS04-021	On-Chip Real Time FPN Correction Without Imager Size Memory	Pending	11/132769	19-May-05			US
DS04-022	A Camera Test System	Pending	10/930351	31-Aug-04			US
DS04-022(EU)	A Camera Test System	Pending	04368054.5	17-Aug-04			EU
DS04-023	A Camera Handling System	Pending	10/929651	30-Aug-04			US
DS04-023(EU)	A Camera Handling System	Pending	04368055.2	17-Aug-04			EU
DS04-024	An Intelligent Light Source With Synchronization With A Digital Camera	Pending	10/930353	31-Aug-04			US
DS04-024(EU)	An Intelligent Light Source With Synchronization With A Digital Camera	Pending	04368056.0	17-Aug-04			EU
DS04-025	Multi-Processing Of A Picture To Speed Up Mathematics And Calculation For One Picture	Pending	10/929652	30-Aug-04			US
DS04-025(EU)	Multi-Processing Of A Picture To Speed Up Mathematics And Calculation For One Picture	Pending	04368057.8	17-Aug-04			EU
DS04-026	Focus Processing With The Distance Of Different Target Wheels	Pending	10/929300	30-Aug-04			US
DS04-026(EU)	Focus Processing With The Distance Of Different Target Wheels	Pending	04368058.6	17-Aug-04			EU
DS04-027	Testing Of Miniaturized Digital Camera Modules With Electrical And/Or Optical Zoom Functions	Pending	10/929653	30-Aug-04			US
DS04-027(EU)	Testing Of Miniaturized Digital Camera Modules With Electrical And/Or Optical Zoom Functions	Pending	04368059.4	17-Aug-04			EU

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DS04-036	Pixel Binning/Averaging Approach On Large Format Color, CMOS Image Sensor Decimation	Pending	10/999875	30-Nov-04			US	
DS04-036(EU)	Pixel Binning/Averaging Approach On Large Format Color, CMOS Image Sensor Decimation	Pending	04392044.6	23-Nov-04			EU	
DS04-042	Single Chip Stereo Imaging System With Dual Array Design	Pending	10/966124	15-Oct-04			US	
DS04-042(EU)	Single Chip Stereo Imaging System With Dual Array Design	Pending	04368066.9	08-Oct-04			EU	
DS04-045	A Column Averaging/Row Averaging... In High Intensity Light Environment	Pending	10/999843	30-Nov-04			US	
DS04-045(EU)	A Column Averaging/Row Averaging... In High Intensity Light Environment	Pending	04392046.1	23-Nov-04			EU	
DS04-046	A Column Averaging/Row Binning... Resolution Adjustment In Lower Intensity Light	Pending	10/997383	24-Nov-04			US	
DS04-046(EU)	A Column Averaging/Row Averaging... In High Intensity Light Environment	Pending	04392047.9	23-Nov-04			EU	
DS04-051	Location Based Anti-Flicker Frequency Adjustment For Camera In Mobile Application	Question						
DS04-052	Camera-Based Input Device	D						
DS04-053	IR Camera-Based Fingerprint Detection	Question						
DS04-054	IR Camera-Based Blood Circulation Detection	Question						
DS04-056A	Hexagonal Color Pixel Structure With White Pixels	Pending	11/032538	10-Jan-05			US	
DS04-056A(EU)	Hexagonal Color Pixel Structure With White Pixels	Pending	05368001.3	05-Jan-05			EU	
DS04-056B	Hexagonal Color Pixel Structure With White Pixels	Pending	11/032540	10-Jan-05			US	
DS05-003	Pixel Optimized To Match Mosaic Display Pixel Layout	New						
DS05-005	High Shutter Rejection Ratio For Snap-Shot CMOS Imager	Pending	11/139743	27-May-05			US	
DS05-006	High Shutter Rejection Ratio For	Pending	11/284300	21-Nov-05			US	

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	Snap-Shot For Small Pixel CMOS Imager								
DS05-007	High Performance Imager IC With Minimum Peripheral Circuits	New							
DSR10015A	X-Y Addressable Solid State Imager For Low Noise Operation	Issued	07/643893	28-Dec-90	5134489	28-Jul-92	US		
DSR10015B	X-Y Addressable Imager With Variable Integration	Issued	07/635452	28-Dec-90	5134488	28-Jul-92	US		
DSR11162_XL	Pixel Array Having Image Forming Elements Integral With Peripheral Circuit Elements	Pending							
DSR1162B_XL	Pixel Array Having Image Forming Elements Integral With Peripheral Circuit Elements	Pending							
DSR11994	CMOS Image Sensor With Reduced Fixed Pattern Noise	Granted	08/867653	02-Jun-97	6320616	20-Nov-01	US	20-Nov-19	
DSR11994(EU)	CMOS Image Sensor With Reduced Fixed Pattern Noise	Abandoned	98925089.9	02-Jun-98			EP		
DSR11994(JP)	CMOS Image Sensor With Reduced Fixed Pattern Noise	Pending	11-502656	02-Jun-98			JP		
DSR11994(KR)	CMOS Image Sensor With Reduced Fixed Pattern Noise	Pending	99-7011298	02-Jun-98			KR		
DSR11994(TW)	CMOS Image Sensor With Reduced Fixed Pattern Noise	Granted	87108231	27-May-98	NT-108567	11-Jan-99	TW	11-Jan-17	
DSR11994A	CMOS Image Sensor With Improved Fill Factor	Granted	08/867577	02-Jun-97	5920345	07-Jun-99	US	07-Jun-17	
DSR11994A(EU)	CMOS Image Sensor With Improved Fill Factor	Granted	98926174.8	01-Jun-97	0986898	03-Aug-05	EP		
DSR11994A(JP)	CMOS Image Sensor With Improved Fill Factor	Pending	11-501011	01-Jun-98			JP		
DSR11994A(KR)	CMOS Image Sensor With Improved Fill Factor	Pending	99-7011301	01-Jun-98			KR		
DSR12217_XL	Extended Dynamic Range Imaging System And Method	Issued							
DSR12217B_XL	Extended Dynamic Range Imaging System And Method	Pending							
DSR12295_XL	Method And Apparatus For Performing Image Enhancement	Issued							
DSR12346	DC Offset And Gain Correction For CMOS Image Sensor	Granted	08/867654	02-Jun-97	5969758	19-Oct-99	US	02-Jun-17	
DSR12346(EU)	DC Offset And Gain Correction For CMOS Image Sensor	Granted	98925088.1	06-Feb-98	0986900	03-Aug-05	EP		

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DSR12346(JP)	DC Offset And Gain Correction For CMOS Image Sensor	Pending	11-502655	06-Feb-98			JP	
DSR12346(KR)	DC Offset And Gain Correction For CMOS Image Sensor	Pending	99-7011300	06-Feb-98			KR	
DSR12346(TW)	DC Offset And Gain Correction For CMOS Image Sensor	Granted	87108079	25-May-98	NI-123111	11-Nov-00	TW	11-Nov-18
DSR12708	Extended Dynamic Range Image Sensor System	Issued	09/087087	29-May-98	6040570	21-Mar-00	US	29-May-18
DSR12708(EU)	Extended Dynamic Range Image Sensor System	Granted	99927117.4	28-May-98	1082849	29-Sep-04	EP	
DSR12708(JP)	Extended Dynamic Range Image Sensor System	Pending	00-551532	28-May-98			JP	
DSR12708(KR)	Extended Dynamic Range Image Sensor System	Pending	00-7013393	28-May-98			KR	
DSR12708A	Extended Dynamic Range Image Sensor System	Issued	09/497898	04-Feb-00	6441852	27-Aug-02	US	
DSR12955	Multiplexing Prism CMOS Imagers To A Single Data Bus	Pending	10/766648	27-Jan-04			US	
DSR12955(EU)	Multiplexing Prism CMOS Imagers To A Single Data Bus	Pending	04392005.7	24-Feb-04			EP	
DSR12955(JP)	Multiplexing Prism CMOS Imagers To A Single Data Bus	Pending	2004-49326	25-Feb-04			JP	
DSR12955(KR)	Multiplexing Prism CMOS Imagers To A Single Data Bus	Pending	10-2004-0013071	26-Feb-04			KR	
DSR13451	Method And Apparatus For Dual Resolution Foveating Display Output	RCE filed	09/510812	23-Feb-00			US	
DSR13451C	Method And Apparatus For Dual Resolution Foveating Display Output	RCE filed	09/510812	23-Feb-00			US	
DSR13451CC	Method And Apparatus For Dual Resolution Foveating Display Output	Pending	09/510812	23-Feb-00			US	
DSR13451(EU)	Method And Apparatus For Dual Resolution Foveating Display Output	Published	00915857.7	24-Feb-00			EP	
DSR13451(JP)	Method And Apparatus For Dual Resolution Foveating Display Output	Pending	00-601837	24-Feb-00			JP	
DS02-017	Multilevel Transistor DRAM - Cell Naturalanalog US no 2005/001501 A1	Issued	10/615124	08-Jul-03	6969652	29-Nov-05	US	
DS2005009	A2x2 Photo Sensor Pixel For Pentile Matrix Image Detection	Pending	11/252840	18-Oct-05				
DS2005010	An Integration and Conditioning Readout Circuit for a Pentile	New						

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	Matrix Image Sensor Array							
DS2005012	Compact Size Pixel	Pending	11/301436	13-Dec-05				
DS2005013	Operation of Compact Size Pixel	New						

DSR13531	Simultaneous Readout Of CMOS APS Imagers	Pending	10/744566	27-Jan-04				
DSR13531(EU)	Simultaneous Multiple Line Readout of CMOS APS Imagers	Pending	04392006.5	24-Feb-04			EP	
DSR13531(JP)	Simultaneous Multiple Line Readout of CMOS APS Imagers	Pending	2004-49359	25-Feb-04			JP	
DSR13531(KR)	Simultaneous Multiple Line Readout of CMOS APS Imagers	Pending	10-2004-0013062	26-Feb-04			KR	
DSR13648_XL	CMOS/CCD Line Transfer With Low Dark Current	Allowed						
DSR13736	High Resolution 3-D Imaging Range Finder	Issued	09/772361	30-Jan-01	6533275	18-Mar-03	US	30-Jan-21
DSR13829	Method Apparatus Simultaneous Multi-Field-Of-View Imager Using Digital Subsampling...	Pending	10/744566	23-Dec-03			US	
DSR13829(EU)	Fully Integrated Solid State Imager	Pending	04368040.4	13-May-04			EU	
DSR13829(JP)	Fully Integrated Solid State Imager	Pending	2004-143502	13-May-04			JP	
DSR13829(KR)	Fully Integrated Solid State Imager	Pending	10-2004-0033955	13-May-04			KR	
DSR13829(CH)	Fully Integrated Solid State Imager	Pending	200410044541.0	12-May-04			CH	
DSR13977	Fully Integrated Solid State Imager And Camera Circuitry	Pending						
DSR13977(EU)	Fully Integrated Solid State Imager And Cam And Circuitry	Pending	02768668.2				EU	
DSR13977(JP)	Fully Integrated Solid State Imager And Cam And Circuitry	Pending	2003-524161	23-Aug-02			JP	
DSR13977(KR)	Fully Integrated Solid State Imager And Cam And Circuitry	Pending	10-2003-7005708	24-Apr-03			KR	
DSR13977(CH)	Fully Integrated Solid State Imager And Cam And Circuitry	Pending	02802741.8	23-Aug-02			CH	
DSR14043_XL	Addressable Imager With Real Time Defect Detection And Substitution	Pending						
DSR14056		Closed						
DSR14195	APS Pixel With Reset Noise Suppression And Programmable Binning Capability	Issued	10/339189	09-Jan-03	6878918	12-Apr-05	US	09-Jan-23
DSR14195(EU)	APS Pixel With Reset Noise Suppression And Programmable Binning Capability	Pending	04368002.4	08-Jan-04			EU	
DSR14195(JP)	APS Pixel With Reset Noise Suppression And Programmable Binning Capability	Pending	2004-3819	09-Jan-04			JP	
DSR14195(KR)	APS Pixel With Reset Noise Suppression And Programmable Binning Capability	Pending	10-2004-0001696	09-Jan-04			KR	

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DSR14198/14199	CMOS Pixel Using Vertical Structure And Sub-Micron CMOS Process	Issued	10/439925	16-May-03	6911712	12-Apr-05	US	16-May-23
DSR14198/14199(EU)	Cmos Pixel Using Vertical Structure And Sub-Micron CMOS Process	Pending	04368027.1	14-Apr-04			EU	
DSR14198/14199(JP)	Cmos Pixel Using Vertical Structure And Sub-Micron CMOS Process	Pending	2004-118056	13-Apr-04			JP	
DSR14198/14199(KR)	Cmos Pixel Using Vertical Structure And Sub-Micron CMOS Process	Pending	10-2004-0025699	14-Apr-04			KR	
DSR14198/14199(CH)	Cmos Pixel Using Vertical Structure And Sub-Micron CMOS Process	Pending	200410034368.6	14-Apr-04			CH	
DSR14200	CMOS Pixel With Dual Gate PMOS	Issued	10/339190	09-Jan-03	6870209	22-Mar-05	US	09-Jan-23
DSR14200B	CMOS Pixel With Dual Gate PMOS	P	11/068283	28-Feb-05			US	
DSR14200D	CMOS Pixel With Dual Gate PMOS	P	11/068365	28-Feb-05			US	
DSR14200(EU)	CMOS Pixel With Dual Gate PMOS Transistor	Pending	04368001.6	08-Jan-04			EU	
DSR14200(JP)	CMOS Pixel With Dual Gate PMOS Transistor	Pending	2004-118056	14-Apr-04			JP	
DSR14200(KR)	CMOS Pixel With Dual Gate PMOS Transistor	Pending	10-2004-0025699	14-Apr-04			CH	
DSR14200B	CMOS Pixel With Dual Gate PMOS	Pending	11/068283	28-Feb-05			US	
DSR14200D	CMOS Pixel With Dual Gate PMOS	Pending	11/068365	28-Feb-05			US	
DSR14304	Auto Registered Component RGB Imaging Method	Issued	10/647719	25-Aug-03	6984812	10-Jan-06	US	25-Aug-23
DSR14304(EU)	Auto Registered Component RGB Imaging Method	Pending	04392007.3	24-Feb-04			EU	
DSR14304(JP)	Auto Registered Component RGB Imaging Method	Pending	2004-50715	26-Feb-04			JP	
DSR14304(KR)	Auto Registered Component RGB Imaging Method	Pending	10-2004-0013063	26-Feb-04			KR	
DSR14332		Closed						
DSR14446	Vertical Charge Transfer Active Pixel Sensor	Pending	10/706839	12-Nov-03			US	
DSR14446(EU)	Vertical Charge Transfer Active Sensor	Pending	04392008.1	24-Feb-04			EU	
DSR14446(JP)	Vertical Charge Transfer Active Sensor	Pending	2004-50727	26-Feb-04			JP	
DSR14446(KR)	Vertical Charge Transfer Active Sensor	Pending	10-2004-0013064	26-Feb-04			KR	
DSR14460	CMOS APS Readout Scheme That Combines Reset Drain Current And The Source Follower Output	Pending	10/753638	08-Jan-04			US	
DSR14460(EU)	CMOS APS Readout Scheme That Combines Reset Drain Current And The Source Follower Output	Pending	04392003.2	24-Feb-04			EU	
DSR14460(JP)	CMOS APS Readout Scheme That Combines Reset Drain Current And The Source Follower Output	Pending	2004-49330	25-Feb-04			JP	
DSR14460(KR)	CMOS APS Readout Scheme That Combines Reset Drain Current And The Source Follower Output	Pending	10-2004-0013041	26-Feb-04			KR	

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DYM00-001	Method And Apparatus For Color Interpolation	Pending	09/496607	02-Feb-00			US	
DYM00-002	Method And Apparatus For Color Compensation	Pending	09/495971	02-Feb-00			US	
DYM00-004	Method And Apparatus For Fixed Pattern Noise	Pending	09/496533	02-Feb-00			US	
DYM00-005	Method And Apparatus For Controlling Pixel Sensor Elements	Issued	09/496534	02-Feb-00	6888568	3-May-05	US	02-Feb-20
DYM00-005B	Method And Apparatus For Controlling Pixel Sensor Elements	Pending	11/113438	02-Feb-00			US	

New	Einreichung des Mitarbeiters
Question	Fragen vom Patentanwalt
RCE filed	Patent eingereicht
P	Pending
Issued	Patent erteilt

_XL = Exclusively Licensed Patents

3. Licenses

a) Exclusive Licenses (by Sarnoff, (limitations see column Status and No. 7. of this schedule F.I Contracts No. 1-4)):

Case No.	Title	Status	App. No.	Filing Date	Patent No.	Issue Date	Country
11162	Pixel Array Having Image Forming Pixel Elements Integral With Peripheral Circuit Elements	Granted	94919213.2	27-May-94	0700582	22-Jul-98	ES
11162	Pixel Array Having Image Forming Pixel Elements Integral With Peripheral Circuit Elements	Granted	94919213.2	27-May-94	0700582	22-Jul-98	FR
11162	Pixel Array Having Image Forming Pixel Elements Integral With Peripheral Circuit Elements	Granted	94919213.2	27-May-94	0700582	22-Jul-98	IT
11162	Pixel Array Having Image Forming Pixel Elements Integral With Peripheral Circuit Elements	Granted	94919213.2	27-May-94	0700582	22-Jul-98	GB
11162	Pixel Array Having Image Forming Pixel Elements Integral With Peripheral Circuit Elements	Granted	94919213.2	27-May-94	69411914.8	22-Jul-98	DE
11162	Pixel Array Having Image Forming Pixel Elements Integral With Peripheral Circuit Elements	Granted	07-500882	27-May-94	3552233	14-May-04	JP
11162	Pixel Array Having Image Forming Pixel Elements Integral With Peripheral Circuit Elements	Granted	08/068340	28-May-93	5336879	9-Aug-94	US
11162	Pixel Array Having Image Forming Pixel Elements Integral With Peripheral Circuit Elements	Pending	90/006003	09-May-01			US
12217	Extended Dynamic Range Imaging System And Method	Granted	98926148.2	01-Jun-98	69828046.6	8-Dec-04	DE
12217	Extended Dynamic Range Imaging System And Method	Granted	98926148.2	01-Jun-98	0986816	8-Dec-04	EP

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[REDACTED]

The protocol including the appendices had been read by the notary to the persons appearing, been approved by them and been signed by them and the notary *manu propria* as follows:

Dr. Hady
H. H. H.
S. S.
P. P.
U. U.
T. T.
H. H.

[Signature]
notary

