

PATENT ASSIGNMENT

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NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Cypress Semiconductor Corporation	06/28/2011
RECEIVING PARTY DATA	
Name:	Sampsons Group, LLC
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Internal Address:	Suite 101
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State/Country:	DELAWARE
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PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	6137308
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NAME OF SUBMITTER:	Jose S. Garcia
Total Attachments: 4 source=2635 Cypress Semi - Multiplexer Exh B (signed)#page1.tif source=2635 Cypress Semi - Multiplexer Exh B (signed)#page2.tif source=2635 Cypress Semi - Multiplexer Exh B (signed)#page3.tif source=2635 Cypress Semi - Multiplexer Exh B (signed)#page4.tif	

OP \$40.00 6137308

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Cypress Semiconductor Corporation, a Delaware corporation, with an office at 198 Champion Court, San Jose, CA 95134-1709 ("**Assignor**"), does hereby sell, assign, transfer, and convey unto Sampsons Group, L.L.C., a Delaware limited liability company, with an address at 160 Greentree Drive, Suite 101; Dover, DE 19904 ("**Assignee**"), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the "**Patent Rights**");

(a) the provisional patent applications, patent applications and patents listed in the table below (the "**Patents**");

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
6,233,294	US	08/17/1999	Method and Apparatus for Accomplishing High Bandwidth Serial Communication Between Semiconductor Devices Bowers, Richard
6,385,263	US	11/10/2000	Method and Apparatus for Accomplishing High Bandwidth Serial Communication Between Semiconductor Devices Bowers, Richard
6,977,981	US	02/22/2001	Method and Apparatus for Reducing Power Requirements in a Multi Gigabit Parallel to Serial Converter Measor, Grahame Christopher
7,230,856	US	10/24/2005	High-Speed Multiplexer Latch Venugopal, Rajesh
5,298,810	US	09/11/1992	BICMOS CMOS/ECL Data Multiplexer Scott, Paul H.
6,208,193	US	01/15/1999	Multiplexer Control Scheme Moyal, Nathan Y.
6,239,646	US	10/29/1998	High-Speed, Multiple-Input

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
			Multiplexer Scheme
6,433,612	US	02/21/2001	Navabi, Mohammad J. Method for Achieving Low Feed-Through And Consistent Transition Delay in a Multiplexor Circuit
6,778,004	US	12/20/2002	Jenkins, Julian L. Decoupling Capacitor Multiplier
6,370,140	US	01/20/1998	Jackson, Scott A. Programmable Interconnect Matrix Architecture for Complex Programmable Logic Device
6,137,308	US	01/20/1998	Nayak, Anup Programmable interconnect matrix architecture for complex programmable logic device
			Nayak, Anup

(b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, and/or (ii) for which any of the Patents directly or indirectly forms a basis for priority;

(c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisionals, registrations of any item in any of the foregoing categories (a) and (b);

(d) all foreign patents, foreign patent applications, and foreign counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;

(e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents below and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;

(f) inventions, invention disclosures, and discoveries described in any of the Patents and/or any item in the foregoing categories (b) through (e) that (i) are included in any claim in the Patents and/or any item in the foregoing categories (b) through (e), (ii) are subject matter capable of being reduced to a patent claim in a reissue or reexamination proceeding brought on any of the Patents and/or any item in the foregoing categories (b) through (e), and/or (iii) could have been included as a claim in any of the Patents and/or any item in the foregoing categories (b) through (e), except subject matter incorporated by reference in any of the Patents;

(g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (f), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;

(h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

- (1) damages,
- (2) injunctive relief, and
- (3) any other remedies of any kind

for past, current, and future infringement; and

(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h).

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

Assignor will, at the reasonable request of Assignee, do all things necessary, proper, or advisable, including without limitation, the execution, acknowledgment, and recordation of specific assignments, oaths, declarations, and other documents on a country-by-country basis, to assist Assignee in obtaining, perfecting, sustaining, and/or enforcing the Patent Rights.

The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at San Jose,
California on JUNE 28, 2011.

ASSIGNOR:

Cypress Semiconductor Corporation

By: _____

Name: Cathal G. Phelan

Title: Chief Technical Officer

(Signature MUST be attested)

ATTESTATION OF SIGNATURE PURSUANT TO 28 U.S.C. § 1746

The undersigned witnessed the signature of Cathal G. Phelan to the above
Assignment of Patent Rights on behalf of Cypress Semiconductor Corporation
and makes the following statements:

1. I am over the age of 18 and competent to testify as to the facts in this
Attestation block if called upon to do so.
2. Cathal G. Phelan is personally known to me (or proved to me on the basis
of satisfactory evidence) and appeared before me on
June 28, 2011 to execute the above Assignment of
Patent Rights on behalf of Cypress Semiconductor Corporation.

3. Cathal G. Phelan subscribed to the above Assignment of Patent Rights on
behalf of Cypress Semiconductor Corporation.
I declare under penalty of perjury under the laws of the United States of America
that the statements made in the three (3) numbered paragraphs immediately
above are true and correct.

EXECUTED on 6/28/11 (date)

Print Name: _____

