

**PATENT ASSIGNMENT**

Electronic Version v1.1  
 Stylesheet Version v1.1

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
SanDisk Corporation	04/04/2011
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	SanDisk Technologies Inc.
<b>Street Address:</b>	Two Legacy Town Center
<b>Internal Address:</b>	6900 North Dallas Parkway
<b>City:</b>	Plano
<b>State/Country:</b>	TEXAS
<b>Postal Code:</b>	75024
<b>PROPERTY NUMBERS Total: 2</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	13235781
<b>Application Number:</b>	13235852
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(415)369-9665
<b>Phone:</b>	415-369-9660
<b>Email:</b>	lmiller@vierramagen.com
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Correspondent Name:</b>	Vierra Magen Marcus & DeNiro LLP
<b>Address Line 1:</b>	575 Market Street, Suite 2500
<b>Address Line 4:</b>	San Francisco, CALIFORNIA 94105
<b>ATTORNEY DOCKET NUMBER:</b>	SAND01057US1&US2-SDUF
<b>NAME OF SUBMITTER:</b>	Brian I. Marcus

CH \$80.00 13235781

Total Attachments: 85  
 source=SDC to SDT Patent-Assignment-April 4, 2011#page1.tif

**501662399**

**PATENT**  
**REEL: 026927 FRAME: 0430**



source=SDC to SDT Patent-Assignment-April 4, 2011#page57.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page58.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page59.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page60.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page61.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page62.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page63.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page64.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page65.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page66.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page67.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page68.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page69.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page70.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page71.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page72.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page73.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page74.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page75.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page76.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page77.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page78.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page79.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page80.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page81.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page82.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page83.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page84.tif  
source=SDC to SDT Patent-Assignment-April 4, 2011#page85.tif

## ASSIGNMENT

**WHEREAS**, SanDisk Corporation, a Delaware corporation, doing business at 601 McCarthy Boulevard, Milpitas, CA 95035 (hereinafter referred to as **ASSIGNOR**), holds right, title and interest throughout the world in and to the patents and patent applications set forth in the attached Exhibit A, together with all divisions, continuations, or continuations-in-part thereof, and all patents issuing thereon including reissues, renewals, substitutions, re-examinations and extensions thereof, and any and all corresponding foreign patents and patent applications (hereinafter referred to as, collectively, **THE ASSIGNED PATENT RIGHTS**);

**WHEREAS**, SanDisk Technologies Inc., a Texas corporation, doing business at Two Legacy Town Center, 6900 North Dallas Parkway, Plano, Texas 75024, (hereinafter referred to as **ASSIGNEE**), is desirous of acquiring the full and exclusive right, title and interest in **THE ASSIGNED PATENT RIGHTS**;

**NOW, THEREFORE**, for good and valuable consideration, the receipt of which is hereby acknowledged, **ASSIGNOR** hereby assigns, transfers and conveys unto the said **ASSIGNEE**, and **ASSIGNEE** accepts, all of **ASSIGNOR**'s right, title and interest throughout the world in and to **THE ASSIGNED PATENT RIGHTS** and to all Letters Patent or applications or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for **THE ASSIGNED PATENT RIGHTS**, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; and **ASSIGNOR** hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents on applications as aforesaid, to issue all patents for **THE ASSIGNED PATENT RIGHTS** to **ASSIGNEE** in accordance with the terms of this **ASSIGNMENT** but subject to reserved rights including but not limited to those previously retained by, granted to, or owned by, the United States government, educational institutions or both and hereby transfers and conveys all rights of action, power and benefit belonging to or accruing from **THE ASSIGNED PATENT RIGHTS** including the right to undertake proceedings to recover past and future damages and claim all other relief in respect of any acts of infringement thereof whether such acts shall have been committed before or after the date of this assignment.

No other rights, immunities, or licenses, including, without limitation, any rights to any intellectual property owned, controlled or licensable by **ASSIGNOR** are granted or assigned to **ASSIGNEE** under this **ASSIGNMENT**, either expressly or by implication, estoppel, or otherwise, other than the rights expressly recited herein.

**ASSIGNOR** hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would knowingly conflict with this Agreement;

**ASSIGNOR** further covenants that **ASSIGNEE** will, upon its lawful request, reasonably be provided with all pertinent facts and documents relating to **THE ASSIGNED PATENT RIGHTS** and legal equivalents as may be known and reasonably accessible to **ASSIGNOR** and that **ASSIGNOR** will testify as to the same in any administrative contest or litigation related thereto and will promptly execute and deliver to **ASSIGNEE** or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce **THE ASSIGNED PATENT RIGHTS** and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

IN TESTIMONY WHEREOF, I hereunto set my hand as of the date indicated below.

SANDISK CORPORATION

Dated: 4<sup>th</sup> April 2011

Judy Bruner  
Name: Judy Bruner  
Title: EVP 2nd CFO

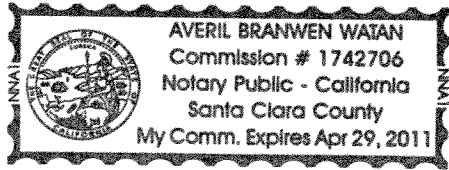
STATE OF California )  
COUNTY OF Santa Clara ) ss.

On this 4<sup>th</sup> day of April, 2011, before me Averil Branwen Watan (Notary Public), the undersigned Notary Public, personally appeared Judy Bruner,

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: Averil Watan  
Signature of Notary Public

We accept the Assignment

SANDISK TECHNOLOGIES INC.

Dated: 4/4/11

James F. Brelsford  
Name: James F. Brelsford  
Title: President and Secretary

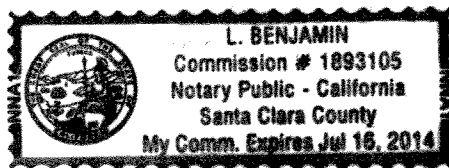
STATE OF California )  
COUNTY OF Santa Clara ) ss.

On this 4 day of April, 2011, before me L. Benjamin, the undersigned Notary Public, personally appeared JAMES F. BRELSFORD

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.



Signature: L. Benjamin  
Signature of Notary Public

**Exhibit A**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
IBM0001.000US	Wilbur D. Pricer	Endurance Management For Solid State Files	US	Issued	7635910	28-Dec-90	5222109	22-Jun-93
INV0169.001US	Carl W. Werner, Andreas M. Haeberli, Leon Sea Jiunn Wong, Cheng-Yuan Michael Wang, Hock C So, Sau C. Wong	Integrated Circuit with Multilevel and Analog Storage Cells and User Selectable Sampling Frequency	US	Issued	9343117	29-Jun-99	6549456	15-Apr-03
INV0169.002US	Carl W. Werner, Andreas M. Haeberli, Leon Sea Jiunn Wong, Cheng-Yuan Michael Wang, Sau C. Wong, Hock C So	Integrated circuit with analog or multilevel storage cells and user-selectable sampling frequency	US	Issued	10359476	5-Feb-03	7106632	12-Sep-06
INV0169.003US	Carl W. Werner, Andreas M. Haeberli, Leon Sea Jiunn Wong, Cheng-Yuan Michael Wang, Hock C So, Sau C. Wong	Integrated circuit with analog or multilevel storage cells and user selectable sampling frequency	US	Issued	11460325	27-Jul-06	7298670	20-Nov-07
INV0169.004US	Carl W. Werner, Andreas M. Haeberli, Leon Sea Jiunn Wong, Cheng-Yuan Michael Wang, Hock C So, Sau C. Wong	Integrated circuit with analog or multilevel storage cells and user selectable sampling frequency	US	Issued	11876093	22-Oct-07	7554844	30-Jun-09
INV0170.000US	James Sansbury, Sau C. Wong	Programmable Impedance Device	US	Issued	9159848	25-Sep-98	6201734	13-Mar-01
INV0170.002US	James Sansbury, Sau C. Wong	Programmable Impedance Device	US	Issued	9967513	27-Sep-01	6456529	24-Sep-02
INV0173.000US	Andreas M. Haeberli, Sau C. Wong, Carl W. Werner, Cheng-Yuan Michael Wang, Leon Sea Jiunn Wong, Hock C So	Charge Pump Circuit Adjustable In Response To An External Voltage Source	US	Issued	9343206	29-Jun-99	6370075	9-Apr-02
INV0173.001US	Sau C. Wong, Carl W. Werner, Leon Sea Jiunn Wong, Andreas M. Haeberli, Cheng-Yuan Michael Wang, Hock C So	Adjustable Circuits For Analog Or Multi-Level Memory	US	Issued	10109353	27-Mar-02	6556465	29-Apr-03
INV0173.002US	Andreas M. Haeberli, Sau C. Wong, Carl W. Werner, Cheng-Yuan Michael Wang, Leon Sea Jiunn Wong, Hock C So	Adjustable Charge Pump Circuit Adjustable In Response To An External Voltage Source	US	Issued	10382333	4-Mar-03	6760262	6-Jul-04
INV0175.000US	Sau C. Wong, Hock C So	High Resolution Analog Storage EPROM and Flash EPROM	US	Issued	8333381	2-Nov-94	5694356	2-Dec-97
INV0175.002US	Sau C. Wong, Hock C So	High Resolution Analog Storage EPROM and Flash EPROM	US	Issued	8585138	11-Jan-96	5638320	10-Jun-97
INV0175.003US	Sau C. Wong, Hock C So	Read Circuits For Analog Memory Cells	US	Issued	8585072	11-Jan-96	5751635	12-May-98
INV0176.000US	Sau C. Wong, Hock C So	MultiBit-Per-Cell Non-Volatile Memory With Error Detection and Correction	US	Issued	8924909	8-Sep-97	5909449	1-Jun-99
INV0176.001US	Sau C. Wong, Hock C So	Multi-Bit-Per-Cell Flash EEPROM Memory With Refresh	US	Issued	9200220	25-Nov-98	6151246	21-Nov-00
INV0176.004US	Hock C So, Sau C. Wong	Multi-Bit-Per-Cell Flash EEPROM Memory with Refresh	US	Issued	11101938	7-Apr-05	7170781	30-Jan-07
INV0176.005US	Hock C So, Sau C. Wong	Multi-Bit-Per-Cell Flash EEPROM Memory with Refresh	US	Issued	11620127	5-Jan-07	7397697	8-Jul-08
INV0177.000US	Sau C. Wong, Hock C So	Non-Volatile Memory With Analog and Digital Interface and Storage	US	Issued	8535441	28-Sep-95	5745409	28-Apr-98
INV0177.001US	Sau C. Wong, Hock C So	Testing Of An Analog Memory Using An On-Chip Digital Input/Output Interface	US	Issued	8847236	1-May-97	5801980	1-Sep-98
INV0177.002US	Sau C. Wong, Hock C So	Combined Digital Write and Analog Rewrite Process for Non-Volatile Memory	US	Issued	8847234	30-Apr-97	5815425	29-Sep-98
INV0178.000US	Sau C. Wong, Hock C So	Pipelined Record And Playback For Analog Non-Volatile Memory	US	Issued	8587310	16-Jan-96	5680341	21-Oct-97



FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
INV0179.000US	Sau C. Wong, Hock C So	Digital Testing of Analog Memory Devices	US	Issued	8598485	8-Feb-96	5682352	28-Oct-97
INV0180.000US	Frank M. Dunlap, Sau C. Wong, Hock C So	Feedback Loop For Reading Threshold Voltage	US	Issued	8622333	26-Mar-96	5748534	5-May-98
INV0181.000US	Frank M. Dunlap, Sau C. Wong, Hock C So	Read Circuit Which Uses A Coarse-To-Fine Search When Reading The Threshold Voltage Of A Memory Cell	US	Issued	8622332	26-Mar-96	5748533	5-May-98
INV0182.000US	Hock C So, Sau C. Wong	Analog And Multi-Level Memory With Reduced Program Disturb	US	Issued	8684788	22-Jul-96	5818757	6-Oct-98
INV0183.000US	Hock C So, Sau C. Wong	Source Biasing In Non-Volatile Memory Having Row-Based Sectors	US	Issued	8781741	10-Jan-97	5923585	13-Jul-99
INV0184.000US	Sau C. Wong, Hock C So	Look-Ahead Erase For Sequential Data Storage	US	Issued	8839288	16-Apr-97	5949716	7-Sep-99
INV0185.000US	Hock C So, Sau C. Wong	Memory Architecture For Recording of Multiple Messages	US	Issued	8867473	2-Jun-97	5909387	1-Jun-99
INV0186.000US	Hock C So, Sau C. Wong	Multiple Array Architecture For Analog Or Multi-Bit-Cell Memory	US	Issued	8889111	7-Jul-97	5896340	20-Apr-99
INV0187.000US	Sau C. Wong, Hock C So	Address Scrambling In A Semiconductor Memory	US	Issued	8986210	5-Dec-97	5943283	24-Aug-99
INV0188.000US	Jing-Lu Gu	Multiple Function, Bi-Directional Input/Output Interface For Sound Processing System	US	Issued	8936559	24-Sep-97	6968065	22-Nov-05
INV0189.000US	Sau C. Wong, Cheng-Yuan Michael Wang, Roger Ying Kuen Lo, Hock C So	Flash Memory Permitting Simultaneous Read/Write and Erase Operations in a Single Memory Array	US	Issued	9199971	25-Nov-98	6345000	5-Feb-02
INV0190.000US	Sau C. Wong	High Resolution Multi-Bit-Per-Cell Memory	US	Issued	9053716	1-Apr-98	6038166	14-Mar-00
INV0191.000US	Sau C. Wong	Non-Linear Mapping Of Threshold Voltages For Analog/Multi-Level Memory	US	Issued	9200205	25-Nov-98	6154157	28-Nov-00
INV0192.000US	Sau C. Wong, Leo Petropoulos, John H. Chan	Analog Multi-Level Memory For Digital Imaging	US	Issued	9086785	28-May-98	6594036	15-Jul-03
INV0193.000US	Sau C. Wong, Hock C So	High-Bandwidth Read and Write Architecture For Non-Volatile Memories	US	Issued	9103623	23-Jun-98	5969986	19-Oct-99
INV0193.001US	Sau C. Wong	High-Bandwidth Read and Write Architecture For Non-Volatile Memories	US	Issued	9128225	3-Aug-98	6134145	17-Oct-00
INV0193.003US	Sau C. Wong	High Data Rate Write Process For Non-Volatile Flash Memories	US	Issued	10042504	18-Oct-01	6606267	12-Aug-03
INV0193.005US	Sau C. Wong	High Data Rate Write Process For Non-Volatile Flash Memories	US	Issued	11184322	18-Jul-05	7349255	25-Mar-08
INV0194.000US	Sau C. Wong	Programmable Arrays For Data Conversions Between Analog And Digital	US	Issued	9159704	23-Sep-98	6169503	2-Jan-01
INV0195.000US	Sau C. Wong, Leo Petropoulos	Analog Buffer Memory For High-Speed Digital Image Capture	US	Issued	9159397	23-Sep-98	7268809	11-Sep-07
INV0196.000US	Sau C. Wong	Data Encryption And Signal Scrambling Using Programmable Conversion Arrays	US	Issued	9200500	25-Nov-98	6498851	24-Dec-02
INV0196.001US	Sau C. Wong	Data Encryption And Signal Scrambling Using Programmable Data Conversion Arrays	US	Issued	10264970	4-Oct-02	6624773	23-Sep-03
INV0197.000US	Leo Petropoulos, Sau C. Wong	Correction Of Corrupted Elements In Sensors Using Analog/Multi-Level Non-Volatile Memory	US	Issued	9224168	31-Dec-98	6760068	6-Jul-04
INV0198.000US	Sau C. Wong	Dynamic Write Process For High Bandwidth Multi-Bit-Per-Cell And Analog/Multi-Level Non-Volatile Memories	US	Issued	9224656	31-Dec-98	6134141	17-Oct-00

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
INV0199.000US	Sau C. Wong	Multi-Bit-Per-Cell And Analog/Multi-Level Non-Volatile Memories With Improved Resolution And Signal-To-Noise Ratio	US	Issued	9224183	31-Dec-98	6058060	2-May-00
INV0199.002US	Sau C. Wong	Word-Line Decoder For Multi-Bit-Per-Cell And Analog/Multi-Level Memories With Improved Resolution	US	Issued	9305544	5-May-99	6285593	4-Sep-01
INV0200.000US	Geoffrey S Gongwer	Method For Applying Variable Row Bias to Reduce Program Disturb in a Flash Memory Storage Array	US	Issued	9291249	13-Apr-99	6181599	30-Jan-01
INV0201.000US	Sau C. Wong	Auto-Tracking Write And Read Processes For Multi-Bit-Per-Cell Non-Volatile Memories	US	Issued	9262946	4-Mar-99	6094368	25-Jul-00
MDM0004.003US	Dan Harkabi, Gidon Elazar, Nehemiah Weingarten	Electrical Connector	US	Published	12367894	9-Feb-09		
MDM0011.002US	Gidon Elazar, Dan Harkabi, Nehemiah Weingarten, Avraham Halachmi, Noam Carmel, Eran Euchs	Protection of Digital Data Content	US	Published	12641076	17-Dec-09		
MDM0012.001US	Gidon Elazar, Dan Harkabi, Nehemiah Weingarten	Method for Page Translation	US	Issued	10955506	1-Oct-04	7350143	25-Mar-08
MGA-0001-1-II-a-US	Robert Khedouri, Jonathan Axelrod, Harold Price, Mark Edinger, Douglas A Kraul, John Becker	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Published	12824777	28-Jun-10		
MGA-0001-1-III-US	Robert Khedouri, Jonathan Axelrod, Harold Price, John Becker, Mark Edinger, Douglas A Kraul	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Issued	11964107	26-Dec-07	7881656	1-Feb-11
MGA-0001-1-I-US	Robert Khedouri, Jonathan Axelrod, Harold Price, Mark Edinger, Douglas A Kraul	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Pending	11964182	26-Dec-07		
MGA-0001-1-IV-US	Robert Khedouri, Jonathan Axelrod, Harold Price, Mark Edinger, Douglas A Kraul	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Published	11964090	26-Dec-07		
MGA-0001-1-US	Robert Khedouri, Jonathan Axelrod, Harold Price, Mark Edinger, Douglas A Kraul	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Published	11780535	20-Jul-07		
MGA-0001-1-VIII-US	Robert Khedouri, Jonathan Axelrod, Harold Price, Mark Edinger, Douglas A Kraul	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Published	11964238	26-Dec-07		
MGA-0001-1-VII-US	Robert Khedouri, Jonathan Axelrod, Harold Price, Mark Edinger, Douglas A Kraul	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Pending	11964263	26-Dec-07		
MGA-0001-1-VI-US	Robert Khedouri, Jonathan Axelrod, Harold Price, Mark Edinger, Douglas A Kraul	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Pending	11964200	26-Dec-07		
MGA-0001-1-V-US	Robert Khedouri, Jonathan Axelrod, Harold Price, Mark Edinger, Douglas A Kraul	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Published	11964134	26-Dec-07		
MGA-0001-1-XIII-US	Robert Khedouri, Jonathan Axelrod, Harold Price, Mark Edinger, Douglas A Kraul	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Pending	11965030	27-Dec-07		

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
MGA-0001-1-XII-US	Robert Khedouri, Jonathan Axelrod, Harold Price, Mark Edinger, Douglas A Kraul	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Pending	11964765	27-Dec-07		
MGA-0001-1-XI-US	Robert Khedouri, Jonathan Axelrod, Harold Price, Mark Edinger, Douglas A Kraul	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Pending	11964748	27-Dec-07		
MGA-0001-1-XIV-US	Robert Khedouri, Jonathan Axelrod, Harold Price, Mark Edinger, Douglas A Kraul	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Pending	11964885	27-Dec-07		
MGA-0001-1-X-US	Robert Khedouri, Jonathan Axelrod, Harold Price, Mark Edinger, Douglas A Kraul	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Pending	11964798	27-Dec-07		
MGA-0001-1-XV-a-US	Robert Khedouri, Jonathan Axelrod, Harold Price, Mark Edinger, Douglas A Kraul, John Becker	Wireless Portable Device and Method for Sending a Digital File of an Audio and/or Video Message	US	Published	12824683	28-Jun-10		
MGA-0001-a-US	Robert Khedouri, Jonathan Axelrod, Harold Price	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Pending	12834515	12-Jul-10		
MGA-0001-III-US	Robert Khedouri, Jonathan Axelrod, Harold Price	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Pending	12041903	4-Mar-08		
MGA-0001-II-US	Robert Khedouri, Jonathan Axelrod, Harold Price	Method of Content Distribution Using the Same	US	Pending	12041751	4-Mar-08		
MGA-0001-I-US	Robert Khedouri, Jonathan Axelrod, Harold Price	Network system of media players sharing content	US	Pending	12041727	4-Mar-08		
MGA-0001-IV-US	Robert Khedouri, Jonathan Axelrod, Harold Price	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Pending	12041862	4-Mar-08		
MGA-0001-IX-US	Robert Khedouri, Jonathan Axelrod, Harold Price	Method of Content Distribution Using the Same	US	Published	12045297	10-Mar-08		
MGA-0001-VIII-US	Robert Khedouri, Jonathan Axelrod, Harold Price	SYSTEM DISPLAYING RECEIVED CONTENT ON A PORTABLE WIRELESS MEDIA DEVICE	US	Published	12045213	10-Mar-08		
MGA-0001-VII-US	Robert Khedouri, Jonathan Axelrod, Harold Price	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Published	12045184	10-Mar-08		
MGA-0001-VI-US	Robert Khedouri, Jonathan Axelrod, Harold Price	System Sharing Content	US	Published	12045162	10-Mar-08		
MGA-0001-V-US	Robert Khedouri, Jonathan Axelrod, Harold Price	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Pending	12045162	10-Mar-08		
MGA-0001-XIII-US	Robert Khedouri, Jonathan Axelrod, Harold Price	Portable media device with list management tools	US	Published	12041937	4-Mar-08		
MGA-0001-XII-US	Robert Khedouri, Jonathan Axelrod, Harold Price	Remotely Configured Media Device	US	Published	12045897	11-Mar-08		
MGA-0001-XIV-US	Robert Khedouri, Jonathan Axelrod, Harold Price	Device for Accessing a Digital Media Service	US	Published	12045809	11-Mar-08		
MGA-0001-X-US	Robert Khedouri, Jonathan Axelrod, Harold Price	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Pending	12045910	11-Mar-08		
MGA-1205-US	Robert Khedouri, Jonathan Axelrod, Harold Price	Audio Visual Player Apparatus and System and Method of Content Distribution Using the Same	US	Pending	12045330	10-Mar-08		
MSA-1243-US	Soloman Junzhi Wang, Alon Marcu, Ori Stern, Susan Cannon, James Xian Jun Liu, Chieh-Hao (John) Yang, Po Yuan	METHOD AND SYSTEM FOR ACCESSING A STORAGE SYSTEM WITH MULTIPLE FILE SYSTEMS	US	Published	12101569	11-Apr-08		
MSA-1243-US	Yaron Sheba, Michael A Yurochko, Derek Niizawa	MEDIA PLAYER	US	Issued	29308150	30-May-08	D593066	26-May-09

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
MSA-1337-a-US	Damian Yurzola, Rajeev Nagabhirava, Arjun Kapoor, Itai Dror, Annie Chang, Peter Hwang, Jian Chen	System and Method of Error Correction of Control Data at a Memory Device	US	Pending	12955174	29-Nov-10		
MSA-1337-US	Damian Yurzola, Rajeev Nagabhirava, Arjun Kapoor, Itai Dror	System and Method of Error Correction of Control Data at a Memory Device	US	Pending	12645700	23-Dec-09		
MXA-255-US	Matt Fruin, Christopher Moore, Samuel Yusheng Yu	Memory device operable in read-only and re-writable modes of operation	US	Published	12421229	9-Apr-09		
MXA-304-US	Matt Fruin, Christopher Moore, Samuel Yusheng Yu	Memory device operable in read-only and write-once, read-many (WORM) modes of operation	US	Published	12421238	9-Apr-09		
MXA-387-US	Samuel Yusheng Yu, Christopher Moore, Jason Whetstone, Ron Barzilai, Hironaga Ino	Authentication and Securing of Write-Once, Read-Many (WORM) Memory Devices	US	Pending	12572991	2-Oct-09		
SDA-1000Y-US	Tyler J. Thorp, Brent Haukness, Henry R Hutton	An Electronic Library for Managing Data on Removable Storage Devices	US	Published	11677388	21-Feb-07		
SDA-1002X-US	Teruhiko Kamei, Yan Li	Method for Programming with Initial Programming Voltage Based on Trial	US	Issued	11616647	27-Dec-06	7551482	23-Jun-09
SDA-1002Y-a-US	Teruhiko Kamei, Yan Li	Non-Volatile Storage System with Initial Programming Voltage Based on Trial	US	Issued	12493040	26-Jun-09	7768834	3-Aug-10
SDA-1002Y-US	Teruhiko Kamei, Yan Li	Non-Volatile Storage System with Initial Programming Voltage Based on Trial	US	Issued	11616665	27-Dec-06	7570520	4-Aug-09
SDA-1003X-III-US	Yan Li, Teruhiko Kamei, Jeffrey Lutze	Segmented Bitscan for Verification of Programming	US	Pending	13035539	25-Feb-11		
SDA-1003X-II-US	Yan Li, Teruhiko Kamei, Jeffrey Lutze	Segmented Bitscan for Verification of Programming	US	Allowed	12755610	7-Apr-10		
SDA-1003X-I-US	Yan Li, Teruhiko Kamei, Jeffrey Lutze	Segmented Bitscan for Verification of Programming	US	Issued	12431573	28-Apr-09	7724580	25-May-10
SDA-1003X-US	Yan Li, Teruhiko Kamei, Jeffrey Lutze	Segmented Bitscan for Verification of Programming	US	Issued	11563585	27-Nov-06	7545681	9-Jun-09
SDA-1003Y-US	Yan Li, Teruhiko Kamei, Jeffrey Lutze	Apparatus with Segmented Bitscan for Verification of Programming	US	Issued	11563590	27-Nov-06	7440319	21-Oct-08
SDA-1007-US	Steven T. Sprouse, Liliy (Ka Ian) Yung, Suresh Upadhyayula, Patricia Collantes	Electrical Connector with Grounding Pin	US	Issued	11618084	29-Dec-06	7354314	8-Apr-08
SDA-1011X-US	Jeffrey Lutze, Yingda Dong	Reducing Program Disturb in Non-Volatile Memory Using Multiple Boosting Modes	US	Issued	11555850	2-Nov-06	7440323	21-Oct-08
SDA-1011Y-I-US	Jeffrey Lutze, Yingda Dong	Non-Volatile Memory Using Multiple Boosting Modes for Reduced Program Disturb	US	Issued	12211348	16-Sep-08	7796430	14-Sep-10
SDA-1011Y-US	Jeffrey Lutze, Yingda Dong	Non-Volatile Memory Using Multiple Boosting Modes for Reduced Program Disturb	US	Issued	11555856	2-Nov-06	7468911	23-Dec-08
SDA-1014-1X-US	Gerrit Jan Hemink, Yingda Dong, Jeffrey Lutze, Dana Lee	Programming Non-Volatile Memory with Reduced Program Disturb by Using Different Pre-Charge Enable Voltages	US	Issued	11618600	29-Dec-06	7450430	11-Nov-08
SDA-1014-1Y-US	Gerrit Jan Hemink, Yingda Dong, Jeffrey Lutze, Dana Lee	Systems for Programming Non-Volatile Memory with Reduced Program Disturb by Using Different Pre-Charge Enable Voltages	US	Issued	11618606	29-Dec-06	7463551	9-Dec-08
SDA-1014X-US	Yingda Dong, Jeffrey Lutze, Dana Lee, Gerrit Jan Hemink	Programming Non-Volatile Memory with Reduced Program Disturb by Removing Pre-Charge Dependency on Word Line Data	US	Issued	11618580	29-Dec-06	7433241	7-Oct-08

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1014Y-US	Yingda Dong, Jeffrey Lutze, Dana Lee, Gerrit Jan Hemink	Systems for Programming Non-Volatile Memory with Reduced Program Disturb by Removing Pre-Charge Dependency on Word Line Data	US	Issued	11618594	29-Dec-06	7468918	23-Dec-08
SDA-1016-US	Loc Tu, Wanfang (Frank) Tsai	Defective Block Isolation in a Non-Volatile Memory System	US	Issued	11470945	7-Sep-06	7561482	14-Jul-09
SDA-1018X-US	Man Muji, Seungpil Lee	Complete Word Line Look Ahead with Efficient Data Latch Assignment in Non-Volatile Memory Read Operations	US	Issued	11617544	28-Dec-06	7615505	10-Nov-09
SDA-1018Y-US	Man Muji, Seungpil Lee	Systems for Complete Word Line Look Ahead with Efficient Data Latch Assignment in Non-Volatile Memory Read Operations	US	Issued	11617550	28-Dec-06	7616506	10-Nov-09
SDA-1021X-US	Nima Mokhlesi	Method of Forming a Flash NAND Memory Cell Array with Charge Storage Elements Positioned in Trenches	US	Issued	11614894	21-Dec-06	7642160	5-Jan-10
SDA-1021Y-US	Nima Mokhlesi	Flash NAND Memory Cell Array with Charge Storage Elements Positioned in Trenches	US	Issued	11614909	21-Dec-06	7800161	21-Sep-10
SDA-1022X-US	Dana Lee, Jeffrey Lutze	Method of Low Voltage Programming of Non-Volatile Memory Cells	US	Allowed	11614879	21-Dec-06		
SDA-1022Y-US	Dana Lee, Jeffrey Lutze	System for Low-Voltage Programming of Non-Volatile Memory Cells	US	Issued	11614884	21-Dec-06	7623389	24-Nov-09
SDA-1026X-US	Fabrice Jogand-Coulomb, David Guidry, Pascal Caillon, Benjamin Vigier	Methods for Launching a Program Application	US	Published	11647995	29-Dec-06		
SDA-1027X-US	Hem Takiar, Shrikar Bhagath	Method of Making a Semiconductor Device having Multiple Die Redistribution Layer	US	Issued	11617689	28-Dec-06	7560304	14-Jul-09
SDA-1027Y-US	Hem Takiar, Shrikar Bhagath	Semiconductor Device Having Multiple Die Redistribution Layer	US	Issued	11617687	28-Dec-06	7791191	7-Sep-10
SDA-1028X-US	Yigal Brandman, Kevin Conley	Methods of Varying Read Threshold Voltage in Nonvolatile Memory	US	Issued	11556615	3-Nov-06	7904788	8-Mar-11
SDA-1028Y-US	Yigal Brandman, Kevin Conley	Non-Volatile Memory with Variable Read Threshold	US	Issued	11556626	3-Nov-06	7558109	7-Jul-09
SDA-1029X-US	Yigal Brandman	Methods of Modulating Error Correction Coding	US	Issued	11556632	3-Nov-06	7904780	8-Mar-11
SDA-1029Y-US	Yigal Brandman	Non-Volatile Memory with Modulated Error Correction Coding	US	Published	11556636	3-Nov-06		
SDA-1031X-US	Deepak C. Sekar, Nima Mokhlesi	Biasing Non-Volatile Storage to Compensate for Temperature Variations	US	Issued	11618782	30-Dec-06	7583535	1-Sep-09
SDA-1031Y-US	Deepak C. Sekar, Nima Mokhlesi	Non-Volatile Storage with Bias for Temperature Compensation	US	Issued	11618786	30-Dec-06	7583539	1-Sep-09
SDA-1032X-US	Deepak C. Sekar, Nima Mokhlesi	Biasing Non-Volatile Storage Based on Selected Word Line	US	Issued	11618788	30-Dec-06	7468919	23-Dec-08
SDA-1032Y-US	Deepak C. Sekar, Nima Mokhlesi	Non-Volatile Storage with Bias based on Selected Word Line	US	Issued	11618790	30-Dec-06	7554853	30-Jun-09
SDA-1034X-II-US	Deepak C. Sekar, Nima Mokhlesi	Applying Different Body Bias To Different Substrate Portions For Non-Volatile Storage	US	Published	12759581	13-Apr-10		
SDA-1034X-I-US	Deepak C. Sekar, Nima Mokhlesi	Applying Adaptive Body Bias to Non-Volatile Storage Based On Number Of Programming Cycles	US	Issued	12335803	16-Dec-08	7751244	6-Jul-10

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1034X-US	Deepak C. Sekar, Nima Mokhlesi	Applying Adaptive Body Bias to Non-Volatile Storage	US	Issued	11618791	30-Dec-06	7468920	23-Dec-08
SDA-1034Y-US	Deepak C. Sekar, Nima Mokhlesi	Non-Volatile Storage with Adaptive Body Bias	US	Issued	11618793	30-Dec-06	7525843	28-Apr-09
SDA-1035X-US	Feng Pan	Method for Reducing Wordline Recovery Time	US	Issued	11615868	22-Dec-06	7443735	28-Oct-08
SDA-1035Y-US	Feng Pan	System for Reducing Wordline Recovery Time	US	Issued	11615873	22-Dec-06	7495992	24-Feb-09
SDA-1038Y-US	Feng Pan, Trung Pham	Apparatus for Controlling Bitline Biasing Voltage	US	Issued	11617531	28-Dec-06	7529135	5-May-09
SDA-1043X-US	Warren Middlekauf, Robert C Miller	Method for Producing Portable Memory	US	Allowed	11551402	20-Oct-06		
SDA-1043Y-US	Warren Middlekauf, Robert C Miller	Portable Memory Devices	US	Allowed	11551423	20-Oct-06		
SDA-1045X-US	Steven T. Sprouse, Patricia Collantes, Dhaval Parikh	Method of Making an Electrical Connector with ESD Grounding Clip	US	Issued	11618292	29-Dec-06	7810235	12-Oct-10
SDA-1045Y-US	Steven T. Sprouse, Dhaval Parikh, Patricia Collantes	Electrical Connector with ESD Grounding Clip	US	Issued	11618280	29-Dec-06	7410370	12-Aug-08
SDA-1049X-US	Farookh Moogat, Teruhiko Kamei	Method of NAND Flash Memory Cell Array with Adaptive Memory State Partitioning	US	Issued	11618482	29-Dec-06	7489547	10-Feb-09
SDA-1049Y-US	Farookh Moogat, Teruhiko Kamei	NAND Flash Memory Cell Array with Adaptive Memory State Partitioning	US	Issued	11618498	29-Dec-06	7489548	10-Feb-09
SDA-1051X-US	Yingda Dong, Jeffrey Lutze	Reduced Program Disturb in Non-Volatile Storage using Early Source-side Boosting	US	Issued	11609804	12-Dec-06	7623386	24-Nov-09
SDA-1051Y-US	Yingda Dong, Jeffrey Lutze	Non-Volatile Storage with Early Source-side Boosting for Reducing Program Disturb	US	Issued	11609813	12-Dec-06	7623387	24-Nov-09
SDA-1076-1Y-US	Paul Paternoster, Po-Shen Lai, Paul Lassa, Yongliang Bill Wang	Selectively Powering Data Interfaces	US	Published	11686350	14-Mar-07		
SDA-1081-a-US	Mike Morganstern, Derek Niizawa, Jeffrey A Salazar, Junggi Sung	Flash Drive Design	US	Issued	29275796	5-Jan-07	D558763	1-Jan-08
SDA-1081-b-US	Mike Morganstern, Derek Niizawa, Jeffrey A Salazar, Junggi Sung, Mike Simmons, Arturo Meuniot, Michael Roberts	ELECTRONIC DEVICE WITH DUAL SLIDE ACTUATOR THAT EXTENDS AND RETRACTS A CONNECTOR	US	Issued	11943450	20-Nov-07	7674120	9-Mar-10
SDA-1081-US	Mike Morganstern, Derek Niizawa, Jeffrey A Salazar, Junggi Sung, Mike Simmons, Arturo Meuniot, Michael Roberts	ELECTRONIC DEVICE WITH DUAL SLIDE ACTUATOR THAT EXTENDS AND RETRACTS A CONNECTOR	US	Issued	11618436	29-Dec-06	7301596	27-Nov-07
SDA-1082-US	Mike Morganstern, Derek Niizawa, Jeffrey A Salazar, Junggi Sung, Mike Simmons, Arturo Meuniot, Michael Roberts	ELECTRONIC DEVICE WITH A RETRACTABLE CONNECTOR AND PROTECTIVE DOOR MECHANISM	US	Issued	11618447	29-Dec-06	7303411	4-Dec-07
SDA-1090-US	Darmin Jin, Brian Cheung, Steve Skala	Systems, Circuits, Chips and Methods with Protection at Power Island Boundaries	US	Published	11967382	31-Dec-07		
SDA-1091-US	Stanley Chapski, Darmin Jin, Brian Cheung	Systems, Modules, Chips, Circuits and Methods with Delay Trim Value Updates on Power-Up	US	Issued	11967467	31-Dec-07	7804371	28-Sep-10
SDA-1092-US	Yongliang Bill Wang, Daniel Nguyen	Systems, Methods, and Integrated Circuits with Inrush-Limited Power Islands	US	Allowed	11967580	31-Dec-07		
SDA-1094X-US	Baojing Liu, Radhakrishnan Nair, Paul Lassa	Method for Performing Full Transfer Automation in a USB Controller	US	Issued	11618865	31-Dec-06	7802034	21-Sep-10
SDA-1100-US	Brian Cheung, Emmanuel de Muizon	Integrated Circuits and Methods with Two Types of Decoupling Capacitors	US	Issued	11967778	31-Dec-07	7898013	9-Mar-11
SDA-1101X-US	Feng Pan	Generation of Analog Voltage Using Self-Biased Capacitive Feedback Stage	US	Issued	11618917	1-Jan-07	7626447	1-Dec-09

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1101Y-US	Feng Pan	Analog Voltage Generator with Self-Biased Capacitive Feedback Stage	US	Issued	11618918	1-Jan-07	7492214	17-Feb-09
SDA-1102Y-I-US	Jonathan Hubert, Jason Patrick Hanlon, Kevin Conley	Expandable And Collapsible Peripheral Device	US	Issued	12103533	15-Apr-08	7762849	27-Jul-10
SDA-1102Y-US	Jonathan Hubert, Jason Patrick Hanlon, Kevin Conley	Expandable And Collapsible Peripheral Device	US	Issued	11620519	5-Jan-07	7798840	21-Sep-10
SDA-1103-US	Paul Lassa, Paul Paternoster, Brian Cheung	Connection between an I/O Region and the Core Region of an Integrated Circuit	US	Pending	11651614	8-Jan-07		
SDA-1104X-US	Takashi Orimoto, James Kai, Henry Chien, George Matamis	Methods of Forming NAND Memory with Virtual Channel	US	Issued	11626784	24-Jan-07	7494870	24-Feb-09
SDA-1104Y-US	Takashi Orimoto, James Kai, Henry Chien, George Matamis	NAND Memory with Virtual Channel	US	Issued	11626778	24-Jan-07	7495282	24-Feb-09
SDA-1105X-US	James Kai, George Matamis, Tuan D. Pham, Masaaki Higashitani, Takashi Orimoto	Methods of Forming Spacer Patterns Using Assist Layer for High Density Semiconductor Devices	US	Issued	11623314	15-Jan-07	7592225	22-Sep-09
SDA-1105Y-a-US	James Kai, George Matamis, Tuan D. Pham, Masaaki Higashitani, Takashi Orimoto	Spacer Patterns Using Assist Layer for High Density Semiconductor Devices	US	Allowed	12791103	1-Jun-10		
SDA-1105Y-US	James Kai, George Matamis, Tuan D. Pham, Masaaki Higashitani, Takashi Orimoto	Spacer Patterns Using Assist Layer for High Density Semiconductor Devices	US	Issued	11623315	15-Jan-07	7773403	10-Aug-10
SDA-1106-US	Takashi Orimoto, George Matamis, James Kai, Tuan D. Pham, Masaaki Higashitani, Henry Chien	Methods of Forming Integrated Circuit Devices using Composite Spacer Structures	US	Issued	12014689	15-Jan-08	7795080	14-Sep-10
SDA-1107Y-US	Darmin Jin, Brian Cheung	De-Glitch Circuit	US	Issued	11686828	15-Mar-07	7557643	7-Jul-09
SDA-1108X-US	Nima Mokhlesi	Multiple Pass Write Sequence for Non-Volatile Storage	US	Issued	11694987	31-Mar-07	7564711	21-Jul-09
SDA-1108Y-US	Nima Mokhlesi	Non-Volatile Storage Apparatus with Multiple Pass Write Sequence	US	Issued	11694989	31-Mar-07	7616500	10-Nov-09
SDA-1109X-a-US	Nima Mokhlesi	Variable Initial Program Voltage Magnitude for Non-Volatile Storage	US	Published	12482696	11-Jun-09		
SDA-1109X-US	Nima Mokhlesi	Variable Initial Program Voltage Magnitude for Non-Volatile Storage	US	Issued	11694990	31-Mar-07	7564715	21-Jul-09
SDA-1109Y-US	Nima Mokhlesi	Non-Voltage Storage Apparatus with Variable Initial Program Voltage Magnitude	US	Issued	11694991	31-Mar-07	7616495	10-Nov-09
SDA-1110X-US	Nima Mokhlesi	Dynamic Verify Based on Threshold Voltage Distribution	US	Issued	11694992	31-Mar-07	7619930	17-Nov-09
SDA-1111X-a-US	Michael James McCarthy, Ning Ye, Naveen Kini	PCB Circuit Modification From Multiple to Individual Chip Enable Signals	US	Published	12857365	16-Aug-10		
SDA-1111X-US	Michael James McCarthy, Ning Ye, Naveen Kini	Method of Making PCB Circuit Modification From Multiple to Individual Chip Enable Signals	US	Issued	11679153	26-Feb-07	7709278	4-May-10
SDA-1111Y-US	Michael James McCarthy, Ning Ye, Naveen Kini	PCB Circuit Modification From Multiple to Individual Chip Enable Signals	US	Issued	11679157	26-Feb-07	7778057	17-Aug-10
SDA-1112X-US	Yan Li	Method for Cache Page Copy in a Non-Volatile Memory	US	Issued	11683356	7-Mar-07	7502255	10-Mar-09
SDA-1112Y-US	Yan Li	Non-Volatile Memory with Cache Page Copy	US	Issued	11683365	7-Mar-07	7499320	3-Mar-09

PATENT

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1113X-US	Sergey Anatolievich Gorobets, Neil Dunlop, Kevin Kealy	Method for Combining Lower-Endurance/Performance and Higher-Endurance/Performance Storage to Support Data Processing	US	Issued	11731531	30-Mar-07	7633799	15-Dec-09
SDA-1114X-US	Shai Traister	Methods for Conversion of Update Blocks Based On Comparison with a Threshold Size	US	Issued	11725628	19-Mar-07	7904670	8-Mar-11
SDA-1115X-US	Bum So, Shai Traister	Methods for Conversion of Update Blocks based on Association with Host File Management Data Structures	US	Published	11725746	19-Mar-07		
SDA-1116X-US	Sergey Anatolievich Gorobets	Methods for Forcing an Update Block to Remain Sequential	US	Published	11725720	19-Mar-07		
SDA-1117X-US	Shai Traister	Methods for Storing Memory Operations in a Queue	US	Published	11726648	21-Mar-07		
SDA-1118X-US	Jason Lin	Flash Memory Refresh Techniques Triggered by Controlled Scrub Data Reads	US	Issued	11692840	28-Mar-07	7477547	13-Jan-09
SDA-1118Y-US	Jason Lin	Flash Memory Refresh Techniques Triggered by Controlled Scrub Data Reads	US	Issued	11692829	28-Mar-07	7573773	11-Aug-09
SDA-1119X-US	Henry Chin, Nima Mokhlesi, Dengtao Zhao	Adjusting Resistance of Non-Volatile Memory Using Dummy Memory Cells	US	Issued	11688874	21-Mar-07	7535764	19-May-09
SDA-1120X-US	Deepak C. Sekar, Man Mui, Nima Mokhlesi	Method of Compensating Variations Along a Word Line in a Non-Volatile Memory	US	Issued	11693601	29-Mar-07	7508713	24-Mar-09
SDA-1120Y-US	Deepak C. Sekar, Man Mui, Nima Mokhlesi	Non-Volatile Memory with Compensation for Variations along a Word Line	US	Issued	11693616	29-Mar-07	7577031	18-Aug-09
SDA-1121X-a-US	Takashi Orimoto, George Matamis, Henry Chien, James Kai	Methods of Forming NAND Flash Memory with Fixed Charge	US	Published	12729874	23-Mar-10		
SDA-1121X-US	Takashi Orimoto, George Matamis, Henry Chien, James Kai	Methods of Forming NAND Flash Memory with Fixed Charge	US	Issued	11692961	29-Mar-07	7732275	8-Jun-10
SDA-1121Y-US	Takashi Orimoto, George Matamis, Henry Chien, James Kai	NAND Flash Memory with Fixed Charge	US	Issued	11692958	29-Mar-07	7619926	17-Nov-09
SDA-1122X-II-US	Fabrice Jogand-Coulomb	Controlling Access to Digital Content	US	Pending	12943271	10-Nov-10		
SDA-1122X-I-US	Fabrice Jogand-Coulomb	Controlling Access to Digital Content	US	Pending	12943213	10-Nov-10		
SDA-1122X-US	Fabrice Jogand-Coulomb	Method for Controlling Access to Digital Content	US	Published	11694866	30-Mar-07		
SDA-1123X-I-US	Nima Mokhlesi	Methods for Forming and Operating NAND Memory with Side-Tunneling	US	Published	12782957	19-May-10		
SDA-1123X-US	Nima Mokhlesi	Methods for Forming and Operating NAND Memory with Side-Tunneling	US	Issued	11693765	30-Mar-07	7745285	29-Jun-10
SDA-1123Y-US	Nima Mokhlesi	NAND Memory with Side-Tunneling	US	Issued	11693769	30-Mar-07	7512005	31-Mar-09
SDA-1124X-US	Maria Sabatini, Kevin Kealy	Method for Using a Memory Device with a Built-In Memory Array and a Connector for a Removable Memory Device	US	Issued	11731523	30-Mar-07	7603499	13-Oct-09
SDA-1124Y-US	Kevin Kealy, Maria Sabatini	Memory Device with a Built-In Memory Array and a Connector for a Removable Memory Device	US	Issued	11731511	30-Mar-07	7613857	3-Nov-09
SDA-1125X-US	Steven T. Sprouse	Method for Enabling an Oscillator Circuit Using Transition Detection	US	Issued	11695009	31-Mar-07	7642873	5-Jan-10
SDA-1125Y-US	Steven T. Sprouse	Oscillator Circuit with Transition Detection Enable	US	Issued	11695010	31-Mar-07	7642874	5-Jan-10



FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1126X-US	Henry Chin, Nima Mokhlesi	Guided Simulated Annealing in Non-Volatile Memory Error Correction Control	US	Published	11694951	31-Mar-07		
SDA-1126Y-US	Henry Chin, Nima Mokhlesi	Non-Volatile Memory with Guided Annealing Error Correction Control	US	Allowed	11694950	31-Mar-07		
SDA-1127-US	James Kai, Tuan D. Pham, Masaaki Higashitani, George Matamis, Takashi Ozimato	Integrated Non-Volatile Memory and Peripheral Circuitry Fabrication	US	Issued	12058512	28-Mar-08	7704832	27-Apr-10
SDA-1128X-US	Nima Mokhlesi, Henry Chin, Dengtao Zhao	Soft Bit Data Transmission for Error Correction Control in Non-Volatile Memory	US	Allowed	11694947	31-Mar-07		
SDA-1128Y-US	Nima Mokhlesi, Henry Chin, Dengtao Zhao	Non-Volatile Memory with Soft Bit Data Transmission for Error Correction Control	US	Allowed	11694948	31-Mar-07		
SDA-1130X-a-US	Nima Mokhlesi, Henry Chin, Dengtao Zhao	Method for Decoding Data in Non-Volatile Storage Using Reliability Metrics Based on Multiple Reads	US	Pending	13024676	10-Feb-11		
SDA-1130X-US	Nima Mokhlesi, Henry Chin, Dengtao Zhao	Method for Decoding Data in Non-Volatile Storage Using Reliability Metrics Based on Multiple Reads	US	Issued	11693649	29-Mar-07	7904793	8-Mar-11
SDA-1131X-US	Nima Mokhlesi, Dengtao Zhao, Henry Chin	Method for Reading Non-Volatile Storage Using Pre-Conditioning Waveforms and Modified Reliability Metrics	US	Issued	11693663	29-Mar-07	7797480	14-Sep-10
SDA-1133X-US	Alan M. Chiou, Bahman Qawami, Farshid Sabet-Sharghi	Method of Testing Memory Card Operation	US	Issued	11693644	29-Mar-07	7685478	23-Mar-10
SDA-1133Y-US	Alan M. Chiou, Bahman Qawami, Farshid Sabet-Sharghi	Extender Strip and Test Assembly for Testing Memory Card Operation	US	Issued	11693648	29-Mar-07	7676714	9-Mar-10
SDA-1134X-US	Hao Thai Nguyen, Seungpil Lee, Man Mui, Shahzad Khalid, Hock C So, Prashanti Govindu, Nima Mokhlesi, Deepak C. Sekar	Method for Sensing Negative Threshold Voltages in Non-Volatile Storage Using Current Sensing	US	Issued	11771982	29-Jun-07	7447079	4-Nov-08
SDA-1134Y-US	Hao Thai Nguyen, Seungpil Lee, Man Mui, Shahzad Khalid, Hock C So, Prashanti Govindu, Nima Mokhlesi, Deepak C. Sekar	Non-Volatile Storage Using Current Sensing of Sensing Negative Threshold Voltages	US	Issued	11771987	29-Jun-07	7532516	12-May-09
SDA-1135X-US	Hao Thai Nguyen, Seungpil Lee, Man Mui, Shahzad Khalid, Hock C So, Prashanti Govindu	Method for Current Sensing with Biasing of Source and P-Well in Non-Volatile Storage	US	Issued	11771992	29-Jun-07	7489554	10-Feb-09
SDA-1135Y-US	Hao Thai Nguyen, Seungpil Lee, Man Mui, Shahzad Khalid, Hock C So, Prashanti Govindu	Non-Volatile Storage Using Current Sensing with Biasing of Source and P-Well	US	Issued	11771997	29-Jun-07	7539060	26-May-09
SDA-1136P-US	Hao Thai Nguyen, Seungpil Lee, Man Mui	Temperature Compensation for Bit Lines During Erase Verify Operations in Non-Volatile Storage	US	Expired	60910404	5-Apr-07		
SDA-1137X-a-US	Raul-Adrian Cernea	Predictive Programming for Non-Volatile Memory	US	Allowed	12649184	29-Dec-09		
SDA-1137X-US	Raul-Adrian Cernea	Predictive Programming in Non-Volatile Memory	US	Issued	11733694	10-Apr-07	7643348	5-Jan-10
SDA-1137Y-US	Raul-Adrian Cernea	Non-Volatile Memory with Predictive Programming	US	Issued	11733706	10-Apr-07	7551483	23-Jun-09
SDA-1138X-US	Shai Traister	Scheduling Methods of Phased Garbage Collection and Housekeeping Operations in a Flash Memory System	US	Published	11769033	27-Jun-07		

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1139X-US	Deepak C. Sekar, Nima Mokhlesi, Hao Thai Nguyen, Seungpil Lee, Man Mui	Compensation Source Voltage Drop in Non-Volatile Storage	US	Issued	11739501	24-Apr-07	7606071	20-Oct-09
SDA-1139Y-US	Deepak C. Sekar, Nima Mokhlesi, Hao Thai Nguyen, Seungpil Lee, Man Mui	Non-Volatile Storage with Compensation for Source Voltage Drop	US	Issued	11739509	24-Apr-07	7606072	20-Oct-09
SDA-1140X-US	Deepak C. Sekar, Nima Mokhlesi, Hock C So	Reducing Power Consumption During Read Operations in Non-Volatile Storage	US	Issued	11740091	25-Apr-07	7606079	20-Oct-09
SDA-1140Y-US	Deepak C. Sekar, Nima Mokhlesi, Hock C So	Non-Volatile Storage with Reduced Power Consumption during Read Operations	US	Issued	11740096	25-Apr-07	7440327	21-Oct-08
SDA-1141X-a-US	Nima Mokhlesi	Non-Volatile Memory and Method With Improved Sensing Having Bit-Line Lockout Control	US	Issued	12371479	13-Feb-09	7808832	5-Oct-10
SDA-1141X-b-US	Nima Mokhlesi	Non-Volatile Memory and Method With Improved Sensing Having Bit-Line Lockout Control	US	Published	12897089	4-Oct-10		
SDA-1141X-US	Nima Mokhlesi	Sensing with Bit-Line Lockout Control in Non-Volatile Memory	US	Issued	11759898	7-Jun-07	7492640	17-Feb-09
SDA-1141Y-US	Nima Mokhlesi	Non-Volatile Memory with Improved Sensing Having Bit-Line Lockout Control	US	Issued	11759909	7-Jun-07	7489553	10-Feb-09
SDA-1142X-US	Yingda Dong, Jeffrey Lutze, Shih-Chung Lee, Gerrit Jan Hemink, Ken Oowada	Boosting for Non-Volatile Storage Using Channel Isolation Switching	US	Issued	11745082	7-May-07	7460404	2-Dec-08
SDA-1142Y-US	Yingda Dong, Jeffrey Lutze, Shih-Chung Lee, Gerrit Jan Hemink, Ken Oowada	Non-Volatile Storage With Boosting Using Channel Isolation Switching	US	Issued	11745092	7-May-07	7463522	9-Dec-08
SDA-1143X-US	Bill Thanos, Hyuk-Il Kwon, In-Soo Yoon, Joe Meza, Dave Jeppson	Method for Avoiding Refreshing a Database of Metadata Associated with Digital Media Content	US	Published	11804994	18-May-07		
SDA-1144X-US	Bill Thanos, Hyuk-Il Kwon, Dave Jeppson, In-Soo Yoon, Joe Meza	Method for Improving a Digital Media Player Experience	US	Published	11804580	18-May-07		
SDA-1145X-US	Derek J. Bosch, Christopher Moore	Method for Repairing a Neighborhood of Rows in a Memory Array Using a Patch Table	US	Published	11803776	15-May-07		
SDA-1145Y-US	Derek J. Bosch, Christopher Moore	Memory Device for Repairing a Neighborhood of Rows in a Memory Array Using a Patch Table	US	Published	11803756	15-May-07		
SDA-1147X-US	Michael Holtzman, Rotem Seia, Ron Barzilai, Fabrice Jogand-Coulomb	Method for Improving Accuracy of a Time Estimate from a Memory Device	US	Published	11811284	8-Jun-07		
SDA-1148X-US	Michael Holtzman, Rotem Seia, Ron Barzilai, Fabrice Jogand-Coulomb	Method for Improving Accuracy of a Time Estimate Used to Authenticate an Entry to a Memory Device	US	Published	11811289	8-Jun-07		
SDA-1149X-US	Michael Holtzman, Rotem Seia, Ron Barzilai, Fabrice Jogand-Coulomb	Method for Improving Accuracy of a Time Estimate Used in Digital Rights Management (DRM) License Validation	US	Published	11811354	8-Jun-07		
SDA-1150X-US	Kevin Conley, Michael Holtzman, Rotem Seia, Ron Barzilai, Fabrice Jogand-Coulomb	Method for Using Time from a Trusted Host Device	US	Published	11811346	8-Jun-07		
SDA-1151X-US	Yingda Dong, Jeffrey Lutze, Dana Lee	Method for Using Transitional Voltage During Programming of Non-Volatile Storage	US	Issued	11753958	25-May-07	7656703	2-Feb-10
SDA-1151Y-US	Yingda Dong, Jeffrey Lutze, Dana Lee	Non-Volatile Storage System Using Transitional Voltage During Programming	US	Issued	11753963	25-May-07	7706189	27-Apr-10
SDA-1152Y-US	Cheeman Yu, Chih Chin Liao, Hem Takiar	Semiconductor Molded Panel Having Reduced Warpage	US	Published	11759486	7-Jun-07		
SDA-1153-US	Raul-Adrian Cernea	Sensing with Compact NMOS Circuits in Nonvolatile Memory (Method)	US	Unfiled				

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1154-1-US	Alan Welsh Sinclair	Method and Apparatus for Providing Data Type and Host File Information to a Mass Storage System	US	Published	12030018	12-Feb-08		
SDA-1154X-US	Alan Welsh Sinclair	Method of Interfacing a Host Operating Through a Logical Address Space with a Direct File Storage Medium	US	Published	11760469	8-Jun-07		
SDA-1155-US	Alan Welsh Sinclair, Barry Wright	Method and System for Storage Address Re-Mapping for a Memory Device	US	Published	12036014	22-Feb-08		
SDA-1156X-US	Robert C Chang, Po Yuan, Bahman Qawami, Farshid Sabet-Sharghi, Solomon Junzhi Wang, James Xian Jun Liu, Chieh-Hao (John) Yang, June Li, Mei Yan	Method of Storing and Accessing Header Data from Memory	US	Published	11771804	29-Jun-07		
SDA-1157X-US	Chih Chin Liao, Ning Ye, Cheeman Yu, Jack Chang Chien, Hem Takiar	Method of Mimizing Kerf Width on a Semiconductor Substrate Panel	US	Issued	11760385	8-Jun-07	7611927	3-Nov-09
SDA-1157Y-US	Chih Chin Liao, Ning Ye, Cheeman Yu, Jack Chang Chien, Hem Takiar	Two-Sided Substrate Lead Connection for Mimizing Kerf Width on a Semiconductor Substrate Panel	US	Published	11760405	8-Jun-07		
SDA-1158X-US	Loc Tu, Jian Chen, Alexander Mak, Tien-Chien Kuo, Long Pham	Programmable Chip Enable and Chip Address in Semiconductor Memory	US	Issued	11763287	14-Jun-07	7715255	11-May-10
SDA-1158Y-US	Loc Tu, Jian Chen, Alexander Mak, Tien-Chien Kuo, Long Pham	Systems for Programmable Chip Enable and Chip Address in Semiconductor Memory	US	Issued	11763292	14-Jun-07	7477545	13-Jan-09
SDA-1159X-US	Andrew Tomlin, Sergey Anatolievich Gorobets, Reuven Elharnias, Shai Traister, Alan Bennett	Method for Writing Data on an Atomic Transaction to a Memory Device	US	Pending	11820617	19-Jun-07		
SDA-1159Y-US	Andrew Tomlin, Sergey Anatolievich Gorobets, Reuven Elharnias, Shai Traister, Alan Bennett	Memory Device with Circuitry for Writing Data of an Atomic Transaction	US	Pending	11820670	19-Jun-07		
SDA-1161X-US	George Matamis, James Kai, Takashi Orimoto, Nima Mokhlesi	Methods of Forming High Density Semiconductor Devices Using Recursive Spacer Technique	US	Published	11765866	20-Jun-07		
SDA-1162X-a-US	Yupin Fong, Jun Wan	Intelligent Control of Program Pulse Duration for Non-Volatile Storage	US	Published	12607329	28-Oct-09		
SDA-1162X-US	Yupin Fong, Jun Wan	Intelligent Control of Program Pulse Duration	US	Issued	11766583	21-Jun-07	7630249	8-Dec-09
SDA-1162Y-US	Yupin Fong, Jun Wan	Non-Volatile Storage System with Intelligent Control of Program Pulse Duration	US	Issued	11766580	21-Jun-07	7580290	25-Aug-09
SDA-1163X-US	Jason Lin	Methods of Programming Multilevel Cell Non-Volatile Memory	US	Issued	11767605	25-Jun-07	7719889	18-May-10
SDA-1163Y-US	Jason Lin	Systems for Programming Multilevel Cell Non-Volatile Memory	US	Issued	11767611	25-Jun-07	7630252	8-Dec-09
SDA-1164X-US	Nima Mokhlesi, Jun Wan	Method for Forming Dual Bit Line Metal Layers for Non-Volatile Memory	US	Published	11768461	26-Jun-07		
SDA-1164Y-US	Nima Mokhlesi, Jun Wan	Dual Bit Line Metal Layers for Non-Volatile Memory	US	Published	11768468	26-Jun-07		
SDA-1165X-US	Haluk Kent Tanik	Method for Communicating with a Non-Volatile Memory Storage Device	US	Published	11771165	29-Jun-07		
SDA-1165Y-US	Haluk Kent Tanik	System for Communicating with a Non-Volatile Memory Storage Device	US	Published	11771203	29-Jun-07		
SDA-1166Y-US	Jason Lin	Systems and Methods For Reading Nonvolatile Memory Using Multiple Reading Schemes	US	Issued	11767587	25-Jun-07	7849383	7-Dec-10

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1167X-US	Shih-Chung Lee	Coarse/Fine Program Verification in Non-Volatile Memory Using Different Reference Levels for Improving Sensing	US	Issued	11773032	3-Jul-07	7508715	24-Mar-09
SDA-1167Y-US	Shih-Chung Lee	Systems for Coarse/Fine Program Verification in Non-Volatile Memory Using Different Reference Levels for Improving Sensing	US	Issued	11773035	3-Jul-07	7599224	6-Oct-09
SDA-1168X-I-US	Cheeman Yu, Vani Verma, Hem Takiar	Method of Fabricating a Two-Sided Die in a Four-Sided Leadframe Based Package	US	Published	12819818	21-Jun-10		
SDA-1168X-US	Cheeman Yu, Vani Verma, Hem Takiar	Method of Fabricating a Two-Sided Die in a Four-Sided Leadframe Based Package	US	Allowed	11770052	28-Jun-07		
SDA-1168Y-US	Cheeman Yu, Vani Verma, Hem Takiar	Two-Sided Die in a Four-Sided Leadframe Based Package	US	Published	11770066	28-Jun-07		
SDA-1169X-US	Chin-Tien Hope Chiu, Hem Takiar, Chih Chin Liao, Cheeman Yu, Ning Ye, Jack Chang Chien	Method of Fabricating a Semiconductor Package having Through Holes for Molding Back Side of Package	US	Allowed	11770078	28-Jun-07		
SDA-1169Y-US	Chin-Tien Hope Chiu, Hem Takiar, Chih Chin Liao, Cheeman Yu, Ning Ye, Jack Chang Chien	Semiconductor Package having Through Holes for Molding Back Side of Package	US	Published	11770088	28-Jun-07		
SDA-1170X-US	Andrew (Chien-Ko) Liao, Chin-Tien Hope Chiu, Jack Chang Chien, Cheeman Yu, Hem Takiar	Method of Fabricating a Semiconductor Die Having a Redistribution Layer	US	Issued	11769927	28-Jun-07	7772047	10-Aug-10
SDA-1170Y-I-US	Andrew (Chien-Ko) Liao, Chin-Tien Hope Chiu, Jack Chang Chien, Cheeman Yu, Hem Takiar	Semiconductor Die Having a Redistribution Layer	US	Allowed	12843279	26-Jul-10		
SDA-1170Y-US	Andrew (Chien-Ko) Liao, Chin-Tien Hope Chiu, Jack Chang Chien, Cheeman Yu, Hem Takiar	Semiconductor Die Having a Redistribution Layer	US	Issued	11769937	28-Jun-07	7763980	27-Jul-10
SDA-1171X-US	Ning Ye, Robert C Miller, Cheeman Yu, Hem Takiar, Andre McKenzie	Method of Fabricating a Memory Card Using SIP/SMT Hybrid Technology	US	Published	11769942	28-Jun-07		
SDA-1171Y-US	Ning Ye, Robert C Miller, Cheeman Yu, Hem Takiar, Andre McKenzie	Memory Card Fabricated Using SIP/SMT Hybrid Technology	US	Issued	11769954	28-Jun-07	7772686	10-Aug-10
SDA-1174X-US	Jonathan Hubert, Jason Patrick Hanlon, Jordan MacDonald	Method of Adapting an Expresscard Slot for Smaller Form Factor Memory Compatibility	US	Published	11771763	29-Jun-07		
SDA-1174Y-US	Jonathan Hubert, Jason Patrick Hanlon, Jordan MacDonald	Adaptor for an Expresscard Slot	US	Issued	11771767	29-Jun-07	7699660	20-Apr-10
SDA-1175Y-I-US	Jonathan Hubert, Jason Patrick Hanlon, Jordan MacDonald, Wesley G. Brewer	Memory Card for an Expresscard Slot	US	Published	12727519	19-Mar-10		
SDA-1175Y-US	Jonathan Hubert, Jason Patrick Hanlon, Jordan MacDonald, Wesley G. Brewer	Memory Card for an Expresscard Slot	US	Issued	11771756	29-Jun-07	7686654	30-Mar-10
SDA-1176Y-US	Jonathan Hubert, Jason Patrick Hanlon, Matthijs Hutten	Adapter System for Use with ExpressCard Slot	US	Issued	11771730	29-Jun-07	7780477	24-Aug-10
SDA-1177X-US	Jonathan Hubert, Jason Patrick Hanlon	Method of Using the Dual Bus Interface in an ExpressCard Slot	US	Issued	11771744	29-Jun-07	7779184	17-Aug-10
SDA-1177Y-US	Jonathan Hubert, Jason Patrick Hanlon	Dual Bus ExpressCard Peripheral Device	US	Published	11771752	29-Jun-07		
SDA-1178-1X-US	Gerrit Jan Hemink, Teruhiko Kamei	Erase Voltage Manipulation in Non-Volatile Memory for Controlled Shifts in Threshold Voltage	US	Issued	11773921	5-Jul-07	7457166	25-Nov-08

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1178-1Y-US	Gerrit Jan Hemink, Teruhiko Kamei	Systems for Erase Voltage Manipulation in Non-Volatile Memory for Controlled Shifts in Threshold Voltage	US	Issued	11773927	5-Jul-07	7522457	21-Apr-09
SDA-1179X-US	Paul McAvoy, Fabrice Jogand-Coulomb, Pascal Caillon, Benjamin Vigier, Chieh-Hao (John) Yang	Methods of Auto Starting with Portable Mass Storage Device	US	Issued	11769087	27-Jun-07	7861037	28-Dec-10
SDA-1179Y-US	Paul McAvoy, Fabrice Jogand-Coulomb, Pascal Caillon, Benjamin Vigier, Chieh-Hao (John) Yang	Auto Start Configuration with Portable Mass Storage Device	US	Issued	11769099	27-Jun-07	7917697	29-Mar-11
SDA-1180X-US	Prajit Nandi, Sridhar Yadala	Bottom Plate Regulation of Charge Pumps	US	Allowed	11845903	28-Aug-07		
SDA-1180Y-US	Prajit Nandi, Sridhar Yadala	Bottom Plate Regulation of Charge Pumps	US	Published	11845939	28-Aug-07		
SDA-1181X-US	Seungpil Lee, Hao Thai Nguyen, Man Mui	Method for Source Bias All Bit Line Sensing in Non-Volatile Storage	US	Issued	11772002	29-Jun-07	7471567	30-Dec-08
SDA-1181Y-US	Seungpil Lee, Hao Thai Nguyen, Man Mui	Non-Volatile Storage with Source Bias All Bit Line Sensing	US	Issued	11772009	29-Jun-07	7545678	9-Jun-09
SDA-1182X-US	Masaaki Higashitani	Method for Operating Non-Volatile Storage with Individually Controllable Shield Plates between Storage Elements	US	Issued	11767647	25-Jun-07	7636260	22-Dec-09
SDA-1182Y-US	Masaaki Higashitani	Non-Volatile Storage with Individually Controllable Shield Plates between Storage Elements	US	Issued	11767652	25-Jun-07	7808826	5-Oct-10
SDA-1183-1-US	Yan Li	Nonvolatile Memory And Method For Compensating During Programming For Perturbing Charges Of Neighboring Cells	US	Pending	13029787	17-Feb-11		
SDA-1183-1-US	Yan Li	Nonvolatile Memory And Method For Compensating During Programming For Perturbing Charges of Neighboring Cells	US	Issued	11856625	17-Sep-07	7894269	22-Feb-11
SDA-1184-US	Yan Li	Compensating For Coupling During Programming	US	Unfiled				
SDA-1185X-US	Masaaki Higashitani	Method for Fabricating Non-Volatile Storage With Individually Controllable Shield Plates Between Storage Elements	US	Published	12832833	8-Jul-10		
SDA-1185X-US	Masaaki Higashitani	Method for Fabricating Non-Volatile Storage With Individually Controllable Shield Plates Between Storage Elements	US	Issued	11767661	25-Jun-07	7781286	24-Aug-10
SDA-1186-US	Rajeev Nagabhirava, Dhaval Parikh, Arjun Kapoor	Leveraging Portable System Power (Battery) to Enhance Flash Management and Enable Application Level Feature	US	Published	11863035	27-Sep-07		
SDA-1187X-US	J. James Tringali, Sergey Anatolievich Gorobets, Shai Traister, Yosief Atakti	Urgency and Time Window Manipulation to Accomodate Unpredictable Memory Operations	US	Published	11864740	28-Sep-07		
SDA-1187Y-US	J. James Tringali, Sergey Anatolievich Gorobets, Shai Traister, Yosief Atakti	Managing Processing Delays in an Isochronous System	US	Published	11864793	28-Sep-07		
SDA-1188-US	Yan Li	Non-Volatile Memory and Method for Biasing Adjacent Word Line for Verify During Programming	US	Issued	11856639	17-Sep-07	7652929	26-Jan-10
SDA-1189-US	Yan Li, Yupin Fong, Nima Mokhlesi	Non-Volatile Memory and Method for On-Chip Pseudo-Randomization of Data Within a Page and Between Pages	US	Issued	11852229	7-Sep-07	7885112	8-Feb-11

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1191-US	Yaron Sheba, Derek Niizawa	Media Player	US	Issued	29293979	11-Dec-07	D570875	10-Jun-08
SDA-1192-US	Yaron Sheba, Derek Niizawa	Media Player	US	Issued	29293985	11-Dec-07	D578140	7-Oct-08
SDA-1193-US	Yaron Sheba, Derek Niizawa	Media Player	US	Issued	29293982	11-Dec-07	D578137	7-Oct-08
SDA-1194-US	Yaron Sheba, Derek Niizawa	Media Player	US	Issued	29293986	11-Dec-07	D578141	7-Oct-08
SDA-1195-US	Yaron Sheba, Derek Niizawa	Media Player	US	Issued	29293984	11-Dec-07	D578139	7-Oct-08
SDA-1196-US	Yaron Sheba, Derek Niizawa	Media Player	US	Issued	29293983	11-Dec-07	D578138	7-Oct-08
SDA-1197-US	Derek Niizawa, Yaron Sheba	Media Player	US	Issued	29284467	7-Sep-07	D570871	10-Jun-08
SDA-1198-US	Derek Niizawa, Yaron Sheba	Media Player	US	Issued	29284468	7-Sep-07	D572268	1-Jul-08
SDA-1199-US	Derek Niizawa, Yaron Sheba	Media Player	US	Issued	29284469	7-Sep-07	D570872	10-Jun-08
SDA-1200-US	Derek Niizawa, Yaron Sheba	Media Player	US	Issued	29284470	7-Sep-07	D570873	10-Jun-08
SDA-1201-US	Derek Niizawa, Yaron Sheba	Media Player	US	Issued	29284471	7-Sep-07	D570874	10-Jun-08
SDA-1202-US	Derek Niizawa, Jennifer Lee, Matt Schoenholz, Eric Freedman, Stephen Hall	Icon for a Portion of a Display Screen	US	Issued	29284472	7-Sep-07	D580454	11-Nov-08
SDA-1203-US	Derek Niizawa, Jennifer Lee, Matt Schoenholz, Eric Freedman, Stephen Hall	Icon for a Portion of a Display Screen	US	Issued	29284473	7-Sep-07	D580951	18-Nov-08
SDA-1204-a-US	Dana Lee, Emilio Yero	Reducing the Impact of Interference During Programming	US	Pending	12962902	8-Dec-10		
SDA-1204-US	Dana Lee, Emilio Yero	Reducing the Impact of Interference During Programming	US	Issued	11849992	4-Sep-07	7869273	11-Jan-11
SDA-1205-US	Shai Traister, Bum So, Kyeoon Park	Adaptive Block List Management	US	Issued	11851899	7-Sep-07	7818493	19-Oct-10
SDA-1206-US	Toru Miwa	Control Gate Line Architecture	US	Issued	12060746	1-Apr-08	7672163	2-Mar-10
SDA-1208-US	Jason Lin	Secure Digital Host Sector Application Flag Compression	US	Issued	11824183	29-Jun-07	7861046	28-Dec-10
SDA-1209-US	Sergey Anatolievich Gorobets, Shai Traister, Jason Lin, Alan Bennett, Neil Hutchinson	Data Protection For Write Abort	US	Issued	12025937	5-Feb-08	7719890	18-May-10
SDA-1210X-US	Steven T. Sprouse, Dhaval Parikh, Arjun Kapoor	Enhanced Write Abort Mechanism for Non-Volatile Memory	US	Issued	11890734	6-Aug-07	7599241	6-Oct-09
SDA-1211-US	Yaron Sheba, Moshe Raines, David Koren	Media Device	US	Issued	29292396	9-Oct-07	D582419	9-Dec-08
SDA-1212-US	Yaron Sheba, Moshe Raines, David Koren	Remote Control	US	Issued	29292394	9-Oct-07	D589963	7-Apr-09
SDA-1213-US	Yaron Sheba, Moshe Raines, David Koren	Media Device and Remote Control Arrangement	US	Issued	29292395	9-Oct-07	D585077	20-Jan-09
SDA-1214-US	Yaron Sheba, Moshe Raines, David Koren	Media Device Cradle	US	Issued	29292398	9-Oct-07	D582417	9-Dec-08
SDA-1215-US	Yaron Sheba, Moshe Raines, David Koren	Media Device and Cradle Arrangement	US	Issued	29292397	9-Oct-07	D592677	19-May-09
SDA-1216-US	Kurt Webster, Phillip J. Balma, Mairi Macdonald, Ron Barzilai, Avraham Schmuei, Ran Carmeli, David Koren, Moshe Raines	Portable Digital Content Device and Methods for Use Therewith	US	Published	11973871	9-Oct-07		
SDA-1217X-US	Raul-Adrian Cernea	METHOD FOR CORRELATED MULTIPLE PASS PROGRAMMING IN NONVOLATILE MEMORY	US	Issued	12138882	12-Jun-08	7796435	14-Sep-10
SDA-1217Y-a-US	Raul-Adrian Cernea	NONVOLATILE MEMORY WITH CORRELATED MULTIPLE PASS PROGRAMMING	US	Published	12899291	6-Oct-10		

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1217Y-US	Rauli-Adrian Cernea	NONVOLATILE MEMORY WITH CORRELATED MULTIPLE PASS PROGRAMMING	US	Issued	12138387	12-Jun-08	7813172	12-Oct-10
SDA-1218X-US	Rauli-Adrian Cernea	METHOD FOR INDEX PROGRAMMING AND REDUCED VERIFY IN NONVOLATILE MEMORY	US	Issued	12138371	12-Jun-08	7800945	21-Sep-10
SDA-1218Y-US	Rauli-Adrian Cernea	NONVOLATILE MEMORY WITH INDEX PROGRAMMING AND REDUCED VERIFY	US	Issued	12138378	12-Jun-08	7826271	2-Nov-10
SDA-1219-US	Siu Lung Chan	Nonvolatile memory and method for smart program and precharge and verify	US	Unfiled				
SDA-1220-a-US	Yan Li, Yupin Fong, Siu Lung Chan	Nonvolatile Memory And Method With Reduced Program Verify By Ignoring Fastest And/Or Slowest Programming Bits	US	Issued	12407665	19-Mar-09	7894273	22-Feb-11
SDA-1220-b-US	Yan Li, Yupin Fong, Siu Lung Chan	Nonvolatile Memory And Method With Reduced Program Verify By Ignoring Fastest And/Or Slowest Programming Bits	US	Pending	13029848	17-Feb-11		
SDA-1220-US	Yan Li, Yupin Fong, Siu Lung Chan	Nonvolatile Memory And Method With Reduced Program Verify By Ignoring Fastest And/Or Slowest Programming Bits	US	Issued	12249678	10-Oct-08	7768836	3-Aug-10
SDA-1221-a-US	Jeffrey Lutze, Dana Lee	Offset Non-Volatile Storage	US	Published	12822546	24-Jun-10		
SDA-1221-II-US	Jeffrey Lutze, Dana Lee	Offset Non-Volatile Storage	US	Published	12822584	24-Jun-10		
SDA-1221-I-US	Jeffrey Lutze, Dana Lee	Offset Non-Volatile Storage	US	Published	12822568	24-Jun-10		
SDA-1221-US	Jeffrey Lutze, Dana Lee	Offset Non-Volatile Storage	US	Issued	11861135	25-Sep-07	7760547	20-Jul-10
SDA-1224-US	Paul McAvoy, Po Yuan, Alson Kemp	System and Methods for Digital Content Distribution	US	Published	11863714	28-Sep-07		
SDA-1225-US	Kevin Conley, Bill Thanos, Keith D. Washo, Ronald Chung	Portable, Digital Media Player and Associated Methods	US	Published	11863790	28-Sep-07		
SDA-1227-US	Shai Traister	Dynamic Meta-Blocks	US	Published	11864629	28-Sep-07		
SDA-1228-US	Deepak C. Sekar, Nirma Mokhlesi	Single-Layer Metal Conductors With Multiple Thicknesses	US	Published	11861994	26-Sep-07		
SDA-1229-US	Nirma Mokhlesi, Dengtao Zhao, Man Mui, Hao Thai Nguyen, Seungpil Lee, Deepak C. Sekar, Taaban Samaddar	Multiple Bit Line Voltages Based On Distance	US	Issued	11861571	26-Sep-07	7551477	23-Jun-09
SDA-1230-US	Nirma Mokhlesi, Dengtao Zhao, Henry Chin, Taaban Samaddar	Compensation of Non-Volatile-Memory Chip Non-Idealities by Program Pulse Adjustment	US	Allowed	11862485	27-Sep-07		
SDA-1231-I-US	Dana Lee, Hock C So	High Voltage Generation and Control in Source-Side Injection Programming of Non-Volatile Memory	US	Pending	13028847	16-Feb-11		
SDA-1231-US	Dana Lee, Hock C So	High Voltage Generation and Control in Source-Side Injection Programming of Non-Volatile Memory	US	Issued	11864825	28-Sep-07	7894263	22-Feb-11
SDA-1232-US	Dana Lee, Jun Wan	Reducing Programming Voltage Differential Nonlinearity in Non-Volatile Storage	US	Issued	11861909	26-Sep-07	7577034	18-Aug-09
SDA-1233-I-US	Kong Hoo KH Ong, Java Zhu, Ning Ye, Hem Takiar	Method of Reducing Memory Card Edge Roughness by Edge Coating	US	Pending	12901861	11-Oct-10		
SDA-1233-US	Kong Hoo KH Ong, Java Zhu, Ning Ye, Hem Takiar	Method of Reducing Memory Card Edge Roughness by Edge Coating	US	Issued	11863809	28-Sep-07	7811859	12-Oct-10
SDA-1235-US	Steven Cheng	Nonvolatile Memory with Self Recovery	US	Issued	11861146	25-Sep-07	7873803	18-Jan-11
SDA-1236-US	Steven T. Sprouse, Dhaval Parikh, Michael James McCarthy	Multi-Host Interface Controller with USB PHY/Analog Functions Integrated in a Single Package	US	Issued	11906582	2-Oct-07	7898091	1-Mar-11

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1237-US	Steven T. Sprouse, Dhaval Parikh, Sukh Lobana, Shai Traister	Non-Volatile Memory Cell Endurance using Data Encoding	US	Issued	11903764	24-Sep-07	7697325	13-Apr-10
SDA-1238-a-US	Mei Yan, Chieh-Hao (John) Yang, Solomon Junzhi Wang, Paul McAvoy, Bahman Qawami, Farshid Sabet-Sharghi, Patricia Duwer, Po Yuan	File System Filter Authentication	US	Published	12727030	18-Mar-10		
SDA-1238-US	Mei Yan, Chieh-Hao (John) Yang, Solomon Junzhi Wang, Paul McAvoy, Bahman Qawami, Farshid Sabet-Sharghi, Patricia Duwer, Po Yuan	File System Filter Authentication	US	Published	11963960	24-Dec-07		
SDA-1239X-US	Mei Yan, Chieh-Hao (John) Yang, Bahman Qawami, Farshid Sabet-Sharghi, Patricia Duwer, Po Yuan	Authentication for Access to Software Development Kit for a Peripheral Device	US	Published	12124450	21-May-08		
SDA-1239Y-US	Mei Yan, Chieh-Hao (John) Yang, Bahman Qawami, Farshid Sabet-Sharghi, Patricia Duwer, Po Yuan	Systems for Authentication for Access to Software Development Kit for a Peripheral Device	US	Pending	12124456	21-May-08		
SDA-1240-US	Myeong Joon Kang	Portable Handheld Memory Card and Methods for Use Therewith	US	Published	11986389	20-Nov-07		
SDA-1241-US	Sergey Anatolievich Gorobets, Niv Cohen, Russ Reynolds	Method and System for Virtual Fast Access Non-Volatile RAM	US	Published	11939318	13-Nov-07		
SDA-1242-US	Bryan Mee, Yosief Atakti, Alan Bennett	Data Protection After Possible Write Abort or Erase Abort	US	Published	12026357	5-Feb-08		
SDA-1243-US	Jason Lin, Shai Traister, Sergey Anatolievich Gorobets	Pointers for Write Abort Handling	US	Published	12026472	5-Feb-08		
SDA-1244-US	Sergey Anatolievich Gorobets, Alan Bennett, Neil Hutchinson, Shai Traister, Jason Lin	Write Abort and Erase Abort Handling	US	Published	12026021	5-Feb-08		
SDA-1245-US	James Xian Jun Liu, Robert C Chang, Po Yuan, Solomon Junzhi Wang, Ron Barzilai, Bahman Qawami, Farshid Sabet-Sharghi	Integration of Secure Data Transfer Applications for Generic IO Devices	US	Published	11956042	13-Dec-07		
SDA-1246-US	Dana Lee, Nima Mokhlesi, Deepak C. Sekar	Regulation of Source Potential to Combat Cell Source IR Drop	US	Issued	11961871	20-Dec-07	7764547	27-Jul-10
SDA-1250-a-US	Hao Thai Nguyen, Man Muji, Seungpil Lee	Low Noise Sense Amplifier Array and Method for Nonvolatile Memory	US	Published	12563918	21-Sep-09		
SDA-1250-US	Hao Thai Nguyen, Man Muji, Seungpil Lee	Low Noise Sense Amplifier Array and Method for Nonvolatile Memory	US	Issued	11966325	28-Dec-07	7593265	22-Sep-09
SDA-1251-US	Feng Pan, Jonathan Huynh, Qui Nguyen	Low Voltage Charge Pump with Regulation	US	Issued	11955221	12-Dec-07	7586362	8-Sep-09
SDA-1252-US	George Matarnis, Henry Chien, Vinod Robert Purayath, Takashi Orimoto, James Kai	Floating Gate with Universal Etch Stop Layer	US	Published	11951998	6-Dec-07		
SDA-1253-I-US	Steven Cheng	Device Identifiers for Non-Volatile Memory Modules	US	Pending	13042043	7-Mar-11		
SDA-1253-US	Steven Cheng	Device Identifiers for Non-Volatile Memory Modules	US	Allowed	11952744	7-Dec-07		
SDA-1254-US	Feng Pan, Jonathan Huynh	Diode-Connected Regulation of Charge Pumps	US	Issued	11955237	12-Dec-07	7586363	8-Sep-09
SDA-1255-I-US	Feng Pan, Trung Pham, Byungki Woo	Read Verify Word Line Reference Voltage to Track Source Level	US	Published	12715858	2-Mar-10		



FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1255-US	Feng Pan, Trung Pham, Byungki Woo	Read Verify Word Line Reference Voltage to Track Source Level	US	Issued	11961917	20-Dec-07	7701761	20-Apr-10
SDA-1256-US	Gerrit Jan Hemink	Faster Programming of Multi-Level Non-Volatile Storage Through Reduced Verify Operations	US	Issued	11952295	7-Dec-07	7688638	30-Mar-10
SDA-1258-US	Takashi Oritomo, George Matamis, James Kai, Vinod Robert Purayath	Enhanced Endpoint Detection in Non-Volatile Memory Fabrication Processes	US	Published	11960485	19-Dec-07		
SDA-1259-I-US	Vinod Robert Purayath, George Matamis, Takashi Oritomo, James Kai	Composite Charge Storage Structure Formation in Non-Volatile Memory Using Etch Stop Technologies	US	Allowed	12615154	9-Nov-09		
SDA-1259-US	Vinod Robert Purayath, George Matamis, Takashi Oritomo, James Kai	Composite Charge Storage Structure Formation in Non-Volatile Memory Using Etch Stop Technologies	US	Issued	11960498	19-Dec-07	7615447	10-Nov-09
SDA-1260-US	Vinod Robert Purayath, George Matamis, Takashi Oritomo, James Kai	Lithographically Spaced-Defined Charge Storage Regions in Non-Volatile Memory	US	Issued	11960513	19-Dec-07	7807529	5-Oct-10
SDA-1261-US	Vinod Robert Purayath, George Matamis, Takashi Oritomo, James Kai	Non-Volatile Memory Fabrication and Isolation for Composite Charge Storage Structures	US	Issued	11960518	19-Dec-07	7888210	15-Feb-11
SDA-1262-US	Vinod Robert Purayath, George Matamis, Takashi Oritomo, James Kai, Tuan D. Pham	Method of forming memory with floating gates including self-aligned metal nanodots using a polymer solution	US	Pending	11958875	18-Dec-07		
SDA-1263-I-US	Vinod Robert Purayath, George Matamis, Takashi Oritomo, James Kai, Tuan D. Pham	Method of forming memory with floating gates including self-aligned metal nanodots using a coating layer	US	Published	12754408	5-Apr-10		
SDA-1263-US	Vinod Robert Purayath, George Matamis, Takashi Oritomo, James Kai, Tuan D. Pham	Method of forming memory with floating gates including self-aligned metal nanodots using a coating layer	US	Issued	11958941	18-Dec-07	7723186	25-May-10
SDA-1264-US	Alan Welsh Sinclair, Barry Wright	System and Method for Controlling an Amount of Unprogrammed Capacity in Memory Blocks of a Mass Storage System	US	Published	11963413	21-Dec-07		
SDA-1265-US	Sergey Anatolievich Gorobets	System and Method for Implementing Extensions to Intelligently Manage Resources of a Mass Storage System	US	Published	11963479	21-Dec-07		
SDA-1266-I-US	Masaaki Higashitani	Non-Volatile Storage with Substrate Cut-Out and Process of Fabricating	US	Pending	13043980	9-Mar-11		
SDA-1266-US	Masaaki Higashitani	Non-Volatile Storage with Substrate Cut-Out and Process of Fabricating	US	Allowed	11964445	26-Dec-07		
SDA-1267-US	Bill Thanos	MTP-Capable USB Device and Methods for Use Therewith	US	Published	12005547	26-Dec-07		
SDA-1268-US	Paul Lassa, Paul Paternoster, Po-Shen Lai	Optionally Bonding Either Two Sides or More Sides of Integrated Circuits	US	Pending	11966012	28-Dec-07		
SDA-1269-US	Daniel Nguyen, Steve Chi	Systems and Circuits with Multirange and Localized Detection of Valid Power	US	Issued	11965943	28-Dec-07	7830039	9-Nov-10
SDA-1270-US	Po-Shen Lai, Paul Lassa, Paul Paternoster	Exclusive-Option Chips and Methods with All-Options-Active Test Mode	US	Allowed	11966147	28-Dec-07		
SDA-1271-US	Hem Takiar, Shrikar Bhagath, Vincent (Ming Hsun) Lee, Bonnie Chan	TSOP LEADFRAME STRIP OF MULTIPLY ENCAPSULATED PACKAGES	US	Published	11965709	27-Dec-07		
SDA-1272-US	Suresh Upadhyayula, Bonnie Chan, Spencer Fan, Chiang, Hem Takiar	ETCHED SURFACE MOUNT ISLANDS IN A LEADFRAME PACKAGE	US	Published	11966303	28-Dec-07		

PATENT

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1273-a-US	Suresh Upadhyayula, Hem Takiar	SEMICONDUCTOR PACKAGE INCLUDING FLIP CHIP CONTROLLER AT BOTTOM OF DIE STACK	US	Pending	12986927	7-Jan-11		
SDA-1273-US	Suresh Upadhyayula, Hem Takiar	SEMICONDUCTOR PACKAGE INCLUDING FLIP CHIP CONTROLLER AT BOTTOM OF DIE STACK	US	Issued	11965702	27-Dec-07	7867819	11-Jan-11
SDA-1274-US	Suresh Upadhyayula, Robert C Miller, Hem Takiar, Steven T. Sprouse, Lily (Ka lan) Yung	Low Profile Wire Bonded USB Device	US	Published	11965691	27-Dec-07		
SDA-1276-US	Andrew Tomlin, Dennis Ea, Daniel Tuers	Memory Device and Method for Performing a Write-Abort-Safe Firmware Update	US	Published	12005727	28-Dec-07		
SDA-1277-ll-US	Fabrice Jogand-Coulomb, Robert C Chang	Memory Device and Method for Content Virtualization	US	Published	12777399	11-May-10		
SDA-1277-l-US	Fabrice Jogand-Coulomb, Robert C Chang	Memory Device and Method for Content Virtualization	US	Published	12777385	11-May-10		
SDA-1277-US	Fabrice Jogand-Coulomb, Robert C Chang	Memory Device and Method for Content Virtualization	US	Published	12005728	28-Dec-07		
SDA-1279-US	Haluk Kent Taniik, Robert C Chang, Po Yuan, Bahman Qawami, Farshid Sabet-Sharabi	Method and System for Transferring Data and Instructions through a Host File System	US	Published	11967622	31-Dec-07		
SDA-1280-US	Po Yuan, Javier Canis Robles, Mei Yan, Fabrice Jogand-Coulomb, Ahmet Altay, Bahman Qawami, Patricia Dwyer, Robert C Chang, Oktay Rasizade, Farshid Sabet-Sharabi	Method and System for Creating and Accessing a Secure Storage Area in a Non-Volatile Memory Card	US	Pending	11967641	31-Dec-07		
SDA-1281-US	Kevin Conley, Daniel Schreiber, Avi Shmuel, Noam Kedem	Powerfully Simple Digital Media Player and Methods for Use Therewith	US	Published	11967861	31-Dec-07		
SDA-1282-US	Jason Lin, Steven Cheng, Shai Traister	System, Method and Memory Device Providing Data Scrambling Compatible With On-Chip Copy Operation	US	Published	12345921	30-Dec-08		
SDA-1283-US	Paul Paternoster, Vaibhavi Sabharanjak, Po-Shen Lai	Integrated Circuit and Manufacturing Process Facilitating Selective Configuration for Electromagnetic Compatibility	US	Published	11967880	31-Dec-07		
SDA-1284-US	Po-Shen Lai, Vaibhavi Sabharanjak, Ralph Heron, Lakhdar Iguelmamene	Extending Drive Capability in Integrated Circuits Utilizing Programmable-Voltage Output Circuits	US	Issued	11967885	31-Dec-07	7812639	12-Oct-10
SDA-1285-US	Ekram Hossain Bhuiyan	Current Mirror Device and Method	US	Pending	11954924	12-Dec-07		
SDA-1286-US	Steven T. Sprouse, Lily (Ka lan) Yung	Duplicate SD Interface Memory Card Controller	US	Issued	11963519	21-Dec-07	7921255	5-Apr-11
SDA-1289-US	Baojing Liu, Matthew Davidson, Vladamir Kovalev	TEST MODE SOFT RESET CIRCUITRY AND METHODS	US	Allowed	12019534	24-Jan-08		
SDA-1290X-US	Steve Chi, Yongliang Bill Wang, Ekram Hossain Bhuiyan, Daniel Nguyen, Vincent Conditto, Po-Shen Lai	Self-Configurable Multi-Regulator ASIC Core Power Delivery	US	Issued	12005056	21-Dec-07	7859134	28-Dec-10
SDA-1291Y-US	Daniel Nguyen, Steve Chi, Po-Shen Lai	Multi-Regulator Power Delivery System for ASIC Cores	US	Issued	12005144	21-Dec-07	7875996	25-Jan-11
SDA-1292X-US	Hem Takiar, Shrikar Bhagath, Cheeman Yu, Chih Chin Liao	METHOD OF FABRICATING STACKED SEMICONDUCTOR PACKAGE WITH LOCALIZED CAVITIES FOR WIRE BONDING	US	Published	12165297	30-Jun-08		
SDA-1292Y-US	Hem Takiar, Shrikar Bhagath, Cheeman Yu, Chih Chin Liao	STACKED SEMICONDUCTOR PACKAGE WITH LOCALIZED CAVITIES FOR WIRE BONDING	US	Published	12165320	30-Jun-08		

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1293-US	Takashi Orimoto, George Matamis, James Kai	NON-VOLATILE MEMORY ARRAYS HAVING DUAL CONTROL GATE CELL STRUCTURES AND A THICK CONTROL GATE DIELECTRIC AND METHODS OF FORMING	US	Issued	12020428	25-Jan-08	7736973	15-Jun-10
SDA-1294-US	Fabrice Jogand-Coulomb, Mei Yan, Javier Canis Robles, Paul McAvoy	METHODS FOR INSTALLING AN APPLICATION ON A SIM CARD	US	Pending	12629593	2-Dec-09		
SDA-1295-US	Steven Cheng, Stephen Tam	Guaranteed Memory Card Performance to End-of-Life	US	Published	12112204	30-Apr-08		
SDA-1296-US	Alan M. Chiou, Shi-Yen (Wes) Lee, Benjamin Telya, Donald J. Kadish	Methods and Systems for Reconfiguring Data Memory of Embedded Controller Managed Flash Memory Devices	US	Published	12030740	13-Feb-08		
SDA-1297-US	Mei Yan, Robert C Chang, Farshid Sabet-Sharghi, Po Yuan, Bahman Qawami	Memory Device Upgrade	US	Pending	12229090	20-Aug-08		
SDA-1298-US	Mei Yan, Robert C Chang, Farshid Sabet-Sharghi, Po Yuan, Bahman Qawami	Accessing Memory Device Content Using A Network	US	Pending	12229165	20-Aug-08		
SDA-1301-US	Dana Lee, Deepanshu Dutta, Yingda Dong	PROGRAMMING ALGORITHM TO REDUCE DISTURB WITH MINIMAL EXTRA TIME PENALTY	US	Issued	12163073	27-Jun-08	7800956	21-Sep-10
SDA-1302-1-US	Hao Thai Nguyen, Man Muji, Seungpil Lee, Chi-Ming Wang	Sensing in Non-Volatile Storage Using PullDown to Regulated Source Voltage to Remove System Noise	US	Issued	12099439	8-Apr-08	7606076	20-Oct-09
SDA-1303-1-US	Dana Lee, Nima Mokhesi, Anubhav Khandelwal	TWO PASS ERASE FOR NON-VOLATILE STORAGE	US	Published	12835423	13-Jul-10		
SDA-1303-US	Dana Lee, Nima Mokhesi, Anubhav Khandelwal	TWO PASS ERASE FOR NON-VOLATILE STORAGE	US	Issued	12421098	9-Apr-09	7907449	15-Mar-11
SDA-1304-US	Gerrit Jan Hemink, Shih-Chung Lee, Toru Miwa, Yupin Fong, Jun Wan, Ken Oowada	VERIFICATION PROCESS FOR NON-VOLATILE STORAGE	US	Published	12132452	3-Jun-08		
SDA-1305-US	Qui Nguyen, Feng Pan, Jonathan Huynh	Clock Generator Circuit for a Charge Pump	US	Published	12144808	24-Jun-08		
SDA-1306-US	Deepanshu Dutta, Jeffrey Lutze	COMPENSATING NON-VOLATILE STORAGE USING DIFFERENT PASS VOLTAGES DURING PROGRAM-VERIFY AND READ	US	Published	12118446	9-May-08		
SDA-1307-US	Haluk Kent Tanik, Po Yuan, Robert C Chang, Oktay Rasizade, Bahman Qawami, Farshid Sabet-Sharghi	Identification of Memory Cards by Host	US	Published	12082356	9-Apr-08		
SDA-1311-US	Vadzim Struk, Aleh Dzenisiuk, Derrill Sturgeon	Overlay Management in a Flash Memory Storage Device	US	Allowed	12031384	14-Feb-08		
SDA-1312-US	Lily (Ka Ian) Yung, Steven T. Sprouse, Dhaval Parikh, Nathan (Nati) Rapaport	Providing Device Parameters	US	Published	12032501	15-Feb-08		
SDA-1313-US	Jeffrey Lutze, Yan Li	Selective Erase Operation For Non-Volatile Storage	US	Pending	12167124	2-Jul-08		
SDA-1314-a-US	Jeffrey Lutze, Yan Li	Correcting For Over Programming Non-Volatile Storage	US	Published	12877383	8-Sep-10		
SDA-1314-US	Jeffrey Lutze, Yan Li	Correcting For Over Programming Non-Volatile Storage	US	Issued	12167128	2-Jul-08	7852683	14-Dec-10
SDA-1315-US	Alan Welsh Sinclair	Method and System for Memory Block Flushing	US	Published	12036023	22-Feb-08		
SDA-1316-US	Yaron Sheba, Derek Nizawa	Media Player	US	Issued	29301604	7-Mar-08	D593535	2-Jun-09
SDA-1317-US	Yaron Sheba, Derek Nizawa	Media Player	US	Issued	29301603	7-Mar-08	D584743	13-Jan-09

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1318-US	Yaron Sheba, Derek Nizawa	Media Player	US	Issued	29301602	7-Mar-08	D584742	13-Jan-09
SDA-1319-US	Yaron Sheba, Derek Nizawa	Media Player	US	Issued	29301600	7-Mar-08	D584740	13-Jan-09
SDA-1320-US	Yaron Sheba, Derek Nizawa	Media Player	US	Issued	29301601	7-Mar-08	D584741	13-Jan-09
SDA-1323-US	Ahmet Altay, Haluk Kent Tanik, Robert C Chang, James Xian Jun Liu, Oktay Rasizade, Po Yuan	Managing Host Application Privileges	US	Pending	12342965	23-Dec-08		
SDA-1324-US	Rotem Sela, Michael Holtzman, Ron Barzilai, Donald Rav Brvant-Rich	Managing Access to an Address Range in a Storage Device	US	Pending	12338738	18-Dec-08		
SDA-1325-US	Xingzhi Nemo Liang, Hai Bo Fang, Li Wang	METHOD OF FABRICATING WIRE ON WIRE STITCH BONDING IN A SEMICONDUCTOR DEVICE	US	Published	12165375	30-Jun-08		
SDA-1325-US	Xingzhi Nemo Liang, Hai Bo Fang, Li Wang	WIRE ON WIRE STITCH BONDING IN A SEMICONDUCTOR DEVICE	US	Published	12165391	30-Jun-08		
SDA-1326-US	James Kai, Dana Lee, Takashi Orimoto, Vinod Robert Purayath, George Matamis, Henry Chin	METHOD OF FORMING DIELECTRIC LAYER ABOVE FLOATING GATE FOR REDUCING LEAKAGE CURRENT	US	Issued	12170321	9-Jul-08	7915124	29-Mar-11
SDA-1326-US	Dana Lee, Henry Chin, James Kai, Takashi Orimoto, Vinod Robert Purayath, George Matamis	DIELECTRIC LAYER ABOVE FLOATING GATE FOR REDUCING LEAKAGE CURRENT	US	Allowed	12170327	9-Jul-08		
SDA-1327-US	Seungpil Lee, Hao Thai Nguyen, Man Mui	MINIMIZING POWER NOISE DURING SENSING IN MEMORY DEVICE	US	Issued	12163115	27-Jun-08	7751249	6-Jul-10
SDA-1327-US	Seungpil Lee, Hao Thai Nguyen, Man Mui	MEMORY DEVICE WITH POWER NOISE MINIMIZATION DURING SENSING	US	Issued	12163133	27-Jun-08	7751250	6-Jul-10
SDA-1328-US	Jonathan Huynh, Qui Nguyen, Feng Pan	Techniques of Ripple Reduction for Charge Pumps	US	Issued	12146243	25-Jun-08	7683700	23-Mar-10
SDA-1329-a-US	Nirna Mokhlesi	All-Bit-Line Erase Verify and Soft Program Verify	US	Issued	12767660	26-Apr-10	7920422	5-Apr-11
SDA-1329-US	Nirna Mokhlesi	All-Bit-Line Erase Verify and Soft Program Verify	US	Issued	12323413	25-Nov-08	7764544	27-Jul-10
SDA-1330-US	Yan Li, Pao-Ling Anne Koh	Adaptive Algorithm in Cache Operation with Dynamic Data Latch Requirements	US	Published	12051462	19-Mar-08		
SDA-1331-US	Yan Li	Different Combinations of Wordline Order and Look-Ahead Read to Improve Non-Volatile Memory Performance	US	Published	12051492	19-Mar-08		
SDA-1332-US	Vadzim Struk, Derrill Sturgeon	AVOIDANCE OF SELF EVICTION CAUSED BY DYNAMIC MEMORY ALLOCATION IN A FLASH MEMORY STORAGE DEVICE	US	Published	12177863	22-Jul-08		
SDA-1333-US	Fabrice Jogand-Coulomb, Robert C Chang, Po Yuan, Mei Yan, James Xian Jun Liu	CONTROLLED DATA ACCESS TO NON-VOLATILE MEMORY	US	Published	12336445	16-Dec-08		
SDA-1334-US	Oktay Rasizade, Haluk Kent Tanik, Fabrice Jogand-Coulomb	CONTENT BINDING AT FIRST ACCESS	US	Pending	12619112	16-Nov-09		
SDA-1335-US	Rotem Sela, Vijay Ahuja, Michael Holtzman, John Michael Podobnik, Avi Shmuel	Methods for Producing Products with Certificates and Keys	US	Published	12408308	20-Mar-09		
SDA-1336-US	Hyekyung (Sophia) Kim	Housing and Clip Assembly for Portable Electronics Device	US	Published	12134664	6-Jun-08		
SDA-1337-US	George Matamis, Takashi Orimoto, Masaaki Higashitani, James Kai, Tuan D. Pham	Methods of Fabricating Non-Volatile Memory With Integrated Peripheral Circuitry and Pre-Isolation Memory Cell Formation	US	Issued	12061641	2-Apr-08	7582529	1-Sep-09

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1338-US	Tuan D. Pham, Takashi Orimoto, Masaaki Higashitani, James Kai, George Matamis	Methods of Fabricating Non-Volatile Memory With Integrated Select And Peripheral Circuitry and Post-Isolation Memory Cell Formation	US	Issued	12061642	2-Apr-08	75922223	22-Sep-09
SDA-1339-US	Masaaki Higashitani	Non-Volatile Storage Having A Connected Source and Well	US	Published	12060956	2-Apr-08		
SDA-1340X-US	Richard C Spence, Kevin Conley	Method for Playing Digital Media Files with a Digital Media Player Using a Plurality of Playlists	US	Pending	12157844	13-Jun-08		
SDA-1340Y-US	Richard C Spence, Kevin Conley	Memory Device Storing a Plurality of Digital Media Files and Playlists	US	Pending	12157846	13-Jun-08		
SDA-1341-US	Henry R Hutton	USB Memory Card Reader Having Memory Card Identification	US	Allowed	12273463	18-Nov-08		
SDA-1342-US	Hao Thai Nguyen, Man Mui, Seungpil Lee, Fanglein Zhang, Chi-Ming Wang	High Speed Sense Amplifier Array and Method for Nonvolatile Memory	US	Published	12128535	28-May-08		
SDA-1343-US	Henry Chien, Takashi Orimoto, George Matamis, James Kai, Vinod Robert Puravath	Non-Volatile Memory with Sidewall Channels and Raised Source/Drain Regions	US	Issued	12105242	17-Apr-08	7915664	29-Mar-11
SDA-1344-US	Alan Welsh Sinclair	Method and System for Storage Address Re-Mapping for a Multi-Bank Memory Device	US	Pending	12110050	25-Apr-08		
SDA-1345-US	Nima Mokhlesi, Henry Chin	DYNAMIC AND ADAPTIVE OPTIMIZATION OF READ COMPARE LEVELS BASED ON MEMORY CELL THRESHOLD VOLTAGE DISTRIBUTION	US	Published	12338850	18-Dec-08		
SDA-1346-1-US	Yingda Dong, Jeffrey Lutze	Source and Drain Side Early Boosting Using Local Self Boosting For Non-Volatile Storage	US	Issued	12060487	1-Apr-08	7577026	18-Aug-09
SDA-1347-US	Yingda Dong, Man Mui, Jeffrey Lutze, Shinji Sato, Gerrit Jan Hemink	Enhanced Bitline Pre-charge Scheme for increasing Channel Boosting in Non-Volatile Storage	US	Issued	12126375	23-May-08	7719902	18-May-10
SDA-1348X-US	Hem Takiar, Shrikar Bhagath	METHOD OF FABRICATING STACKED WIRE BONDED SEMICONDUCTOR PACKAGE WITH LOW PROFILE BOND LINE	US	Published	12165340	30-Jun-08		
SDA-1348Y-US	Hem Takiar, Shrikar Bhagath	STACKED WIRE BONDED SEMICONDUCTOR PACKAGE WITH LOW PROFILE BOND LINE	US	Published	12165362	30-Jun-08		
SDA-1349X-US	Robert C Miller	METHOD FOR PREVENTING DAMAGE TO A MEMORY CARD	US	Issued	12239093	26-Sep-08	7909611	22-Mar-11
SDA-1349Y-a-US	Robert C Miller	CONNECTOR BLOCK FEATURE	US	Pending	12983752	3-Jan-11		
SDA-1349Y-US	Robert C Miller	CONNECTOR BLOCK FEATURE	US	Issued	12239109	26-Sep-08	7862381	4-Jan-11
SDA-1350-US	Kong Hoo KH Ong, Robertito Piaduche, Ning Ye, Hem Takiar	Method For Reclaiming Semiconductor Package	US	Issued	12165383	30-Jun-08	7745234	29-Jun-10
SDA-1351-a-US	Nima Mokhlesi	Data Refresh for Non-Volatile Storage	US	Published	12903067	12-Oct-10		
SDA-1351-US	Nima Mokhlesi	Data Refresh for Non-Volatile Storage	US	Issued	12338879	18-Dec-08	7859932	28-Dec-10
SDA-1352-I-US	Nima Mokhlesi, Klaus Schuegraf	READ DISTURB MITIGATION IN NON-VOLATILE MEMORY	US	Issued	12832822	8-Jul-10	7867620	25-Jan-11
SDA-1352-US	Nima Mokhlesi, Klaus Schuegraf	READ DISTURB MITIGATION IN NON-VOLATILE MEMORY	US	Issued	12165302	30-Jun-08	7808831	5-Oct-10
SDA-1353-US	Suresh Upadhyayula, Chih Chin Liao, Hem Takiar	Use of BT Substrate as Interposer in SIP Cards	US	Closed				
SDA-1354-US	Benjamin Telya	FLASH Memory Bootloader	US	Unfiled				
SDA-1355-US	Geoffrey S Gongwer	Sloped IC Die Edge to Allow connection Between Stacked IC's	US	Unfiled				

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1359-US	Omprakash Bisen, Karthikeyan Ramamurthi, Hima Bindu	Majority Voting Logic Circuit for Dual Bus Width	US	Published	12166174	1-Jul-08		
SDA-1360-US	Jeffrey Lutze, Yan Li	Programming And Selectively Erasing Non-Volatile Storage	US	Pending	12167135	2-Jul-08		
SDA-1361X-US	Jun Wan, Yupin Fong, Man Mui	METHOD FOR SCRAMBLING DATA IN WHICH SCRAMBLING DATA AND SCRAMBLED DATA ARE STORED IN CORRESPONDING NON-VOLATILE MEMORY LOCATIONS	US	Published	12209697	12-Sep-08		
SDA-1361Y-US	Jun Wan, Yupin Fong, Man Mui	Built-In On-Chip Data Scrambler for Non-Volatile Memory	US	Published	12209708	12-Sep-08		
SDA-1364-US	Yaron Sheba, Derek Niizawa	Media Player	US	Issued	29319349	6-Jun-08	D594874	23-Jun-09
SDA-1365-US	Yaron Sheba, Derek Niizawa	Media Player	US	Issued	29319351	6-Jun-08	D594875	23-Jun-09
SDA-1367-US	Deepanshu Dutta, Jeffrey Lutze, Yingda Dong, Henry Chin, Toru Ishigaki	Compensating for Coupling During Read Operations in Non-Volatile Storage	US	Issued	12188629	8-Aug-08	7876611	25-Jan-11
SDA-1368-US	Masaaki Higashitani	Improving Data Retention of Last Word Line of Non-Volatile Memory Arrays	US	Published	12237321	24-Sep-08		
SDA-1370-US	Mohan Vamsi Dunga, Masaaki Higashitani	Data state-based temperature compensation during sensing in non-volatile memory	US	Issued	12233950	19-Sep-08	7755946	13-Jul-10
SDA-1371-US	Feng Pan, Yuxin Wang, Jonathan Huynh, Albert Chang, Khin Htoo, Qui Nguyen	BANDGAP VOLTAGE AND TEMPERATURE COEFFICIENT TRIMMING ALGORITHM	US	Published	12235467	22-Sep-08		
SDA-1372-US	Rauli-Adrian Cernea	NON-VOLATILE MEMORY AND METHOD FOR SENSING WITH PIPELINED CORRECTIONS FOR NEIGHBORING PERTURBATIONS	US	Issued	12347864	31-Dec-08	7813181	12-Oct-10
SDA-1373-US	Nirma Mokhlesi	Programming Non-Volatile Storage Using Binary and Multi-State Programming Processes	US	Pending	12339005	18-Dec-08		
SDA-1374-US	Steven T. Sprouse, Henry R Hutton, Dhaval Parikh	SWITCHABLE ACCESS STATES FOR NON-VOLATILE STORAGE DEVICES	US	Published	12136672	10-Jun-08		
SDA-1375-US	Yuxin Wang, Feng Pan, Byungki Woo, Truong Pham, Khin Htoo	ON-CHIP BIAS VOLTAGE TEMPERATURE COEFFICIENT SELF-CALIBRATION MECHANISM	US	Issued	12235474	22-Sep-08	7889575	15-Feb-11
SDA-1376-I-US	Shih-Chung Lee, Gerrit Jan Hemink	Adaptive Erase and Soft Programming for Memory	US	Published	12899403	6-Oct-10		
SDA-1376-US	Shih-Chung Lee, Gerrit Jan Hemink	Adaptive Erase and Soft Programming for Memory	US	Issued	12332646	11-Dec-08	7839690	23-Nov-10
SDA-1377-US	Chin-Tien Hope Chiu, Hem Takiar, JiaQing (Jack) Xi	Semiconductor Die Support in an Offset Die Stack	US	Published	12194809	20-Aug-08		
SDA-1379-US	Nirma Mokhlesi, Henry Chin, Masaaki Higashitani	Programming Non-Volatile Storage with Synchronized Coupling	US	Pending	12785636	24-May-10		
SDA-1380-US	Yaron Sheba	Memory Card Holder	US	Issued	29310555	26-Aug-08	D592667	19-May-09
SDA-1381-US	Yaron Sheba	Memory Card Holder	US	Issued	29310554	26-Aug-08	D593561	2-Jun-09
SDA-1382-US	Yaron Sheba	Memory Card Holder	US	Issued	29310553	26-Aug-08	D613293	6-Apr-10
SDA-1384-US	Sergey Anatolevich Gorobets	Methods and Apparatus for Passing Information to a Host System to Suggest Logical Locations to Allocate to a File	US	Pending	12190767	13-Aug-08		
SDA-1385-US	Rauli-Adrian Cernea	NON-VOLATILE MEMORY AND METHOD FOR RAMP-DOWN PROGRAMMING	US	Issued	12197955	25-Aug-08	7715235	11-May-10
SDA-1386-US	Shai Traister, Jonathan Hsu	Program Control of a Non-Volatile Memory	US	Published	12316159	10-Dec-08		

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1387-1-US	Bo Liu, Yan Li, Alexander Mak, Chi-Ming Wang, Eugene Jinglun Tam, Kwang-ho Kim	Structure and Method for Shuffling Data Within Non-Volatile Memory Devices	US	Published	12635449	10-Dec-09		
SDA-1387-US	Yan Li, Cuong Trinh	Folding Data Stored In Binary Format Into Multi-State Format Within Non-Volatile Memory Devices	US	Published	12478997	5-Jun-09		
SDA-1388-US	Vinod Robert Purayath, George Matamis, Takashi Orimoto, Henry Chien, James Kai	METHOD FOR FORMING SELF-ALIGNED DIELECTRIC CAP ABOVE FLOATING GATE	US	Published	12242857	30-Sep-08		
SDA-1390-US	Toru Miwa, Gerrit Jan Hemink	Multi-Pass Programming for Memory with Reduced Data Storage Requirement	US	Published	12344763	29-Dec-08		
SDA-1391-US	Man Mui, Seungpil Lee, Hao Thai Nguyen	Regulation of Recovery Rates in Charge Pumps	US	Issued	12337050	17-Dec-08	7795952	14-Sep-10
SDA-1392-US	Deepanshu Dutta, Jeffrey Lutze	Multi-Pass Programming for Memory Using Word Line Coupling	US	Issued	12252727	16-Oct-08	7839687	23-Nov-10
SDA-1394-US	Hardwell Chibvongdze, Manabu Sakai, Teruhiko Kamei	POINTER BASED COLUMN SELECTION TECHNIQUES IN NON-VOLATILE MEMORIES	US	Published	12490655	24-Jun-09		
SDA-1395-US	Pascal Caillon, Aviv Alluf, Brendan Kavanagh, Kurt Webster, Elliot Broadwin, Jim Schram, Henry R Hutton	SYSTEM AND METHOD OF PREDICTIVE DATA ACQUISITION	US	Published	12495342	30-Jun-09		
SDA-1396-US	Alan Welsh Sinclair	DYNAMIC MAPPING OF LOGICAL RANGES TO WRITE BLOCKS	US	Published	12346433	30-Dec-08		
SDA-1397-US	Richard (JK) Hong	Electrostatic Discharge Protective Circuit having Rise Time Detector and Discharge Sustaining Circuitry	US	Published	12338056	18-Dec-08		
SDA-1398-US	Jason Patrick Hanlon, Fabrice Jogand-Coulomb	REMOVABLE DATA STORAGE DEVICE WITH INTERFACE TO RECEIVE IMAGE CONTENT FROM A CAMERA	US	Published	12495281	30-Jun-09		
SDA-1400-US	Mohan Vamsi Dunga, Masaaki Higashitani, Man Mui	Robust Sensing Circuit and Method	US	Published	12349417	6-Jan-09		
SDA-1401-US	Sanghyun Lee, Masaaki Higashitani, Shinji Sato, Chi-Ming Wang	Non-Volatile Memory with Reduced Leakage Current for Unselected Blocks and Method for Operating Same	US	Issued	12409020	23-Mar-09	7876618	25-Jan-11
SDA-1402-US	Gerrit Jan Hemink	Programming Non-Volatile Memory with Variable Initial Programming Pulse	US	Published	12427007	21-Apr-09		
SDA-1403-US	Jeffrey Lutze, Deepanshu Dutta	Pair Bit Line Programming to Improve Boost Voltage Clamping	US	Published	12398368	5-Mar-09		
SDA-1404-US	Derek Nijzawa, Joe Halpern, Yaron Sheba, Dotan Koren	Digital Media Player User Interface	US	Pending	12338768	18-Dec-08		
SDA-1405-US	Fabrice Jogand-Coulomb	Storage Device and Method for Dynamic Content Tracing	US	Published	12608747	29-Oct-09		
SDA-1406-US	Deepanshu Dutta, Jeffrey Lutze	CONTROLLING SELECT GATE VOLTAGE DURING ERASE TO IMPROVE ENDURANCE IN NON-VOLATILE MEMORY	US	Pending	12406014	17-Mar-09		
SDA-1407-US	Steven T. Sprouse, Carlos J. Gonzalez, Ron Barzilai, Dhaval Parikh	Method for Using a CAPTCHA Challenge to Protect a Removable Mobile Flash Memory Storage Device	US	Pending	12338696	18-Dec-08		

PATENT

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1408-US	Gerrit Jan Hemink	Programming Non-Volatile Memory with High Resolution Variable Initial Programming Pulse	US	Published	12427013	21-Apr-09		
SDA-1409-US	Alan Welsh Sinclair, Nicholas Thomas	Micro-update architecture for address tables	US	Pending	12650182	30-Dec-09		
SDA-1411-US	Rauli-Adrian Cernea	NON-VOLATILE MEMORY AND METHOD WITH TIME-DOMAIN SENSING	US	Allowed	12347876	31-Dec-08		
SDA-1412-US	Sergey Anatolievich Gorobets, Alexander Sasha Paley, Eugene Zilberman, Alan Bennett, Shai Traister	MAPPING ADDRESS TABLE MAINTENANCE IN A MEMORY DEVICE	US	Published	12348782	5-Jan-09		
SDA-1413-US	Sergey Anatolievich Gorobets, Alan Bennett, Eugene Zilberman	SPARE BLOCK MANAGEMENT IN NON-VOLATILE MEMORIES	US	Published	12348825	5-Jan-09		
SDA-1414-US	Alexander Sasha Paley, Sergey Anatolievich Gorobets, Eugene Zilberman, Alan Bennett, Shai Traister, Andrew Tomlin, William Wu, Bum So	NON-VOLATILE MEMORY AND METHOD WITH WRITE CACHE PARTITION MANAGEMENT METHODS	US	Published	12348899	5-Jan-09		
SDA-1415-US	Alexander Sasha Paley, Sergey Anatolievich Gorobets, Eugene Zilberman, Alan Bennett, Shai Traister, Andrew Tomlin, William Wu, Bum So	NON-VOLATILE MEMORY AND METHOD WITH WRITE CACHE PARTITIONING	US	Allowed	12348891	5-Jan-09		
SDA-1416-US	Sergey Anatolievich Gorobets, Bum So, Eugene Zilberman	WEAR LEVELING FOR NON-VOLATILE MEMORIES; MAINTENANCE OF EXPERIENCE COUNT AND PASSIVE TECHNIQUES	US	Published	12348819	5-Jan-09		
SDA-1417-US	Alexander Sasha Paley, Sergey Anatolievich Gorobets, Eugene Zilberman, Alan Bennett, Shai Traister, Andrew Tomlin, William Wu, Bum So	NONVOLATILE MEMORY WITH WRITE CACHE HAVING FLUSH/EVICTION METHODS	US	Published	12348895	5-Jan-09		
SDA-1419-US	Alan Welsh Sinclair	METHOD AND APPARATUS FOR RELOCATING SELECTED DATA BETWEEN FLASH PARTITIONS IN A MEMORY DEVICE	US	Published	12345990	30-Dec-08		
SDA-1420-US	Alan Welsh Sinclair	OPTIMIZED MEMORY MANAGEMENT FOR RANDOM AND SEQUENTIAL DATA WRITING	US	Published	12346451	30-Dec-08		
SDA-1421-1-US	Gautam Dusija, Jian Chen, Chris Nga Yee Avila, Jianmin Huang, Lee Merrill Gavens	NON-VOLATILE MEMORY AND METHOD WITH POST-WRITE READ AND ADAPTIVE RE-WRITE TO MANAGE ERRORS	US	Pending	12642728	18-Dec-09		
SDA-1421-2-US	Lee Merrill Gavens, Jian Chen	NON-VOLATILE MEMORY AND METHOD WITH ACCELERATED POST-WRITE READ TO MANAGE ERRORS	US	Pending	12874064	1-Sep-10		
SDA-1421-US	Jian Chen	NON-VOLATILE MEMORY AND METHOD WITH POST-WRITE READ AND ADAPTIVE RE-WRITE TO MANAGE ERRORS	US	Pending	12607522	28-Oct-09		
SDA-1423-US	Gerrit Jan Hemink	Detecting the Completion of Programming for Non-Volatile Storage	US	Pending	12492421	26-Jun-09		
SDA-1424-US	Ahmet Altay, Po Yuan, Solomon Junzhi Wang, Haluk Kent Tanik	Method and Host Device for Enforcing a Rule Associated with a Media File	US	Pending	12472929	27-May-09		
SDA-1425-US	Fumitoshi Ito, Shinji Sato	Fabricating and Operating a Memory Array Having a Multi-Level Cell Region and a Single-Level Cell Region	US	Published	12414567	30-Mar-09		
SDA-1426-US	Elyahou Harari, Richard R. Heye, Robert David Selinger	Controller and Method for Interfacing Between a Host Controller in a Host and a Flash Memory Device	US	Pending	12539394	11-Aug-09		

PATENT



FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1427-US	Robert David Selinger	Controller and Method for Detecting a Transmission Error Over a NAND Interface Using Error Detection Code	US	Pending	12539407	11-Aug-09		
SDA-1428-US	Robert David Selinger	Controller and Method for Providing Read Status and Spare Block Management Information in a Flash Memory System	US	Pending	12539379	11-Aug-09		
SDA-1430-US	William Wu, Shai Traister, Jianmin Huang, Neil Hutchinson, Steven T. Sprouse	Pre-Emptive Garbage Collection of Memory Blocks	US	Pending	12828241	30-Jun-10		
SDA-1432-US	Yan Li	Programming Memory with Bit Line Floating to Reduce Channel-to-Floating Gate Coupling	US	Pending	12624584	24-Nov-09		
SDA-1433-US	Steven T. Sprouse, Jianmin Huang, Chris Nga Yee Avila, Yichao Huang, Emilio Yero	Optimized Page Programming Order for Non-Volatile Memory	US	Published	12499219	8-Jul-09		
SDA-1434-US	Yingda Dong, Jeffrey Lutze	Reduced Programming Pulse Width For Enhanced Channel Boosting in Non-Volatile Storage	US	Published	12488967	22-Jun-09		
SDA-1435-US	Alan Welsh Sinclair	Method and System for Concurrent Background and Foreground Operations in a Non-Volatile Memory Array	US	Pending	12632549	7-Dec-09		
SDA-1436-US	Neil Hutchinson, Steven T. Sprouse	Garbage Collection of Memory Blocks using Volatile Memory	US	Pending	12828244	30-Jun-10		
SDA-1437-US	Robert David Selinger, Gary Lin, Chaoyang Wang	Method and Controller for Performing a Sequence of Commands	US	Pending	12650255	30-Dec-09		
SDA-1438-US	Jason Lin	Memory Device and Method for Updating a Security Module	US	Published	12495389	30-Jun-09		
SDA-1439-US	Robert C Chang, Po Yuan, James Xian Jun Liu, Bahman Qawami, Haluk Kent Tanik	FILE SYSTEM AND METHOD OF FILE ACCESS	US	Published	12495450	30-Jun-09		
SDA-1440-US	Philip D. Royall, Kinshuk Rakshit, Kevin Kealy, Fabrice Jogand-Coulomb, Itzhak Pomerantz	Selectively Using Local Non-Volatile Storage in Conjunction with Transmission of Content	US	Pending	12494758	30-Jun-09		
SDA-1442-US	Chris Nga Yee Avila, Jonathan Hsu, Alexander Miak, Jian Chen, Grishma Snalesb Shah	PROGRAM FAILURE HANDLING IN NONVOLATILE MEMORY	US	Published	12485827	16-Jun-09		
SDA-1443-US	Chris Nga Yee Avila, Jonathan Hsu, Alexander Miak, Jian Chen, Grishma Snalesb Shah	DATA RECOVERY IN MULTI-LEVEL CELL NONVOLATILE MEMORY	US	Published	12485846	16-Jun-09		
SDA-1444-US	Jonathan Hsu, Shai Traister	Host stop transmission handling	US	Published	12487063	18-Jun-09		
SDA-1445-US	Steven T. Sprouse, Chris Nga Yee Avila, Jianmin Huang	Method and System for Achieving Die Parallelism Through Block Interleaving	US	Pending	12642181	18-Dec-09		
SDA-1446-US	Khin Htoo, Feng Pan, Byungki Woo, Trung Pham, Yuxin Wang	Charge Pump With Reduced Energy Consumption Through Charge Sharing and Clock Boosting Suitable for High Voltage Word Line in Flash Memories	US	Pending	12509367	24-Jul-09		
SDA-1447-US	James Xian Jun Liu, Po Yuan	SYSTEM AND METHOD OF SELECTING A FILE PATH OF A REMOVABLE STORAGE DEVICE	US	Published	12494233	29-Jun-09		
SDA-1448-US	Jason Lin	Memory Device and Method for Adaptive Protection of Content	US	Published	12431353	28-Apr-09		

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1449-US	Jason Lin, Fabrice Jogand-Coulomb	Method and System for Content Replication Control	US	Published	12478688	4-Jun-09		
SDA-1451-US	Tuan D. Pham, Sanghyun Lee, Masato Horiike, Klaus Schuegraf, Masaaki Hieshifani, Keichi Isono	Integrated Circuits And Fabrication Using Sidewall Nitridation Processes	US	Published	12763963	20-Apr-10		
SDA-1452-US	Yingda Dong	Read Operation for Memory with Compensation for Coupling Based on Write-Erase Cycles	US	Issued	12490550	24-Jun-09	7898864	1-Mar-11
SDA-1453-US	Yingda Dong, Cynthia Hsu	Forecasting Program Disturb in Memory by Detecting Natural Threshold Voltage Distribution	US	Issued	12490557	24-Jun-09	7916553	29-Mar-11
SDA-1454-1-US	Yaron Sheba, Philip Lee Ly	Memory Card Holder and Organizer for Holding and Organizing a Plurality of Portable Memory Cables	US	Published	12493134	26-Jun-09		
SDA-1455-a-US	Shrikar Bhagath, Hem Takiar	Semiconductor device having under-filled die in a die stack	US	Pending	12906523	18-Oct-10		
SDA-1455-US	Shrikar Bhagath, Hem Takiar	Method of Under-Filling Semiconductor Die in a Die Stack and Semiconductor Device formed thereby	US	Issued	12494803	30-Jun-09	7816181	19-Oct-10
SDA-1456-US	Yingda Dong, Tien-Chien Kuo, Gerrit Jan Hemink	Selective Memory Cell Program and Erase	US	Published	12544113	19-Aug-09		
SDA-1457-US	Yaron Sheba, Philip Lee Ly	Media Player (1 of 4)	US	Issued	29337735	28-May-09	D605165	1-Dec-09
SDA-1458-US	Yaron Sheba, Philip Lee Ly	Memory Card Holder	US	Issued	29337965	2-Jun-09	D613744	13-Apr-10
SDA-1460-US	Steven Cheng	Memory System with Multi-Level Signaling and Method for Operation the Same	US	Published	12495717	30-Jun-09		
SDA-1461-US	Yaron Sheba, Philip Lee Ly	Media Player (2 of 4)	US	Issued	29337757	28-May-09	D607874	12-Jan-10
SDA-1462-US	Yaron Sheba, Philip Lee Ly	Media Player (3 of 4)	US	Issued	29337759	28-May-09	D605167	1-Dec-09
SDA-1463-US	Yaron Sheba, Philip Lee Ly	Media Player (4 of 4)	US	Issued	29337755	28-May-09	D605166	1-Dec-09
SDA-1464-US	Manuel Antonio D'Abreu, Steve Skala	SYSTEM AND METHOD OF TRACKING ERROR DATA WITHIN A STORAGE DEVICE	US	Published	12494186	29-Jun-09		
SDA-1465-US	Manuel Antonio D'Abreu, Steve Skala	METHOD AND DEVICE FOR SELECTIVELY REFRESHING A REGION OF A NON-VOLATILE MEMORY OF A DATA STORAGE DEVICE	US	Published	12494210	29-Jun-09		
SDA-1466-US	Manuel Antonio D'Abreu, Steve Skala, Dana Lee	SYSTEM AND METHOD RESPONSIVE TO A RATE OF CHANGE OF A PERFORMANCE PARAMETER OF A MEMORY	US	Published	12494222	29-Jun-09		
SDA-1467-US	Saranyan Rajagopalan, Charles Michael Schroter	Multi-Bank Non-Volatile Memory System with Satellite File System	US	Published	12485500	16-Jun-09		
SDA-1468-US	Yan Li, Kwang-ho Kim, Wanfang (Frank) Tsai, Aldo Bottelli	Bad Column Management with Bit Information in Non-Volatile Memory Systems	US	Published	12498220	6-Jul-09		
SDA-1469-US	Feng Pan	CHARGE PUMP WITH CURRENT BASED REGULATION	US	Pending	12506998	21-Jul-09		
SDA-1470-US	Jason Lin, Alexander Kanaris, Joe Halpern	Memory Device and Method for Embedding Host-Identification Information into Content	US	Published	12492751	26-Jun-09		
SDA-1471-US	Ron Barzilai, Michael Holtzman	Method for Performing Double Domain Encryption in a Memory Device	US	Published	12478691	4-Jun-09		
SDA-1474-US	Man Mui, Yingda Dong, Binh Le, Deepanshu Dutta	Partial Speed and Full Speed Programming for Non-Volatile Memory Using Floating Bit Lines	US	Published	12547449	25-Aug-09		

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1475-US	Yingda Dong, Jun Wan	Dynamically Adjustable Erase and Program Levels for Binary Cache Blocks	US	Published	12504576	16-Jul-09		
SDA-1476-US	Changyuan Chen, Jeffrey Lutze, Yingda Dong, Cynthia Hsu	Programming Non-Volatile Storage With Fast Bit Detection and Verify Skip	US	Pending	12638853	15-Dec-09		
SDA-1477-US	Deepanshu Dutta, Henry Chin	PROGRAMMING MEMORY WITH REDUCED PASS VOLTAGE DISTURB AND FLOATING GATE-TO-CONTROL GATE LEAKAGE	US	Published	12536127	5-Aug-09		
SDA-1478-US	Yingda Dong, Toru Ishigaki, Ken Oowada	Program Voltage Compensation with Word Line Bias Change to Suppress Charge Trapping in Memory	US	Published	12512181	30-Jul-09		
SDA-1479-1-US	Feng Pan	Charge Pump with Vt Cancellation through Parallel Structure	US	Published	12570646	30-Sep-09		
SDA-1480-US	Steven Cheng, Aruna Gutta	NONVOLATILE MEMORY CONTROLLER WITH SCALABLE PIPELINED ERROR CORRECTION	US	Pending	12563455	21-Sep-09		
SDA-1481-US	Ekram Hossain Bhuiyan, Shufan Chan	TEMPERATURE-STABLE OSCILLATOR CIRCUIT HAVING FREQUENCY-TO-CURRENT FEEDBACK	US	Pending	12650208	30-Dec-09		
SDA-1482-US	Neil Hutchinson, Alan Bennett, Sergey Anatolievich Gorobets, Steven T. Sprouse	Erase Command Caching to improve erase performance on flash memory	US	Pending	12651176	31-Dec-09		
SDA-1483-US	Damian Yurzola, Sergey Anatolievich Gorobets, Neil Hutchinson, Eran Erez	Physical Compression of data with flat or systematic pattern	US	Pending	12651133	31-Dec-09		
SDA-1484-US	Vinod Robert Purayath, James Kai, Masaaki Higashitani, Takashi Oritomo, George Matamis, Henry Chien	Integrated Nanostructure-Based Non-Volatile Memory Fabrication	US	Published	12840081	20-Jul-10		
SDA-1489-US	Jack Chang Chien, Kong Hoo KH Ong, Weili Wang, Li Wang, Xingzhi Nemo Liang, Shicai Ma	System for Separating a Diced Semiconductor from a Die Attach Tape	US	Pending	12709952	22-Feb-10		
SDA-1490-US	Ben (Chenche) Huang, Albert Chun-Ming Wang, Masaaki Higashitani	Hybrid Bit-Line Contact and Metal Contact via Design For Non-Volatile Storage	US	Published	12813437	10-Jun-10		
SDA-1491-US	Xiaoyu Yang, Qing Li, Albert T. Meeks, Kim Le	Non-Volatile Memory with Reduced Mobile Ion Diffusion	US	Allowed	12560727	16-Sep-09		
SDA-1492-US	Feng Pan	Techniques to Improve Differential Non-Linearity in R-2R Circuits	US	Issued	12645200	22-Dec-09	7903014	8-Mar-11
SDA-1493-US	Zac Shepard, Xiaoyu Yang, Albert T. Meeks, Qing Li, Enosh Levi, Kim Le, Raz Dan, Brian Murobav	Maintaining Integrity of Preloaded Content in Non-Volatile Memory During Surface Mounting	US	Pending	12569869	29-Sep-09		
SDA-1494-US	Robert David Selinger, Gary Lin, Paul Lassa, Chaoyang Wang	Method and Controller for Performing a Copy-Back Operation	US	Pending	12650263	30-Dec-09		
SDA-1495-US	Manuel Antonio D'Abreu, Steve Skala, Carlos J. Gonzalez	System and Method Correct Data Errors Using A Stored Count of Bit Values	US	Pending	12695038	27-Jan-10		
SDA-1496-US	Jason Lin, Ron Barzilai	Storage Device and Method for Providing a Scalable Content Protection System	US	Pending	12651153	31-Dec-09		
SDA-1497-US	Adam Mashaal, Hagai Heshes, Yaron Sheba, Michael McMurdie, Greg Stevens, John Becker, Erez Testiler, Dave Marini	Systems, Methods and Computer Readable Media for Configuring a Rewritable Non-Volatile Memory for Presentation of Media by a Selected Media Presentation Device Model	US	Pending	12624032	23-Nov-09		

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1498-US	Yaron Sheba, Derek Niizawa, John Becker, Erez Testler	Methods, Systems, and Computer Readable Media for Automatic Generation of Graphic Artwork to be Presented During Displaying, Playing or Browsing of Media Files	US	Pending	12624372	23-Nov-09		
SDA-1499-US	Deepanshu Dutta, Jeffrey Lutze, Grishma Shailesh Shah	Data State-Dependent Channel Boosting to Reduce Channel to Floating Gate Coupling in Memory	US	Pending	12616269	11-Nov-09		
SDA-1500-US	Fanglin Zhang, Jongmin Park, Man Mui, Alexander Chu, Seungpil Lee	Voltage Generator To Compensate Sense Amplifier Trip Point Over Temperature In Non-Volatile Memory	US	Pending	12617860	13-Nov-09		
SDA-1501-US	Deepanshu Dutta, Gerrit Jan Hemink	Programming non-volatile memory with a reduced number of verify operations	US	Pending	12625883	25-Nov-09		
SDA-1502-US	Yingda Dong, Yan Li, Cynthia Hsu	Mitigating Channel Coupling Effects During Sensing of Non-Volatile Storage Elements	US	Pending	12773701	4-May-10		
SDA-1504-US	Gen Pei-Lauer, Lanlan Gu, Nirma Mokhleshi, Idan Alrod, Eran Sharon, Itshak Afriat	IDENTIFYING AT-RISK DATA IN NON-VOLATILE STORAGE	US	Pending	12694118	26-Jan-10		
SDA-1505-I-US	Itzhak Pomerantz, Robert C Miller	MicroSD Memory Card with Different Color Surfaces	US	Issued	29359213	7-Apr-10	D628202	30-Nov-10
SDA-1505-US	Itzhak Pomerantz, Robert C Miller	MicroSD Memory Card with Different Color Surfaces	US	Pending	29345635	20-Oct-09		
SDA-1506-I-US	Itzhak Pomerantz, Robert C Miller	MicroSD Memory Card with a Semi-Transparent Color Surface	US	Pending	29359216	28-Feb-11		
SDA-1506-US	Itzhak Pomerantz, Robert C Miller	MicroSD Memory Card with a Semi-Transparent Color Surface	US	Pending	29345641	20-Oct-09		
SDA-1507-US	Itzhak Pomerantz	MicroSD Memory Card with Colored Grip	US	Pending	29345643	20-Oct-09		
SDA-1508-US	Henry R Hutton	Storage Device and Method for Providing a Partially-Encrypted Content File to a Host Device	US	Pending	12836049	14-Jul-10		
SDA-1509-US	Feng Pan	Multi-Stage Charge Pump With Variable Number of Boosting Stages	US	Pending	12634385	9-Dec-09		
SDA-1511-US	Sergey Anatolievich Gorobets, Alan Bennett, Charles Michael Schroter, Eugene Zilberman	Synchronized Maintenance operations in multi-bank storage system	US	Pending	12895383	30-Sep-10		
SDA-1512-US	Deepak Pancholi, Ekram Hossain Bhuiyan, Steve Chi, Naidu Prasad, Bhavin Odedara	Partial Feedback Mechanism in Voltage Regulators to Reduce Output Noise Coupling and DC Voltage Shift at Outout	US	Pending	12632998	8-Dec-09		
SDA-1513A-US	Johann Alsmeyer, Vinod Robert Purayath, Henry Chien, George Matamis, Yao-Sheng Lee, James Kai, Yuan Zhang	ULTRAHIGH DENSITY VERTICAL NAND MEMORY DEVICE AND METHOD OF MAKING THEREOF	US	Pending	12827947	30-Jun-10		
SDA-1513B-US	Johann Alsmeyer	Ultrahigh Density Vertical NAND Memory Device and Method of Making Thereof	US	Pending	12827577	30-Jun-10		
SDA-1513C-US	Johann Alsmeyer	ULTRAHIGH DENSITY VERTICAL NAND MEMORY DEVICE AND METHOD OF MAKING THEREOF	US	Pending	12827869	30-Jun-10		
SDA-1514-US	Deepak Pancholi, Bhavin Odedara, Naidu Prasad	Controlled Load Regulation and Improved Response Time of LDO with Adaptive Current Distribution Mechanism	US	Pending	12693228	25-Jan-10		
SDA-1515-US	Tien-Chien Kuo, Man Mui	Bit Line Stability Detection	US	Pending	12697003	29-Jan-10		

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1516-US	Man Mui, Pao-Ling Anne Koh, Tien-Chien Kuo, Khanh T Nguyen	Non-Volatile Memory with Fast Binary Programming and Reduced Power Consumption	US	Pending	12697017	29-Jan-10		
SDA-1517-US	Deepanshu Dutta, Jeffrey Lutze, Yan Li	DATA RECOVERY FOR NON-VOLATILE MEMORY BASED ON COUNT OF DATA STATE-SPECIFIC FAILS	US	Pending	12695918	28-Jan-10		
SDA-1518-US	Yingda Dong, Shih-Chung Lee, Ken Oowada	Programming Non-Volatile Storage Including Reducing Impact from other Memory Cells	US	Pending	12762342	18-Apr-10		
SDA-1519-US	Jun Wan, Alexander Mak, Tien-Chien Kuo, Yan Li, Jian Chen	Data Coding for Improved ECC Efficiency	US	Pending	12839237	19-Jul-10		
SDA-1520-US	Steven Cheng, Peter Hwang, Annie Chang	FRDY Pull-up Resistor Activation	US	Pending	12824703	28-Jun-10		
SDA-1521-US	Charles Hung-Hsiang Wu	Rule-based Semiconductor Die Stacking and Bonding within a Multi-Die Package	US	Pending	12702065	8-Feb-10		
SDA-1522-US	Steve Chi, Ekram Hossain Bhuiyan	Apparatus and Method for Host Power-On Reset Control	US	Pending	12748345	26-Mar-10		
SDA-1524-US	Sergey Anatolievich Gorobets, William Wu, Shai Traister, Alexander Lyashuk, Steven T. Sprouse	Maintaining Updates of Multi-Level Non-Volatile Memory in Binary Non-Volatile Memory	US	Pending	12642584	18-Dec-09		
SDA-1525-US	Feng Pan	Techniques to Reduce Charge Pump Overshoot	US	Pending	12640820	17-Dec-09		
SDA-1527-US	Gerrit Jan Hemink, Shinji Sato	Non-Volatile Storage with Temperature Compensation Based on Neighbor State Information	US	Pending	12708699	19-Feb-10		
SDA-1528-US	Albert Chun-Ming Wang, Ben (Chenche) Huang, Masaaki Higashitani	Photomask with Assist Features	US	Pending	12729088	22-Mar-10		
SDA-1529-US	Yan Li	Programming Memory With Sensing-Based Bit Line Compensation To Reduce Channel-To-Floating Gate Coupling	US	Pending	12624595	24-Nov-09		
SDA-1530-US	Yan Li	Programming Memory With Direct Bit Line Driving To Reduce Channel-To-Floating Gate Coupling	US	Pending	12624602	24-Nov-09		
SDA-1531-US	Steven T. Sprouse	POWER MANAGEMENT OF FLASH SYSTEMS USING A TOKEN BUCKET ALGORITHM	US	Pending	12623291	20-Nov-09		
SDA-1532-US	Jason Lin, Daniel Schreiber, Henry R Hutton, Joe Halpern	Storage Device and Method for Resuming Playback of Content	US	Pending	12651223	31-Dec-09		
SDA-1533-US	Eyal Hakoun, Yosi Pinto	Automatic and Controllable System Operation	US	Pending	12706519	16-Feb-10		
SDA-1534-US	Sergey Anatolievich Gorobets, Gautam Dusija	NON-VOLATILE MEMORY AND METHOD WITH ATOMIC PROGRAM SEQUENCE AND WRITE ABORT DETECTION	US	Pending	12642740	18-Dec-09		
SDA-1535-US	Jianmin Huang, Chris Nga Yee Avila, Lee Merrill Gavens, Steven T. Sprouse, Sergey Anatolievich Gorobets, Neil Hutchinson	Non-Volatile Memory with Multi-Gear Control Using On-Chip Folding Of Data	US	Pending	12642611	18-Dec-09		
SDA-1537-US	Jianmin Huang, Chris Nga Yee Avila, Lee Merrill Gavens, Neil Hutchinson, Sergey Anatolievich Gorobets	Data Transfer Flows for On-Chip Folding	US	Pending	12642649	18-Dec-09		

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1540-US	Deepak Pancholi, Bhavin Odedara, Naidu Prasad, Srikanth Bojja, Srinivasa Rao Sabbineni, Jayaprakash Naradasi	Self-Calibrating Relaxation Oscillator Based Clock Cycle	US	Pending	12754836	6-Apr-10		
SDA-1541-US	Dhaval Parikh, Tony Ahwal	Auxiliary Interface for Non-Volatile Memory System	US	Pending	12969167	15-Dec-10		
SDA-1542-US	Ken Oowada, Yingda Dong, Deepanshu Dutta	Extra dummy erase pulse(s) after shallow erase verify to avoid sensing deep voltage	US	Pending	12751265	31-Mar-10		
SDA-1544-US	Yingda Dong, Yupin Fong, Gerrit Jan Hemink	Saw-Shaped Multi-Pulse Program Noise Reduction in Memory	US	Pending	12757399	9-Apr-10		
SDA-1545-US	Yan Li	Fast Random Access to Non-Volatile Storage	US	Pending	12835315	13-Jul-10		
SDA-1546-US	Matthew Davidson, Ralph Heron, Lakhdar Iguelmame, Rony (Pc) Tai, Asher Druck	Enhancement of Input/Output for non source-synchronous interfaces	US	Issued	12731504	25-Mar-10	7888966	15-Feb-11
SDA-1547-US	George Samachisa, Johann Alsmeyer	ULTRAHIGH DENSITY VERTICAL NAND MEMORY DEVICE AND METHOD OF MAKING THEREOF	US	Pending	12827761	30-Jun-10		
SDA-1548-US	Jarrett Liang, Vinod Robert Purayath, Takashi Oritomo	Metal Control Gate Formation in Non-Volatile Storage	US	Pending	12845329	28-Jul-10		
SDA-1549-US	Jayavel Pachamuthu, Vinod Robert Purayath	Process for Fabricating Non-Volatile Storage	US	Pending	12762341	18-Apr-10		
SDA-1550-US	Yaron Sheba, Philip Lee Ly, Derek Nilzawa	Media Player	US	Issued	29360076	20-Apr-10	D627758	23-Nov-10
SDA-1551-US	Yuval Weiss, Daniel Weinfeld	Efficient Electrical Hibernation Entry and Recovery	US	Pending	12727073	18-Mar-10		
SDA-1552-US	Jayaprakash Naradasi, Anand Venkitachalam	System and Method of Distributive ECC Processing	US	Pending	12882357	15-Sep-10		
SDA-1553-US	Yan Li, Dana Lee, Jonathan Huynh	Method and Apparatus to Detect Wordline at high Voltage with built-in self-calibration	US	Pending	12833146	9-Jul-10		
SDA-1554-US	Grishma Shailesh Shah, Yan Li	Detection of Broken Word-Lines in Memory Arrays	US	Pending	12833167	9-Jul-10		
SDA-1555-US	Manuel Antonio D'Abreu, Steve Skala, Jayaprakash Naradasi, Anand Venkitachalam	System and Method of Interleaving Data According to an Adjustable Parameter	US	Pending	13031531	21-Feb-11		
SDA-1557-US	Takashi Oritomo, Atsushi Suyama, Ming Tian, Henry Chin, Henry Chien, Vinod Robert Purayath, Dana Lee	P-Type Control Gate in Non-Volatile Storage and Methods for Forming Same	US	Pending	12887328	21-Sep-10		
SDA-1558-US	George Samachisa, Johann Alsmeyer, Andrei Mihnea	Junctionless TFT NAND Flash Memory	US	Pending	12848458	2-Aug-10		
SDA-1559-US	Yosuke Kato	Non-Volatile Memory and Method for Even/Odd Combined Row Decoding	US	Unfiled				
SDA-1560-US	Chih Chin Liao, Cheeman Yu	Corrugated Die Edge for Stacked Die Semiconductor Package	US	Pending	12825617	29-Jun-10		
SDA-1561-US	Deepanshu Dutta, Jeffrey Lutz	Programming Non-Volatile Memory with Bit Line Voltage Set Up	US	Pending	12838902	19-Jul-10		
SDA-1562-US	Sergey Anatolievich Gorobets, Robert Young	Techniques for Maintaining Logical to Physical Mapping Information in Non-Volatile Memory Systems	US	Pending	12821423	23-Jun-10		
SDA-1563-US	Eran Erez, Steven Cheng	Adaptive flash interface	US	Pending	12835292	13-Jul-10		

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1564-a-US	Krishnamurthy Dhakshinamurthy, Damian Yurzola, Rajeev Nagabhirava, Oren Satriasbera	Systems and Methods for Implementing a Programming Sequence to Enhance Die Interleave	US	Pending	12979686	22-Dec-10		
SDA-1565-US	Ben (Chenche) Huang, Albert Chun-Ming Wang, Masaaki Higashitani	Method of Patterning NAND Strings Using Perpendicular SRAF	US	Pending	12897257	4-Oct-10		
SDA-1566-US	Deepanshu Dutta, Jeffrey Lutze	Natural Threshold Voltage Distribution Compaction in Non-Volatile Memory	US	Pending	12849510	3-Aug-10		
SDA-1567-US	Steven Cheng, Dennis Ea, Jianmin Huang, Alexander Mak, Farookh Moogat	Phased NAND Power on Reset	US	Pending	12770358	29-Apr-10		
SDA-1568-US	Sergey Anatolievich Gorobets, Robert Young, Alan Bennett	USE OF GUARD BANDS AND PHASED MAINTENANCE OPERATIONS TO AVOID EXCEEDING MAXIMUM LATENCY REQUIREMENTS IN NON-VOLATILE MEMORY SYSTEMS	US	Pending	12821759	23-Jun-10		
SDA-1570-US	William Wu, Steven T. Sprouse, Sergey Anatolievich Gorobets, Alan Bennett, Ameen Aslam	Method And System For Binary Cache Cleanup	US	Pending	12790960	31-May-10		
SDA-1574-US	Deepanshu Dutta, Jeffrey Lutze, Henry Chin	Multi-Step Channel Boosting to Reduce Channel to Floating Gate Coupling Memory	US	Pending	12894889	30-Sep-10		
SDA-1575-US	Jong Hak Yuh	Wordline Kicking Method when sensing Non-Volatile Storage	US	Pending	12947693	16-Nov-10		
SDA-1576-US	Manuel Antonio D'Abreu, Steve Skala	HYBRID ERROR CORRECTION CODING TO ADDRESS UNCORRECTABLE ERRORS	US	Pending	12913120	27-Oct-10		
SDA-1578P-US	Eliyahou Harari, Tuan D. Pham, Yupin Fong, Vinod Robert Purayath	Non-Volatile Memory with Air Gaps	US	Pending	61354094	11-Jun-10		
SDA-1579P-US	Vinod Robert Purayath, George Matamis, Henry Chien, James Kai, Yuan Zhang	Flat Non-Volatile Memory Cells With Air Gap Isolation	US	Pending	61356600	19-Jun-10		
SDA-1581P-US	Vinod Robert Purayath, Tuan D. Pham, Hiroyuki Kinoshita, Sanghyun Lee, Masaaki Higashitani, Mohan Vamsi Dunga	Air Gap Isolation in Semiconductor Devices	US	Pending	61356630	20-Jun-10		
SDA-1582P-US	Vinod Robert Purayath, George Matamis, Eliyahou Harari	Air Gap Isolation in Non-Volatile Memory	US	Pending	61656603	19-Jun-10		
SDA-1583-US	Jonathan Huynh, Feng Pan, Qui Nguyen, Trung Pham	Level Shifter with Shoot-Through Current Isolation	US	Pending	12895457	30-Sep-10		
SDA-1584-US	Jonathan Huynh, Feng Pan	High Voltage Switch Suitable For Use In Flash Memory	US	Pending	12895476	30-Sep-10		
SDA-1585-US	Jong Hak Yuh	Techniques for the Fast Settling of Word Lines in NAND flash memory	US	Pending	12893611	29-Sep-10		
SDA-1586-US	Chih Chin Liao, Cheeman Yu, Ya Hwei Lee	Semiconductor Device with Die Stack Arrangement Including Staggered Die and Efficient Wire Bonding	US	Pending	12844959	28-Jul-10		
SDA-1587-US	Johann Alsmeyer, Vinod Robert Purayath, James Kai, George Matamis	Stacked Metal Fin Cell	US	Pending	12974098	21-Dec-10		
SDA-1588-US	Haibo Li	Sensing for NAND Memory Based on Word Line Position	US	Pending	12894922	30-Sep-10		
SDA-1589-US	Deepanshu Dutta, Jeffrey Lutze	Alternate bit line bias during programming to reduce channel-to-floating gate coupling in memory	US	Pending	12976893	22-Dec-10		

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDA-1590-US	Feng Pan, Jonathan Huynh	Charge Pump Systems with Reduction in Inefficiencies due to charge sharing between capacitances	US	Pending	12973641	20-Dec-10		
SDA-1592-US	Yan Li, Alon Marcu, Cynthia Hsu, Grishma Shailesh Shah, Cuong Trinh, Mehrdad Mofidi	Manual Suspend and Resume for Non-Volatile Memory	US	Pending	12978001	23-Dec-10		
SDA-1598P-US	Manuel Antonio D'Abreu, Steve Skala	System and Method of Data Encoding	US	Pending	61389100	1-Oct-10		
SDA-1599-US	Yan Li	Alternative Page by Page Programming Scheme	US	Pending	12974817	21-Dec-10		
SDA-1606-US	Baojing Liu, Aruna Gutta, Steve Skala	System and Method for Bonded Configuration Pad Continuity Check	US	Pending	13039110	2-Mar-11		
SDA-1608-US	Feng Pan, Jonathan Huynh, Viswakairan Popuri, Marco Cazzaniga	Wordline Leakage in Memory Arrays: Current Based Approach	US	Pending	13016732	28-Jan-11		
SDA-1610-US	Charles Moana Hook, Loc Tu, Nyi Nyi Thein, James Floyd Cardoso, Ian Arthur Mavers	Memory Card Test Interface	US	Pending	13022404	7-Feb-11		
SDA-1613P-US	Nima Mokhlesi, Mohan Vamsi Dunga, Masaaki Higashitani	Non-Volatile Storage System with Shared Bit Lines Connected to Single Selection Device	US	Pending	61422385	13-Dec-10		
SDA-1616-US	Manuel Antonio D'Abreu, Steve Skala	Data Recovery Using Additional Error Correction Coding Data	US	Pending	13023147	8-Feb-11		
SDA-1622P-US	Jayavel Pachamuthu, Vinod Robert Puravath, George Matamis	Air Isolation in High Density Non-Volatile Memory	US	Pending	61432189	12-Jan-11		
SDA-1629-US	Yishai Kagan, Ilya Shlimenzon	Methods and Systems, and Computer Readable Media for Advance Power Management for Serial Advanced Technology Attachment (SATA)-Based Storage Devices	US	Pending	13040519	4-Mar-11		
SDA-1631P-US	Mohan Vamsi Dunga, Sanghyun Lee, Masaaki Higashitani, Tuan D. Pham	PN Floating Gate Non-Volatile Storage Element	US	Pending	61450528	8-Mar-11		
SDA-1639-US	Sanghyun Lee, Mohan Vamsi Dunga, Masaaki Higashitani, Tuan D. Pham, Franz Krenul	P-/METAL FLOATING GATE NON-VOLATILE STORAGE ELEMENT	US	Pending	61466295	22-Mar-11		
SDK0001.007US	Eliyahou Harari	Flash EEPROM System and Intelligent Programming and Erasing Methods Therefor	US	Issued	7563287	6-Aug-90	5268870	7-Dec-93
SDK0001.010US	Eliyahou Harari	Highly Compact EPROM and Flash EEPROM Devices	US	Issued	7777673	15-Oct-91	5268319	7-Dec-93
SDK0001.011US	Eliyahou Harari	Highly Compact EPROM and Flash EEPROM Devices	US	Issued	7776826	15-Oct-91	5268318	7-Dec-93
SDK0001.012US	Eliyahou Harari	Multi-State Flash EEPROM System Using Incremental Programming and Erasing Methods	US	Issued	7970949	3-Nov-92	5293560	8-Mar-94
SDK0001.013US	Eliyahou Harari	Flash EEPROM Memory Systems and Methods of Using Them	US	Issued	8116867	3-Sep-93	5434825	18-Jul-95
SDK0001.014US	Eliyahou Harari	Highly Compact EPROM and Flash EEPROM Devices	US	Issued	8154162	17-Nov-93	6570790	27-May-03
SDK0001.015US	Eliyahou Harari	Flash EProm System Cell Array With More Than Two Storage States Per Memory Cell	US	Issued	8389295	16-Feb-95	5583812	10-Dec-96
SDK0001.017US	Eliyahou Harari	Highly Compact EPROM and Flash EEPROM Devices	US	Issued	8467762	6-Jun-95	5554553	10-Sep-96

PATENT



FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0001.018US	Eliyahou Harari	Flash EEPROM System Cell Array With Defect Management Including An Error Correction Scheme	US	Issued	8400034	7-Mar-95	5544118	6-Aug-96
SDK0001.019US	Eliyahou Harari	Flash EEPROM System Which Maintains Individual Memory Block Cycle Counts	US	Issued	8468061	6-Jun-95	5568439	22-Oct-96
SDK0001.026US	Eliyahou Harari	Highly Compact EPROM and Flash EEPROM Devices	US	Issued	9190648	12-Nov-98	5963480	5-Oct-99
SDK0004.001US	Sanjay Mehrotra, Eliyahou Harari, Winston Lee	Multi-State EEPROM Read and Write Circuits and Techniques	US	Issued	7508273	11-Apr-90	5172338	15-Dec-92
SDK0004.003US	Sanjay Mehrotra, Eliyahou Harari, Winston Lee	Multi-State EEPROM Read and Write Circuits and Techniques	US	Issued	7734221	22-Jul-91	5163021	10-Nov-92
SDK0008.000US	Eliyahou Harari, Sanjay Mehrotra	Segmented Column Memory Array	US	Issued	7919715	24-Jul-92	5315541	24-May-94
SDK0009.000US	Stephen J. Gross, Robert Norman	Device and Method for Defect Handling in Semi-Conductor Memory	US	Issued	7422949	17-Oct-89	5200959	6-Apr-93
SDK0011.000US	Jack H. Yuan, George Samachisa, Daniel Guterman, Eliyahou Harari	Dense Vertical Programmable Read Only Memory Cell Structure and Processes for Making Them	US	Issued	7629250	18-Dec-90	5343063	30-Aug-94
SDK0011.003US	Jack H. Yuan, George Samachisa, Daniel Guterman, Eliyahou Harari	Dense Vertical Programmable Read Only Memory Cell Structures and Processes for Making Them	US	Issued	8117219	3-Sep-93	5380672	10-Jan-95
SDK0011.004US	Jack H. Yuan, George Samachisa, Daniel Guterman, Eliyahou Harari	Dense Vertical Programmable Read Only Memory Cell Structures and Processes for Making Them	US	Issued	8341411	17-Nov-94	5847425	8-Dec-98
SDK0011.005US	Jack H. Yuan, George Samachisa, Daniel Guterman, Eliyahou Harari	Method of Making Dense Vertical Programmable Read Only Memory Cell Structure	US	Issued	8423218	17-Apr-95	5512505	30-Apr-96
SDK0011.006US	Jack H. Yuan, George Samachisa, Daniel Guterman, Eliyahou Harari	Dense Vertical Programmable Read Only Memory Cell Structures and Processes for Making Them	US	Issued	8844002	18-Apr-97	5965913	12-Oct-99
SDK0013.000US	Stephen J. Gross, Eliyahou Harari, Daniel Guterman, Sanjay Mehrotra	Method for Optimum Erasing of EEPROM	US	Issued	7670246	15-Mar-91	5270979	14-Dec-93
SDK0013.001US	Eliyahou Harari, Daniel Guterman, Sanjay Mehrotra, Stephen J. Gross, John S Managan	Streamlined Write Operation for EEPROM System	US	Issued	8148932	8-Nov-93	5396468	7-Mar-95
SDK0013.002US	Stephen J. Gross, Eliyahou Harari, Daniel Guterman, Sanjay Mehrotra, Robert Norman	Mixed Data Encoding EEPROM System	US	Issued	8148930	8-Nov-93	5504760	2-Apr-96
SDK0013.003US	Stephen J. Gross, Eliyahou Harari, Daniel Guterman, Sanjay Mehrotra	Method for Optimum Erasing of EEPROM	US	Issued	8149602	8-Nov-93	5369615	29-Nov-94
SDK0014.000US	George Samachisa, Stephen J. Gross, Sanjay Mehrotra, Winston Lee	Latent Defect Handling in EEPROM Devices	US	Issued	7948175	21-Sep-92	5428621	27-Jun-95
SDK0014.003US	George Samachisa, Stephen J. Gross, Sanjay Mehrotra, Winston Lee	Latent Defect Handling in EEPROM Devices	US	Issued	8494090	23-Jun-95	5659550	19-Aug-97
SDK0015.000US	Jeffrey Donald Stai, Robert Norman, Karl M Lofgren, Anil Gupta, Sanjay Mehrotra	Solid-State Memory System Including Plural Memory Chips And A Serialized Bus	US	Issued	7736733	26-Jul-91	5430859	4-Jul-95
SDK0015.002US	Jeffrey Donald Stai, Robert Norman, Karl M Lofgren, Anil Gupta, Sanjay Mehrotra	Device and Method for Controlling Solid-State Memory System	US	Issued	8931193	16-Sep-97	5806070	8-Sep-98

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0015.003US	Jeffrey Donald Stai, Robert Norman, Karl M Lofgren, Anil Gupta, Sanjay Mehrotra	Device and Method for Controlling Solid-State Memory System	US	Issued	9064528	21-Apr-98	6148363	14-Nov-00
SDK0015.004US	Jeffrey Donald Stai, Robert Norman, Karl M Lofgren, Anil Gupta, Sanjay Mehrotra	Device and Method for Controlling Solid-State Memory System	US	Issued	9657369	8-Sep-00	6317812	13-Nov-01
SDK0015.005US	Jeffrey Donald Stai, Karl M Lofgren, Anil Gupta, Robert Norman, Sanjay Mehrotra	Device And Method For Controlling Solid-State Memory System	US	Issued	9939290	22-Aug-01	6715044	30-Mar-04
SDK0015.007US	Jeffrey Donald Stai, Karl M Lofgren, Anil Gupta, Robert Norman, Sanjay Mehrotra	Device And Method For Controlling Solid-State Memory System	US	Issued	10809061	24-Mar-04	7688643	30-Mar-10
SDK0015.008US	Jeffrey Donald Stai, Karl M Lofgren, Anil Gupta, Robert Norman, Sanjay Mehrotra	Device And Method For Controlling Solid-State Memory System	US	Published	12619581	17-Nov-09		
SDK0018.001US	Daniel Auclair, Eliyahou Harari	Mass Computer Storage System Having Both Solid State and Rotating Disk Types of Memory	US	Issued	8287107	8-Aug-94	5778418	7-Jul-98
SDK0018.002US	Daniel Auclair, Eliyahou Harari	Mass Computer Storage System Having Both Solid State and Rotating Disk Types of Memory	US	Issued	9063748	21-Apr-98	6016530	18-Jan-00
SDK0022.000US	Gregory B. Thelin, Karl M Lofgren, Robert Norman, Anil Gupta	Wear Leveling Techniques for Flash EEPROM Systems	US	Issued	7759212	13-Sep-91	6230233	8-May-01
SDK0022.001US	Gregory B. Thelin, Karl M Lofgren, Robert Norman, Anil Gupta	Wear Leveling Techniques for Flash EEPROM Systems	US	Issued	9108084	30-Jun-98	6594183	15-Jul-03
SDK0022.002US	Gregory B. Thelin, Karl M Lofgren, Robert Norman, Anil Gupta	Wear Leveling Techniques for Flash EEPROM Systems	US	Issued	9262813	5-Mar-99	6081447	27-Jun-00
SDK0022.004US	Gregory B. Thelin, Karl M Lofgren, Robert Norman, Anil Gupta	Wear Leveling Techniques for Flash EEPROM Systems	US	Issued	10428422	2-May-03	6850443	1-Feb-05
SDK0022.005US	Karl M Lofgren, Robert Norman, Gregory B. Thelin, Anil Gupta	Wear Leveling Techniques for Flash EEPROM Systems	US	Issued	11028882	3-Jan-05	7353325	1-Apr-08
SDK0023.001US	Jeffrey Craig, Richard Albert, Jeffrey Donald Stai, John S Mangan, Robert Norman, Anil Gupta, Karl M Lofgren	Flash EEPROM Array Data and Header File Structure	US	Issued	8252052	1-Jun-94	5438573	1-Aug-95
SDK0023.002US	Jeffrey Craig, Richard Albert, Jeffrey Donald Stai, John S Mangan, Robert Norman, Anil Gupta, Karl M Lofgren	Flash EEPROM Array Data and Header File Structure	US	Issued	8401942	10-Mar-95	5471478	28-Nov-95
SDK0026.001US	Jeffrey Craig, Daniel Auclair, John S Mangan, Robert Norman, Daniel Guterman, Sanjay Mehrotra	Soft Errors Handling in EEPROM Devices	US	Issued	8408109	21-Mar-95	5532962	2-Jul-96
SDK0026.002US	Jeffrey Craig, Daniel Auclair, John S Mangan, Robert Norman, Daniel Guterman, Sanjay Mehrotra	Soft Errors Handling in EEPROM Devices	US	Issued	8406677	20-Mar-95	5657332	12-Aug-97
SDK0026.004US	Jeffrey Craig, Daniel Auclair, John S Mangan, Robert Norman, Daniel Guterman, Sanjay Mehrotra	Soft Errors Handling in EEPROM Devices	US	Issued	9113800	8-Jul-98	6049899	11-Apr-00
SDK0026.006US	Daniel Auclair, Jeffrey Craig, John S Mangan, Robert Norman, Daniel Guterman, Sanjay Mehrotra	Soft Errors Handling in EEPROM Devices	US	Issued	10874825	22-Jun-04	7548461	16-Jun-09
SDK0026.007US	Daniel Auclair, Jeffrey Craig, John S Mangan, Robert Norman, Daniel Guterman, Sanjay Mehrotra	Soft Errors Handling in EEPROM Devices	US	Issued	10917870	13-Aug-04	7437631	14-Oct-08

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0026.008US	Daniel Auclair, Jeffrey Craig, John S Mangan, Robert Norman, Daniel Guferman, Sanjay Mehrotra	Soft Errors Handling In EEprom Devices	US	Issued	10974366	26-Oct-04	7616484	10-Nov-09
SDK0026.009US	Jeffrey Craig, Daniel Auclair, John S Mangan, Robert Norman, Daniel Guferman, Sanjay Mehrotra	Soft Errors Handling In EEprom Devices	US	Issued	12572098	1-Oct-09	7839685	23-Nov-10
SDK0042.000US	Raul-Adrian Cernea, Sanjay Mehrotra, Douglas Lee	Method and Circuit for Simultaneously Programming and Verifying the Programming of Selected EEprom Cells	US	Issued	8089175	8-Jul-93	5422842	6-Jun-95
SDK0042.001US	Raul-Adrian Cernea, Sanjay Mehrotra, Douglas Lee	Method and Circuit for Simultaneously Programming and Verifying the Programming of Selected EEprom Cells	US	Issued	8434970	4-May-95	5495442	27-Feb-96
SDK0042.002US	Raul-Adrian Cernea, Sanjay Mehrotra, Douglas Lee	Method and Circuit for Simultaneously Programming and Verifying the Programming of Selected EEprom Cells	US	Issued	8434969	4-May-95	5532964	2-Jul-96
SDK0043.000US	Raul-Adrian Cernea, Mehrdad Mofidi, Douglas Lee, Sanjay Mehrotra	Programmable Power Generation Circuit for Flash EEprom Memory Systems	US	Issued	8325774	17-Oct-94	5508971	16-Apr-96
SDK0043.001US	Raul-Adrian Cernea, Mehrdad Mofidi, Sanjay Mehrotra, Douglas Lee	Programmable Power Generation Circuit for Flash EEprom Memory Systems	US	Issued	8478268	7-Jun-95	5568424	22-Oct-96
SDK0043.002US	Raul-Adrian Cernea, Mehrdad Mofidi, Douglas Lee, Sanjay Mehrotra	Programmable Power Generation Circuit for Flash EEprom Memory Systems	US	Issued	8482939	7-Jun-95	5592420	7-Jan-97
SDK0043.003US	Raul-Adrian Cernea, Mehrdad Mofidi, Douglas Lee, Sanjay Mehrotra	Programmable Power Generation Circuit for Flash EEprom Memory Systems	US	Issued	8484991	7-Jun-95	5621685	15-Apr-97
SDK0043.004US	Raul-Adrian Cernea, Mehrdad Mofidi, Douglas Lee, Sanjay Mehrotra	Programmable Power Generation Circuit for Flash EEprom Memory Systems	US	Issued	8478244	7-Jun-95	5563825	8-Oct-96
SDK0045.000US	Raul-Adrian Cernea	Charge Pump Circuit With Exponential Multiplication	US	Issued	8157573	24-Nov-93	5436587	25-Jul-95
SDK0048.000US	Jack H. Yuan	Method of Making Dense Flash EEprom Cell Array and Peripheral Supporting Circuits Formed in Deposited Field Oxide With the Use of Spacers	US	Issued	8248735	25-May-94	5661053	26-Aug-97
SDK0048.001US	Jack H. Yuan, George Samachisa	Technique of Forming Over an Irregular Surface a Polysilicon Layer With a Smooth Surface	US	Issued	8358801	19-Dec-94	5595924	21-Jan-97
SDK0048.002US	Jack H. Yuan, George Samachisa, Henry Chien, Elyahou Harari	Dense Flash EEprom Cell Array and Peripheral Supporting Circuits Formed in Deposited Field Oxide With the Use of Spacers	US	Issued	8413960	30-Mar-95	5654217	5-Aug-97
SDK0048.003US	Jack H. Yuan, Henry Chien, George Samachisa, Elyahou Harari	Method of Making Dense Flash EEprom Cell Array and Peripheral Supporting Circuits Formed in Deposited Field Oxide With Sidewall Spacers	US	Issued	8414333	30-Mar-95	5534456	9-Jul-96
SDK0048.004US	Jack H. Yuan, Henry Chien, George Samachisa, Elyahou Harari	Dense Flash EEprom Cell Array and Peripheral Supporting Circuits Formed in Deposited Field Oxide With the Use of Spacers	US	Issued	8562198	22-Nov-95	5756385	26-May-98
SDK0048.005US	Jack H. Yuan, Henry Chien, George Samachisa, Elyahou Harari	A Method of Patterning Polysilicon Layers on Substrate	US	Issued	8787852	23-Jan-97	5747359	5-May-98
SDK0061.000US	Mehrdad Mofidi, Raul-Adrian Cernea, Douglas Lee, Sanjay Mehrotra	Concurrent Write of Multiple Chunks of Data into Multiple Subarrays of Flash EEprom	US	Issued	8743857	5-Nov-96	5890192	30-Mar-99
SDK0061.002US	Mehrdad Mofidi, Raul-Adrian Cernea, Douglas Lee, Sanjay Mehrotra	Latched Address Multi-Chunk Write to EEprom	US	Issued	9642880	21-Aug-00	6542956	1-Apr-03

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0061.005US	Douglas Lee, Mehrdad Mofidi, Sanjay Mehrotra, Raul-Adrian Cernea	Latched Address Multi-Chunk Write To Eeprom	US	Issued	12469531	20-May-09	7890694	15-Feb-11
SDK0062.000US	George Samachisa, Jack H. Yuan	Low Voltage Erase Of A Flash EEPROM System Having A Common Erase Electrode For Two Individually Erasable Sectors	US	Issued	8453124	31-May-95	5579259	26-Nov-96
SDK0064.000US	Mehrdad Mofidi, Douglas Lee, Sanjay Mehrotra, Daniel Guterman	Plane Decode/Virtual Sector Architecture	US	Issued	8719179	24-Sep-96	5798968	25-Aug-98
SDK0064.001US	Mehrdad Mofidi, Douglas Lee, Sanjay Mehrotra, Daniel Guterman	Plane Decode/Virtual Sector Architecture	US	Issued	9063941	21-Apr-98	6069039	30-May-00
SDK0067.001US	Jack H. Yuan	Triple Polysilicon Flash Eeprom Arrays Having a Separate Erase Gate for Each Row of Floating Gates, and Methods of Manufacturing Such Arrays	US	Issued	8908264	7-Aug-97	6028336	22-Feb-00
SDK0074.000US	Raul-Adrian Cernea, Mehrdad Mofidi, Douglas Lee, Sanjay Mehrotra	Flash Eeprom Self-Adaptive Voltage Generation Circuit Operative Within a Continuous Voltage Source Range	US	Issued	8544456	18-Oct-95	5596532	21-Jan-97
SDK0080.000US	Eliyahou Harari	Integrated Circuit Chips Made Bendable by Forming Indentations in Their Back Surfaces, Flexible Packages Thereof and Methods of Manufacture	US	Issued	8678421	2-Jul-96	5786988	28-Jul-98
SDK0091.000US	Douglas Lee, Jian Chen	Multi-State Non-Volatile Flash Memory Capable of Being Its Own Two State Write Cache	US	Issued	8902776	30-Jul-97	5930167	27-Jul-99
SDK0094.000US	Raul-Adrian Cernea, Rushyah Tang, Chi-Ming Wang, Douglas Lee, Daniel Guterman	Non-Volatile Memory With Improved Sensing And Method Therefor	US	Issued	9177809	23-Oct-98	6044019	28-Mar-00
SDK0094.001US	Raul-Adrian Cernea, William Tang, Chi-Ming Wang, Douglas Lee, Daniel Guterman	Non-Volatile Memory with Improved Sensing and Method Therefor	US	Issued	9536930	27-Mar-00	6282120	28-Aug-01
SDK0094.002US	Rushyah Tang, Chi-Ming Wang, Raul-Adrian Cernea, Douglas Lee, Daniel Guterman	Non-Volatile Memory With Improved Sensing And Method Therefor	US	Issued	9924410	7-Aug-01	6490200	3-Dec-02
SDK0094.003US	Raul-Adrian Cernea, Rushyah Tang, Chi-Ming Wang, Douglas Lee, Daniel Guterman	Non-Volatile Memory With Improved Sensing And Method Therefor	US	Issued	10290558	7-Nov-02	6661708	9-Dec-03
SDK0095.000US	Jian Chen, Yupin Fong	High Density Non-Volatile Flash Memory Without Adverse Effects of Electric Field Coupling Between Adjacent Floating Gates	US	Issued	8974276	19-Nov-97	5867429	2-Feb-99
SDK0097.000US	Jack H. Yuan, Eliyahou Harari	Gate Oxide Encroachment Preventative	US	Issued	9239073	27-Jan-99	6281075	28-Aug-01
SDK0097.001US	Jack H. Yuan, Eliyahou Harari	Control Of Floating Gate Oxide Growth By Use Of An Oxide Barrier	US	Issued	9853052	10-May-01	6458658	1-Oct-02
SDK0106.000US	Robert Wallace	Removable Memory Card For Use With Portable Electronic Devices	US	Issued	29085918	1-Apr-98	D445096	17-Jul-01
SDK0106.001US	Robert Wallace, Robert C Miller	Removable Memory Card For Use With Portable Electronic Devices	US	Issued	29109632	19-Aug-99	D447481	4-Sep-01
SDK0106.002US	Robert Wallace, Robert C Miller	Removable Memory Card For Use With Portable Electronic Devices	US	Issued	29141408	4-May-01	D452690	1-Jan-02
SDK0106.003US	Robert Wallace, Robert C Miller	Removable Memory Card For Use With Portable Electronic Devices	US	Issued	29141406	4-May-01	D452865	8-Jan-02

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0106.004US	Robert Wallace, Robert C Miller	Removable Memory Card For Use With Portable Electronic Devices	US	Issued	29141421	4-May-01	D453934	26-Feb-02
SDK0111.000US	Jack H. Yuan, George Samachisa, Eliyahou Harari	Floating Gate Memory Cells Utilizing Substrate Trenches To Scale Down Their Size	US	Issued	9860704	18-May-01	6894343	17-May-05
SDK0111.001US	Jack H. Yuan, George Samachisa, Henry Chien, Eliyahou Harari	Non-Volatile Memory Cells Utilizing Substrate Trenches	US	Issued	9925134	8-Aug-01	6936887	30-Aug-05
SDK0111.002US	Eliyahou Harari, Jack H. Yuan, George Samachisa, Henry Chien	Non-volatile memory cells utilizing substrate trenches	US	Issued	10848242	17-May-04	7087951	8-Aug-06
SDK0111.003US	Eliyahou Harari, Jack H. Yuan, George Samachisa, Henry Chien	Non-Volatile Memory Cells Utilizing Substrate Trenches	US	Issued	11423121	8-Jun-06	7491999	17-Feb-09
SDK0112.000US	Robert Wallace	Semiconductor Package Using Terminals Formed on a Conductive Layer of a Circuit Board	US	Issued	9096140	11-Jun-98	6040622	21-Mar-00
SDK0112.001US	Robert Wallace	Semiconductor Package Using Terminals Formed On A Conductive Layer Of A Circuit Board	US	Issued	9487106	19-Jan-00	6410355	25-Jun-02
SDK0112.002US	Robert Wallace	Semiconductor Package Using Terminals Formed On A Conductive Layer Of A Circuit Board	US	Issued	9866022	24-May-01	6867485	15-Mar-05
SDK0112.003US	Robert Wallace	Semiconductor Package Using Terminals Formed On A Conductive Layer Of A Circuit Board	US	Issued	11021169	21-Dec-04	7053483	30-May-06
SDK0123.000US	Robert Wallace, Eliyahou Harari	Compact Mechanism for Removable Insertion of Multiple Integrated Circuit Cards into Portable and Other Electronic Devices	US	Issued	9123888	28-Jul-98	5933328	3-Aug-99
SDK0130.000US	Robert Wallace, Robert C Miller	Portable Memory Card for Storage of Personal Information	US	Issued	29109687	19-Aug-99	D442598	22-May-01
SDK0130.001US	Robert Wallace, Robert C Miller	Portable Memory Card For Storage Of Personal Information	US	Issued	29141416	4-May-01	D452246	18-Dec-01
SDK0130.002US	Robert Wallace, Robert C Miller	Portable Memory Card For Storage Of Personal Information	US	Issued	29141407	4-May-01	D452245	18-Dec-01
SDK0130.003US	Robert Wallace, Robert C Miller	Portable Memory Card For Storage Of Personal Information	US	Issued	29141365	4-May-01	D452244	18-Dec-01
SDK0134.000US	Michael Holtzman, Yosi Pinto	Removable Computer With Mass Storage	US	Issued	9924185	2-Aug-01	7418344	26-Aug-08
SDK0134.001US	Michael Holtzman, Yosi Pinto	Removable Computer With Mass Storage	US	Published	12215575	26-Jun-08		
SDK0134.002US	Michael Holtzman, Yosi Pinto	Removable Computer With Mass Storage	US	Published	12215573	26-Jun-08		
SDK0136.000US	George Samachisa, Jack H. Yuan, Eliyahou Harari, Daniel Guterman	Dual Floating Gate EEPROM Cell Array with Steering Gates Shared by Adjacent Cells	US	Issued	9343328	30-Jun-99	6151248	21-Nov-00
SDK0136.001US	George Samachisa, Jack H. Yuan, Eliyahou Harari, Daniel Guterman	Dual Floating Gate EEPROM Cell Array with Steering Gates Shared by Adjacent Cells	US	Issued	9634694	8-Aug-00	6266278	24-Jul-01
SDK0137.000US	George Samachisa, Yupin Fong	Method and Structure for Programming Floating Gate Memory Cells	US	Issued	7658098	20-Feb-91	5272669	21-Dec-93
SDK0138.000US	George Samachisa, Daniel Guterman, Yupin Fong, Eliyahou Harari	Eeprom With Split Gate Source Side Injection	US	Issued	7820364	14-Jan-92	5313421	17-May-94
SDK0138.001US	Daniel Guterman, George Samachisa, Yupin Fong, Eliyahou Harari	Method for forming EEPROM with split gate source side injection	US	Issued	8193707	9-Feb-94	5776810	7-Jul-98
SDK0138.003US	George Samachisa, Daniel Guterman, Eliyahou Harari, Yupin Fong	Eeprom With Split Gate Source Side Injection	US	Issued	8639128	26-Apr-96	5847996	8-Dec-98

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0138.004US	George Samachisa, Daniel Guterman, Yupin Fong	Novel Multi-State Memory	US	Issued	8910947	7-Aug-97	6222762	24-Apr-01
SDK0138.005US	George Samachisa, Daniel Guterman, Yupin Fong, Eliyahou Harari	Eeprom With Split Gate Source Side Injection	US	Issued	8908744	7-Aug-97	5883409	16-Mar-99
SDK0138.007US	George Samachisa, Daniel Guterman, Eliyahou Harari, Yupin Fong	Eeprom With Split Gate Source Side Injection	US	Issued	9065512	23-Apr-98	5910915	8-Jun-99
SDK0138.008US	George Samachisa, Daniel Guterman, Eliyahou Harari, Yupin Fong	Eeprom With Split Gate Source Side Injection	US	Issued	9065409	23-Apr-98	5910925	8-Jun-99
SDK0138.009US	Daniel Guterman	Eeprom With Split Gate Source Side Injection With Sidewall Spacers	US	Issued	9207956	9-Dec-98	6002152	14-Dec-99
SDK0138.010US	Daniel Guterman, George Samachisa, Yupin Fong, Eliyahou Harari	EEPROM with Split Gate Source Side Infection with Sidewall Spacers	US	Issued	9386170	31-Aug-99	7071060	4-Jul-06
SDK0138.011US	Daniel Guterman, Yupin Fong	Multi-State Memory	US	Issued	9687986	13-Oct-00	6317363	13-Nov-01
SDK0138.012US	Daniel Guterman, Yupin Fong	Multi-State Memory	US	Issued	9688061	13-Oct-00	6317364	13-Nov-01
SDK0138.013US	Daniel Guterman, Yupin Fong	Multi-State Memory	US	Issued	9688068	13-Oct-00	6275419	14-Aug-01
SDK0138.014US	Daniel Guterman, Yupin Fong	Novel Multi-State Memory	US	Issued	10013592	13-Nov-01	6856546	15-Feb-05
SDK0138.015US	George Samachisa, Daniel Guterman, Yupin Fong, Eliyahou Harari	EEPROM Cell Array Structure With Specific Floating Gate Shape	US	Issued	10236408	5-Sep-02	6664587	16-Dec-03
SDK0138.016US	George Samachisa, Daniel Guterman, Yupin Fong, Eliyahou Harari	Eeprom With Split Gate Source Side Injection With Sidewall Spacers	US	Issued	10225105	20-Aug-02	6954381	11-Oct-05
SDK0138.017US	George Samachisa, Daniel Guterman, Eliyahou Harari, Yupin Fong	Multi-State Operation Of Dual Floating Gate Array	US	Issued	10266450	7-Oct-02	6704222	9-Mar-04
SDK0138.018US	Daniel Guterman, Yupin Fong	Multi-State Memory	US	Issued	10427602	30-Apr-03	6894926	17-May-05
SDK0138.019US	Daniel Guterman, Yupin Fong	Novel Multi-State Memory	US	Published	10410672	8-Apr-03		
SDK0138.020US	George Samachisa, Daniel Guterman, Yupin Fong, Eliyahou Harari	Eeprom With Split Gate Source Side Injection	US	Issued	10674737	29-Sep-03	6861700	1-Mar-05
SDK0138.021US	Daniel Guterman, Yupin Fong	Novel Multi-State Memory	US	Issued	10788184	25-Feb-04	6862218	1-Mar-05
SDK0138.022US	Daniel Guterman, Yupin Fong	Multi-State Memory	US	Issued	10809572	24-Mar-04	7443723	28-Oct-08
SDK0138.023US	Daniel Guterman, Yupin Fong	Multi-State Memory	US	Issued	10921450	18-Aug-04	7187592	6-Mar-07
SDK0138.024US	Daniel Guterman, Yupin Fong	Multi-State Memory	US	Issued	10900498	27-Jul-04	7088615	8-Aug-06
SDK0138.025US	Daniel Guterman, George Samachisa, Yupin Fong, Eliyahou Harari	EEPROM with split gate source side injection	US	Issued	11278778	5-Apr-06	7449746	11-Nov-08
SDK0138.026US	Daniel Guterman, Yupin Fong	Novel Multi-State Memory	US	Issued	11105855	14-Apr-05	7345934	18-Mar-08
SDK0138.027US	Daniel Guterman, Yupin Fong	Multi-State Memory	US	Issued	11243344	3-Oct-05	7385843	10-Jun-08
SDK0138.028US	Daniel Guterman, Yupin Fong	Multi-State Memory	US	Issued	11330923	11-Jan-06	7289360	30-Oct-07
SDK0138.029US	Daniel Guterman, Yupin Fong	Novel Multi-State Memory	US	Issued	11868225	5-Oct-07	7457162	25-Nov-08
SDK0138.031US	Daniel Guterman, Yupin Fong	Multi-State Memory	US	Issued	12110489	28-Apr-08	7573740	11-Aug-09
SDK0138.032US	Daniel Guterman, Yupin Fong	Multi-State Memory	US	Issued	12499581	8-Jul-09	7898868	1-Mar-11
SDK0140.000US	Eliyahou Harari, Jack H. Yuan, George Samachisa	Processing Techniques for Making a Dual Floating Gate EEPROM Cell Array	US	Issued	9343493	30-Jun-99	6103573	15-Aug-00
SDK0140.001US	Jack H. Yuan, George Samachisa, Eliyahou Harari	Processing Techniques for Making a Dual Floating Gate EEPROM Cell Array	US	Issued	9613640	11-Jul-00	6420231	16-Jul-02
SDK0144.000US	Raul-Adrian Cernea	Charge Pump With Fibonacci Number Multiplication	US	Issued	10260115	27-Sep-02	6861894	1-Mar-05
SDK0144.001US	Raul-Adrian Cernea	Charge Pump With Fibonacci Number Multiplication	US	Issued	11069660	28-Feb-05	7135910	14-Nov-06

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0149.000US	Robert Wallace, Wesley G. Brewer, Yosi Pinto	Use Of Small Electronic Circuit Cards With Different Interfaces In An Electronic System	US	Issued	9633089	4-Aug-00	6824063	30-Nov-04
SDK0149.001US	Robert Wallace, Wesley G. Brewer, Yosi Pinto	Use of small electronic circuit cards with different interfaces in an electronic system	US	Issued	10886309	6-Jul-04	7090124	15-Aug-06
SDK0149.002US	Robert Wallace, Wesley G. Brewer, Yosi Pinto	Use Of Small Electronic Circuit Cards With Different Interfaces In An Electronic System	US	Issued	11383928	17-May-06	7350714	1-Apr-08
SDK0154.001US	Robert Wallace	Integrated Circuit Memory Card	US	Issued	29124606	7-Jun-00	D452243	18-Dec-01
SDK0154.002US	Robert Wallace	Memory Card Electrical Contact Structure	US	Issued	9590029	7-Jun-00	6634561	21-Oct-03
SDK0154.003US	Robert Wallace	Memory Card Electrical Contact Structure	US	Issued	10639051	11-Aug-03	7547234	16-Jun-09
SDK0155.000US	Raul-Adrian Cernea, George Samachisa	Memory Array Architecture Utilizing Global BIT Lines Shared by Multiple Cells	US	Issued	9370775	9-Aug-99	6091633	18-Jul-00
SDK0157.000US	Raul-Adrian Cernea	Source Controlled Operation Of Non-Volatile Memories	US	Issued	10600988	20-Jun-03	6950348	27-Sep-05
SDK0157.001US	Raul-Adrian Cernea	Source Controlled Operation Of Non-Volatile Memories	US	Issued	11201000	9-Aug-05	7173860	6-Feb-07
SDK0164.000US	Leon Malned, Anthony J. Knights, Johnny H. Chen, Daniel Auclair	Updated ImageMate Design	US	Issued	29114371	22-Nov-99	D439579	27-Mar-01
SDK0207.000US	Khandker N Quader, Sharon Huynh	Multiple Output Current Mirror with Improved Accuracy	US	Issued	9592469	9-Jun-00	6285615	4-Sep-01
SDK0207.001US	Khandker N Quader, Sharon Huynh	Multiple Output Current Mirror With Improved Accuracy	US	Issued	9912136	24-Jul-01	6396757	28-May-02
SDK0208.000US	Raul-Adrian Cernea, Khandker N Quader, Sanjay Mehrotra	EEPROM Memory Chip with Multiple Use Pinouts	US	Issued	9591090	9-Jun-00	6282130	28-Aug-01
SDK0209.000US	Geoffrey S Gongwer, Shahzad Khalid, Daniel Guterman, Kevin Conley, Richard T. Simko	Writable Tracking Cells	US	Issued	9671793	27-Sep-00	6538922	25-Mar-03
SDK0209.001US	Richard T. Simko, Kevin Conley, Shahzad Khalid, Daniel Guterman, Geoffrey S Gongwer	Sense Amplifier Suitable For Analogue Voltage Levels	US	Issued	10350874	24-Jan-03	6714449	30-Mar-04
SDK0209.002US	Richard T. Simko, Kevin Conley, Shahzad Khalid, Daniel Guterman, Geoffrey S Gongwer	Writable Tracking Cells	US	Issued	10692922	23-Oct-03	6873549	29-Mar-05
SDK0209.003US	Richard T. Simko, Kevin Conley, Shahzad Khalid, Daniel Guterman, Geoffrey S Gongwer	Writable Tracking Cells	US	Issued	11064529	22-Feb-05	7301807	27-Nov-07
SDK0210.000US	Shahzad Khalid	Sense Amplifier For Multilevel Non-Volatile Integrated Memory Devices	US	Issued	9989996	20-Nov-01	6747892	8-Jun-04
SDK0211.000US	Geoffrey S Gongwer, Daniel Guterman	Non-Volatile Memory With Improved Programming And Method Therefor	US	Issued	9793370	26-Feb-01	6738289	18-May-04
SDK0211.002US	Geoffrey S Gongwer, Daniel Guterman	Non-Volatile Memory With Improved Programming And Method Therefor	US	Issued	11126044	9-May-05	7139465	21-Nov-06
SDK0212.000US	Raul-Adrian Cernea	Background Erase For Memory Cells	US	Issued	9956201	17-Sep-01	6741502	25-May-04
SDK0212.001US	Raul-Adrian Cernea	Background Operation for Memory Cells	US	Issued	10677349	1-Oct-03	6845045	18-Jan-05
SDK0212.002US	Raul-Adrian Cernea	Latched Programming of Memory and Method	US	Issued	10842941	10-May-04	7177197	13-Feb-07
SDK0212.003US	Raul-Adrian Cernea	NAND Flash Memory With A Programming Voltage Held Dynamically In A NAND Chain Channel Region	US	Issued	11621190	9-Jan-07	7660156	9-Feb-10

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0212.004US	Raul-Adrian Cernea	NAND Flash Memory with a Programming Voltage Held Dynamically in a NAND Chain Channel Region	US	Allowed	12650011	30-Dec-09		
SDK0213.000US	Raul-Adrian Cernea	Dynamic Column Block Selection	US	Issued	9956416	17-Sep-01	6560146	6-May-03
SDK0213.001US	Raul-Adrian Cernea	Dynamic Column Block Selection	US	Issued	10414972	14-Apr-03	6822911	23-Nov-04
SDK0213.002US	Raul-Adrian Cernea	Dynamic Column Block Selection	US	Issued	10818887	5-Apr-04	6985388	10-Jan-06
SDK0213.003US	Raul-Adrian Cernea	Dynamic Column Block Selection	US	Issued	11241000	29-Sep-05	7586793	8-Sep-09
SDK0213.004US	Raul-Adrian Cernea	Dynamic Column Block Selection	US	Issued	12478133	4-Jun-09	7768841	3-Aug-10
SDK0214.000US	Raul-Adrian Cernea	Memory Device With Pointer Structure To Map Logical To Physical Addresses	US	Issued	10071795	6-Feb-02	6839826	4-Jan-05
SDK0214.001US	Raul-Adrian Cernea	Memory Device with Sector Pointer Structure	US	Issued	11009439	9-Dec-04	7549012	16-Jun-09
SDK0216.000US	Raul-Adrian Cernea	Segmented Metal Bitlines	US	Issued	9960586	21-Sep-01	6552932	22-Apr-03
SDK0216.001US	Raul-Adrian Cernea	Segmented Metal Bitlines	US	Issued	10418416	18-Apr-03	6856541	15-Feb-05
SDK0216.002US	Raul-Adrian Cernea	Segmented Metal Bitlines	US	Issued	10886299	6-Jul-04	6922358	26-Jul-05
SDK0216.003US	Raul-Adrian Cernea	Segmented Metal Bitlines	US	Issued	11152278	13-Jun-05	7158409	2-Jan-07
SDK0217.000US	Jack H. Yuan, Jacob Haskell	Non-Volatile Memory Cell Array Having Discontinuous Source And Drain Diffusions Contacted By Continuous Bit Line Conductors And Methods Of Forming	US	Issued	9667344	22-Sep-00	6512263	28-Jan-03
SDK0217.001US	Jack H. Yuan, Jacob Haskell	Non-Volatile Memory Cell Array Having Discontinuous Source And Drain Diffusions Contacted By Continuous Bit Line Conductors And Methods Of Forming	US	Issued	10265066	3-Oct-02	6723604	20-Apr-04
SDK0217.003US	Jack H. Yuan, Jacob Haskell	Non-Volatile Memory Cell Array Having Discontinuous Source and Drain Diffusions Contacted By Continuous Bit Line Conductors and Methods Of Forming	US	Issued	11227334	14-Sep-05	7288455	30-Oct-07
SDK0217.004US	Jack H. Yuan, Jacob Haskell	Non-Volatile Memory Cell Array Having Discontinuous Source and Drain Diffusions Contacted By Continuous Bit Line Conductors and Methods Of Forming	US	Issued	11867137	4-Oct-07	7541237	2-Jun-09
SDK0223.000US	George Samachisa, Jack H. Yuan, Eliyahou Harari, Daniel Guterman	Steering Gate And Bit Line Segmentation In Non-Volatile Memories	US	Issued	9871333	31-May-01	6532172	11-Mar-03
SDK0229.000US	Nima Mokhlesi	Compressed Event Counting Technique And Application To A Flash Memory System	US	Issued	9662032	14-Sep-00	6345001	5-Feb-02
SDK0229.001US	Nima Mokhlesi	Compressed Event Counting Technique And Application To A Flash Memory System	US	Issued	10033222	27-Dec-01	6643187	4-Nov-03
SDK0229.002US	Nima Mokhlesi	Compressed event counting technique and application to a flash memory system	US	Issued	10718454	19-Nov-03	7113432	26-Sep-06
SDK0229.004US	Nima Mokhlesi	Compressed Event Counting Technique And Application To A Flash Memory System	US	Issued	11468661	30-Aug-06	7397707	8-Jul-08
SDK0231.000US	Kevin Conley, Carlos J. Gonzalez, Daniel Guterman	Method And Structure For Reliable Data Copy Operation For Non-Volatile Memories	US	Issued	9643151	21-Aug-00	6266273	24-Jul-01
SDK0232.000US	Kevin Conley, Carlos J. Gonzalez, Daniel Guterman	Method And Structure For Efficient Data Verification Operation for Non-Volatile Memories	US	Issued	9751178	28-Dec-00	6349056	19-Feb-02

**PATENT**



FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0232.001US	Kevin Conley, Carlos J. Gonzalez, Daniel Guterman	Novel Method And Structure For Efficient Data Verification Operation For Non-Volatile Memories	US	Issued	10040748	28-Dec-01	6560143	6-May-03
SDK0232.002US	Kevin Conley, Carlos J. Gonzalez, Daniel Guterman	Novel Method And Structure For Efficient Data Verification Operation For Non-Volatile Memories	US	Issued	10360829	7-Feb-03	6972993	6-Dec-05
SDK0232.003US	Kevin Conley, Daniel Guterman, Carlos J. Gonzalez	Novel Method And Structure For Efficient Data Verification Operation For Non-Volatile Memories	US	Issued	11125644	9-May-05	7170782	30-Jan-07
SDK0232.004US	Kevin Conley, Daniel Guterman, Carlos J. Gonzalez	Method And Structure For Efficient Data Verification Operation For Non-Volatile Memories	US	Issued	11619991	4-Jan-07	7376011	20-May-08
SDK0234.000US	Carlos J. Gonzalez, Kevin Conley, Eliyahou Harari, Yoram Cedar	Techniques For Operating Non-Volatile Memory Systems With Data Sectors Having Different Sizes Than The Sizes Of The Pages And/Or Blocks Of The Memory	US	Issued	9718802	22-Nov-00	6684289	27-Jan-04
SDK0234.001US	Carlos J. Gonzalez, Kevin Conley, Eliyahou Harari	Techniques For Operating Non-Volatile Memory Systems With Data Sectors Having Different Sizes Than The Sizes Of The Pages And/Or Blocks Of The Memory	US	Issued	10728229	3-Dec-03	7032065	18-Apr-06
SDK0234.002US	Carlos J. Gonzalez, Kevin Conley, Eliyahou Harari	Techniques For Operating Non-Volatile Memory Systems With Data Sectors Having Different Sizes Than The Sizes Of The Pages And/Or Blocks Of The Memory	US	Issued	10727759	3-Dec-03	7171513	30-Jan-07
SDK0237.000US	Feng Pan, Khandker N Quader	High Voltage Switch Suitable For Non-Volatile Memories	US	Issued	10014161	9-Nov-01	6696880	24-Feb-04
SDK0238.000US	Raul-Adrian Cernea	Dual Cell Reading and Writing Technique	US	Issued	9871332	31-May-01	6493269	10-Dec-02
SDK0239.000US	John H Pasternak	Method and System for Distributed Power Generation in Multi-Chip Memory Systems	US	Issued	9785915	16-Feb-01	6577535	10-Jun-03
SDK0240.000US	Geoffrey S Gongwer, Kevin Conley, Chi-Ming Wang, Yongliang Bill Wang, Raul-Adrian Cernea	Method and System for Generation and Distribution of Supply Voltages in Memory Systems	US	Issued	9788120	16-Feb-01	6434044	13-Aug-02
SDK0241.000US	George Samachisa, Chi-Ming Wang, Khandker N Quader, John S Mangan, Daniel Guterman, Brian Murabiy	Method Of Reducing Disturbs In Non-Volatile Memory	US	Issued	9703083	31-Oct-00	6570785	27-May-03
SDK0241.001US	George Samachisa, Chi-Ming Wang, Khandker N Quader, Daniel Guterman, Brian Murabiy	Method Of Reducing Disturbs In Non-Volatile Memory	US	Issued	9759835	10-Jan-01	6717851	6-Apr-04
SDK0241.002US	George Samachisa, Chi-Ming Wang, Khandker N Quader, John S Mangan, Daniel Guterman, Brian Murabiy	Method Of Reducing Disturbs In Non-Volatile Memory	US	Issued	10613098	1-Jul-03	6888752	3-May-05
SDK0241.003US	Daniel Guterman, George Samachisa, Brian Murphy, Chi-Ming Wang, Khandker N Quader	Method Of Reducing Disturbs In Non-Volatile Memory	US	Issued	11054084	8-Feb-05	6977844	20-Dec-05
SDK0241.004US	Daniel Guterman, George Samachisa, Brian Murphy, Chi-Ming Wang, Khandker N Quader	Method of Reducing Disturbs in Non-Volatile Memory	US	Issued	11238911	28-Sep-05	7145804	5-Dec-06
SDK0241.005US	Daniel Guterman, George Samachisa, Brian Murphy, Chi-Ming Wang, Khandker N Quader	Method of Reducing Disturbs in Non-Volatile Memory	US	Issued	11538521	4-Oct-06	7468915	23-Dec-08

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0246.000US	Carlos J. Gonzalez, Shahzad Khalid	Hybrid Density Memory Car	US	Issued	10017035	14-Dec-01	6807106	19-Oct-04
SDK0248.000US	Geoffrey S Gongwer	Efficient Read, Write Methods For Multi-State Memory	US	Issued	10152536	21-May-02	6751129	15-Jun-04
SDK0250.000US	Raul-Adrian Cernea	Method and System for Efficiently Reading and Programming	US	Issued	10087108	28-Feb-02	6795349	21-Sep-04
SDK0250.001US	Raul-Adrian Cernea	Method and System for Efficiently Reading and Programming	US	Issued	10931616	31-Aug-04	6934195	23-Aug-05
SDK0251.000US	Raul-Adrian Cernea	Non-Volatile Memory With Temperature-Compensated Data Read	US	Issued	10053171	2-Nov-01	6560152	6-May-03
SDK0256.001US	Farshid Sabet-Sharghi, Bahman Qawami, Robert C Chang	System, Method, And Device For Playing Back Recorded Audio, Video Or Other Content From Non-Volatile Memory Cards, Compact Disks Or Other Media	US	Issued	10006554	6-Dec-01	7840818	23-Nov-10
SDK0256.002US	Farshid Sabet-Sharghi, Michael Nguyen, Rick Knoblaugh, Robert C Chang, Bahman Qawami	System, Method, And Device For Playing Back Recorded Audio, Video Or Other Content From Non-Volatile Memory Cards, Compact Disks Or Other Media	US	Issued	10006465	6-Dec-01	7227952	5-Jun-07
SDK0256.003US	Farshid Sabet-Sharghi, Michael Nguyen, Rick Knoblaugh, Robert C Chang, Bahman Qawami	System, Method, And Device For Playing Back Recorded Audio, Video Or Other Content From Non-Volatile Memory Cards, Compact Disks Or Other Media	US	Published	11809222	31-May-07		
SDK0256.005US	Farshid Sabet-Sharghi, Bahman Qawami, Robert C Chang	System, Method, And Device For Playing Back Recorded Audio, Video Or Other Content From Non-Volatile Memory Cards, Compact Disks Or Other Media	US	Pending	12927001	3-Nov-10		
SDK0262.000US	Stephen J. Gross, Daniel Guterman, Geoffrey S Gongwer	Increasing The Effectiveness Of Error Correction Codes And Operating Multi-Level Memory Systems By Using Information About The Quality Of The Stored Data	US	Issued	10152137	20-May-02	6751766	15-Jun-04
SDK0262.001US	Daniel Guterman, Stephen J. Gross, Geoffrey S Gongwer	Increasing the Effectiveness of Error Correction Codes and Operating Multi-Level Memory Systems by Using Information About the Quality of the Stored Data	US	Issued	10866554	10-Jun-04	7360136	15-Apr-08
SDK0262.002US	Daniel Guterman, Stephen J. Gross, Geoffrey S Gongwer	Increasing the Effectiveness of Error Correction Codes and Operating Multi-Level Memory Systems by Using Information About the Quality of the Stored Data	US	Published	12041806	4-Mar-08		
SDK0264.001US	Jeffrey Lutze, Nima Mokhlesi	Technique for Fabricating Logic Element Using Multiple Gate Layers	US	Issued	10211433	2-Aug-02	7064034	20-Jun-06
SDK0264.002US	Jeffrey Lutze, Nima Mokhlesi	Technique for Fabricating Logic Element Using Multiple Gate Layers	US	Issued	11435456	16-May-06	7265423	4-Sep-07
SDK0264.003US	Jeffrey Lutze, Nima Mokhlesi	FABRICATING LOGIC AND MEMORY ELEMENTS USING MULTIPLE GATE LAYERS	US	Issued	11540262	29-Sep-06	7425744	16-Sep-08
SDK0270.000US	Geoffrey S Gongwer, Shahzad Khalid, Daniel Guterman	Error Management for Writable Tracking Storage Units	US	Issued	10053339	2-Nov-01	6678192	13-Jan-04
SDK0271.000US	Nima Mokhlesi, John H Pasternak	System And Method For Programming Cells In Non-Volatile Integrated Memory Devices	US	Issued	10359993	6-Feb-03	6856551	15-Feb-05
SDK0271.002US	Nima Mokhlesi, Daniel Guterman	System And Method For Programming Cells In Non-Volatile Integrated Memory Devices	US	Issued	11196547	2-Aug-05	7630237	8-Dec-09

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0271.003US	Nima Mokhlesj, Daniel Guterman	System And Method For Programming Cells In Non-Volatile Integrated Memory Devices	US	Published	12604904	23-Oct-09		
SDK0272.000US	Carlos J. Gonzalez, Daniel Guterman	Reducing The Effects Of Noise In Non-Volatile Memories Through Multiple Reads	US	Issued	10052888	18-Jan-02	6621739	16-Sep-03
SDK0272.001US	Carlos J. Gonzalez, Daniel Guterman	Reducing The Effects Of Noise In Non-Volatile Memories Through Multiple Reads	US	Issued	10439508	16-May-03	6785164	31-Aug-04
SDK0272.002US	Carlos J. Gonzalez, Daniel Guterman	Reducing The Effects Of Noise In Non-Volatile Memories Through Multiple Reads	US	Issued	10799416	12-Mar-04	6952365	4-Oct-05
SDK0272.003US	Carlos J. Gonzalez, Daniel Guterman	Reducing The Effects Of Noise In Non-Volatile Memories Through Multiple Reads	US	Issued	11191823	27-Jul-05	7177195	13-Feb-07
SDK0272.004US	Carlos J. Gonzalez, Daniel Guterman	Reducing The Effects Of Noise In Non-Volatile Memories Through Multiple Reads	US	Issued	11674000	12-Feb-07	7484149	7-Dec-10
SDK0274.000US	Jeffrey Craig, John S Mangan	Method And Apparatus for Dynamic Degradation Detection	US	Issued	10051833	16-Jan-02	7246268	17-Jul-07
SDK0275.000US	Khandker N Quader, Jian Chen	Read And Erase Verify Methods And Circuits Suitable For Low Voltage Non-Volatile Memories	US	Issued	10414132	14-Apr-03	6839281	4-Jan-05
SDK0275.001US	Khandker N Quader, Jian Chen	Read And Erase Verify Methods And Circuits Suitable For Low Voltage Non-Volatile Memories	US	Issued	10552948	11-Sep-06	7420846	2-Sep-08
SDK0276.000US	Stephen J. Gross, Geoffrey S Gongwer	Symbol Frequency Leveling in a Storage System	US	Issued	10230657	29-Aug-02	6941412	6-Sep-05
SDK0276.001US	Stephen J. Gross, Geoffrey S Gongwer	Symbol Frequency Leveling in a Storage System	US	Issued	11201007	9-Aug-05	7266026	4-Sep-07
SDK0278.001US	Carlos J. Gonzalez, Kevin Conley	Automated Wear Leveling In Non-Volatile Storage Systems	US	Issued	10686399	14-Oct-03	7120729	10-Oct-06
SDK0278.002US	Carlos J. Gonzalez, Kevin Conley	Automated wear leveling in non-volatile storage systems	US	Issued	11539972	10-Oct-06	7552272	23-Jun-09
SDK0279.000US	Jason Lin	Multi-Module Simultaneous Program, Erase Test, and Performance Method for Flash Memory	US	Issued	10000828	31-Oct-01	7127550	24-Oct-06
SDK0279.001US	Jason Lin	MULTI-MODULE SIMULTANEOUS PROGRAM, ERASE TEST, AND PERFORMANCE METHOD FOR FLASH MEMORY	US	Allowed	11551977	23-Oct-06		
SDK0280.000US	Jian Chen	Selective Operation Of A Multi-State Non-Volatile Memory System In A Binary Mode	US	Issued	9956340	17-Sep-01	6456528	24-Sep-02
SDK0280.001US	Jian Chen	Selective Operation Of A Multi-State Non-Volatile Memory System In A Binary Mode	US	Issued	10229258	26-Aug-02	6717847	6-Apr-04
SDK0280.003US	Jian Chen	Selective Operation Of A Multi-State Non-Volatile Memory System In A Binary Mode	US	Issued	10818926	5-Apr-04	7177184	13-Feb-07
SDK0280.004US	Jian Chen	Selective Operation Of A Multi-State Non-Volatile Memory System In A Binary Mode	US	Issued	11619533	3-Jan-07	7385854	10-Jun-08
SDK0281.000US	Robert Wallace	Card Manufacturing Technique And Resulting Card	US	Issued	9921664	3-Aug-01	6597061	22-Jul-03
SDK0281.001US	Robert Wallace	Card Manufacturing Technique And Resulting Card	US	Issued	10456077	6-Jun-03	7022547	4-Apr-06
SDK0282.000US	Kevin Conley, Carlos J. Gonzalez	Management Of Non-Volatile Memory Systems Having Large Erase Blocks	US	Published	10749831	30-Dec-03		
SDK0282.001US	Kevin Conley, Carlos J. Gonzalez	Management Of Non-Volatile Memory Systems Having Large Erase Blocks	US	Published	12433702	30-Apr-09		

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0283.000US	Kevin Conley, Yoram Cedar	Pipelined Parallel Programming Operation In A Non-Volatile Memory System	US	Issued	10081375	22-Feb-02	6871257	22-Mar-05
SDK0283.001US	Kevin Conley, Yoram Cedar	Pipelined Parallel Programming Operation In A Non-Volatile Memory System	US	Issued	11058359	14-Feb-05	7162569	9-Jan-07
SDK0283.002US	Kevin Conley, Yoram Cedar	Pipelined Parallel Programming Operation In A Non-Volatile Memory System	US	Issued	11611706	15-Dec-06	7461199	2-Dec-08
SDK0285.000US	Geoffrey S Gongwer, Shahzad Khalid	System And Method For Achieving Fast Switching Of Analog Voltages On Large Capacitive Load	US	Issued	9825615	2-Apr-01	6486715	26-Nov-02
SDK0288.000US	Robert C Chang, Jong Guo, Farshid Sabet-Sharghi	System and Method For Booting From A Non-Volatile Application And File Storage Device	US	Issued	9923874	6-Aug-01	7165137	16-Jan-07
SDK0288.001US	Robert C Chang, Jong Guo, Farshid Sabet-Sharghi	System and Method For Booting From A Non-Volatile Application And File Storage Device	US	Issued	11613907	20-Dec-06	7454557	18-Nov-08
SDK0289.000US	Henry Chien, Yupin Fong	Deep Wordline Trench To Shield Cross Coupling Between Adjacent Cells For Scaled NAND	US	Issued	10175764	19-Jun-02	6894930	17-May-05
SDK0289.001US	Henry Chien, Yupin Fong	Deep Wordline Trench To Shield Cross Coupling Between Adjacent Cells For Scaled NAND	US	Issued	10353570	28-Jan-03	6898121	24-May-05
SDK0289.002US	Henry Chien, Yupin Fong	Deep Wordline Trench To Shield Cross Coupling Between Adjacent Cells For Scaled NAND	US	Issued	11086648	21-Mar-05	7170786	30-Jan-07
SDK0289.003REIS-US	Henry Chien, Yupin Fong	Deep Wordline Trench To Shield Cross Coupling Between Adjacent Cells For Scaled NAND	US	Pending	12363165	30-Jan-09		
SDK0293.000US	Nima Mokhlesi, Daniel Guterma, Geoffrev S Gongwer	Noise Reduction Technique For Transistors And Small Devices Utilizing An Episodic Agitation	US	Issued	10052924	18-Jan-02	6850441	1-Feb-05
SDK0293.001US	Nima Mokhlesi, Daniel Guterma, Geoffrev S Gongwer	Noise reduction technique for transistors and small devices utilizing an episodic agitation	US	Issued	10976692	28-Oct-04	7092292	15-Aug-06
SDK0293.002US	Nima Mokhlesi, Daniel Guterma, Geoffrev S Gongwer	Noise Reduction Technique For Transistors And Small Devices Utilizing An Episodic Agitation	US	Issued	11426082	23-Jun-06	7403421	22-Jul-08
SDK0294.000US	Chi-Ming Wang, Kuo-Lung Chen, Shouchang Tsao	Current-Limited Latch	US	Issued	10313738	6-Dec-02	7339822	4-Mar-08
SDK0294.001US	Chi-Ming Wang, Kuo-Lung Chen, Shouchang Tsao	Current-Limited Latch	US	Issued	11019990	20-Dec-04	7319630	15-Jan-08
SDK0295.000US	Jack H. Yuan, George Samachisa, Eliyahou Harari, Yupin Fong	Scalable Self-Aligned Dual Floating Gate Memory Cell Array And Methods Of Forming The Array	US	Issued	9925102	8-Aug-01	6762092	13-Jul-04
SDK0295.002US	Jack H. Yuan, George Samachisa, Eliyahou Harari, Yupin Fong	Scalable Self-Aligned Dual Floating Gate Memory Cell Array And Methods Of Forming The Array	US	Issued	10799180	12-Mar-04	6953970	11-Oct-05
SDK0295.003US	Jack H. Yuan, George Samachisa, Eliyahou Harari, Yupin Fong	Scalable Self-Aligned Dual Floating Gate Memory Cell Array And Methods Of Forming The Array	US	Issued	11111129	20-Apr-05	7211866	1-May-07
SDK0295.004US	Jack H. Yuan, Eliyahou Harari, Yupin Fong, George Samachisa	Scalable Self-Aligned Dual Floating Gate Memory Cell Array And Methods Of Forming The Array	US	Issued	11689775	22-Mar-07	7858472	28-Dec-10
SDK0298.000US	Raul-Adrian Cernea	Highly Compact Non-Volatile Memory And Method Thereof	US	Issued	10254483	24-Sep-02	6983428	3-Jan-06

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0298.002US	Raul-Adrian Cernea	Highly Compact Non-Volatile Memory And Method Thereof	US	Published	12274959	20-Nov-08		
SDK0299.000US	Eliyahou Harari	Memory Cards Including A Standard Security Function	US	Issued	10418910	17-Apr-03	7305535	4-Dec-07
SDK0299.001US	Eliyahou Harari, Yoram Cedar, Wesley G. Brewer, Yosi Pinto, Reuven Elhamias, Michael Holtzman	Memory Cards Including A Standard Security Function	US	Published	11946558	28-Nov-07		
SDK0300.000US	Nima Mokhlesi, Jeffrey Lutze	Alternate Sensing Techniques for Non-Volatile Memories	US	Issued	11321996	28-Dec-05	7349264	25-Mar-08
SDK0300.001US	Nima Mokhlesi, Jeffrey Lutze	Memories with Alternate Sensing Techniques	US	Issued	11320917	28-Dec-05	7616481	10-Nov-09
SDK0300.002US	Nima Mokhlesi, Jeffrey Lutze	Alternate Sensing Techniques for Non-Volatile Memories	US	Issued	12023317	31-Jan-08	7460406	2-Dec-08
SDK0301.000US	Nima Mokhlesi, Jeffrey Lutze	Pillar Cell Flash Memory	US	Issued	10732967	10-Dec-03	7049652	23-May-06
SDK0301.001US	Nima Mokhlesi, Jeffrey Lutze	Pillar cell flash memory technology	US	Issued	11277907	29-Mar-06	7253055	7-Aug-07
SDK0302.000US	George Samachisa, Jack H. Yuan, Eliyahou Harari, Daniel Guterman	Multi-State Non-Volatile Integrated Circuit Memory Systems That Employ Dielectric Storage Elements	US	Issued	10002696	31-Oct-01	6897522	24-May-05
SDK0302.002US	George Samachisa, Jack H. Yuan, Eliyahou Harari, Daniel Guterman	Multi-State Non-Volatile Integrated Circuit Memory Systems That Employ Dielectric Storage Elements	US	Issued	10280352	25-Oct-02	6925007	2-Aug-05
SDK0302.003US	Eliyahou Harari, George Samachisa, Jack H. Yuan, Daniel Guterman	Multi-State Non-Volatile Integrated Circuit Memory Systems That Employ Dielectric Storage Elements	US	Issued	11075423	7-Mar-05	7342279	11-Mar-08
SDK0302.004US	Eliyahou Harari, George Samachisa, Jack H. Yuan, Daniel Guterman	Multi-State Non-Volatile Integrated Circuit Memory Systems That Employ Dielectric Storage Elements	US	Issued	11075427	7-Mar-05	7341918	11-Mar-08
SDK0302.005US	Eliyahou Harari, George Samachisa, Jack H. Yuan, Daniel Guterman	Multi-State Non-Volatile Integrated Circuit Memory Systems That Employ Dielectric Storage Elements	US	Issued	12020266	25-Jan-08	7479677	20-Jan-09
SDK0302.006US	Eliyahou Harari, George Samachisa, Jack H. Yuan, Daniel Guterman	Multi-State Non-Volatile Integrated Circuit Memory Systems That Employ Dielectric Storage Elements	US	Issued	12020296	25-Jan-08	7579247	25-Aug-09
SDK0302.007US	Eliyahou Harari, George Samachisa, Jack H. Yuan, Daniel Guterman	Multi-State Non-Volatile Integrated Circuit Memory Systems That Employ Dielectric Storage Elements	US	Issued	12510077	27-Jul-09	7834392	16-Nov-10
SDK0303.000US	Jian Chen, Long Pham, Alexander Mak	Techniques Of Recovering Data From Memory Cells Affected By Field Coupling With Adjacent Memory Cells	US	Issued	10052759	18-Jan-02	6542407	1-Apr-03
SDK0303.001US	Jian Chen, Long Pham, Alexander Mak	Techniques of Recovering Data From Memory Cells Affected by Field Coupling with Adjacent Memory Cells	US	Issued	10357840	3-Feb-03	6847553	25-Jan-05
SDK0303.002US	Jian Chen, Long Pham, Alexander Mak	Techniques of recovering data from memory cells affected by field coupling with adjacent memory cells	US	Issued	11028906	3-Jan-05	7102924	5-Sep-06
SDK0304.000US	Reuven Elhamias, Andrew Tomlin, Wesley G. Brewer, Yosi Pinto, Michael Holtzman	Optimized Non-Volatile Storage Systems	US	Issued	10901849	28-Jul-04	7427027	23-Sep-08

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0304.001US	Reuven Elhamias, Andrew Tomlin, Wesley G. Brewer, Yosi Pinto, Michael Holtzman	Optimized Non-Volatile Storage Systems	US	Allowed	12166533	2-Jul-08		
SDK0304.002USx	Reuven Elhamias, Andrew Tomlin, Wesley G. Brewer, Yosi Pinto, Michael Holtzman	Optimized Non-Volatile Storage Systems	US	Pending	13047631	14-Mar-11		
SDK0307.000US	Raul-Adrian Cernea	Area Efficient Charge Pump	US	Issued	10636839	7-Aug-03	6922096	26-Jul-05
SDK0307.001US	Raul-Adrian Cernea	Area efficient charge pump	US	Issued	11158557	21-Jun-05	7113023	26-Sep-06
SDK0308.000US	Raul-Adrian Cernea, Yan Li, Jian Chen	Operating Techniques For Reducing Program And Read Disturbs Of A Non-Volatile Memory	US	Issued	10086495	27-Feb-02	6771536	3-Aug-04
SDK0308.001US	Raul-Adrian Cernea, Yan Li, Jian Chen	Operating Techniques for Reducing Program and Read Disturbs of a Non-Volatile Memory	US	Issued	10868728	14-Jun-04	6996003	7-Feb-06
SDK0308.002US	Yan Li, Jian Chen, Raul-Adrian Cernea	Operating Techniques for Reducing Program and Read Disturbs of a Non-Volatile Memory	US	Issued	11298104	9-Dec-05	7298647	20-Nov-07
SDK0308.003US	Yan Li, Jian Chen, Raul-Adrian Cernea	Operating Techniques for Reducing Program and Read Disturbs of a Non-Volatile Memory	US	Issued	11923126	24-Oct-07	7554848	30-Jun-09
SDK0309.000US	Raul-Adrian Cernea, Yan Li	Non-Volatile Memory And Method With Reduced Source Line Bias Errors	US	Issued	10254830	24-Sep-02	7196931	27-Mar-07
SDK0309.001US	Raul-Adrian Cernea, Yan Li	Non-Volatile Memory and Method With Improved Sensing	US	Issued	10665828	17-Sep-03	7023736	4-Apr-06
SDK0309.002US	Raul-Adrian Cernea, Yan Li	Non-Volatile Memory and Method With Improved Sensing	US	Issued	10678918	2-Oct-03	7212445	1-May-07
SDK0309.003US	Raul-Adrian Cernea	Memory Sensing Circuit and Method for Low Voltage Operation	US	Issued	11015199	16-Dec-04	7046568	16-May-06
SDK0309.004US	Raul-Adrian Cernea	Memory Sensing Circuit and Method for Low Voltage Operation	US	Issued	11383247	15-May-06	7376030	20-May-08
SDK0309.007US	Raul-Adrian Cernea, Yan Li	Non-Volatile Memory And Method with Reduced Source Line Bias Errors	US	Issued	11620946	8-Jan-07	7551484	23-Jun-09
SDK0309.008US	Raul-Adrian Cernea, Yan Li	Non-Volatile Memory and Method With Improved Sensing	US	Issued	11621750	10-Jan-07	7428171	23-Sep-08
SDK0310.000US	Raul-Adrian Cernea, Yan Li	Flexible And Area Efficient Column Redundancy For Non-Volatile Memories	US	Issued	10751097	31-Dec-03	7170802	30-Jan-07
SDK0310.001US	Raul-Adrian Cernea, Yan Li	Flexible And Area Efficient Column Redundancy For Non-Volatile Memories	US	Issued	11619524	3-Jan-07	7405985	29-Jul-08
SDK0311.000US	Raul-Adrian Cernea, Khandker N Quader, Yan Li, Jian Chen, Yupin Fong	Techniques For Reducing Effects Of Coupling Between Storage Elements Of Adjacent Rows Of Memory Cells	US	Issued	10237426	6-Sep-02	6781877	24-Aug-04
SDK0311.001US	Raul-Adrian Cernea, Khandker N Quader, Yan Li, Jian Chen, Yupin Fong	Techniques for Reducing Effects of Coupling Between Storage Elements of Adjacent Rows of Memory Cells	US	Issued	10923320	20-Aug-04	6870768	22-Mar-05
SDK0311.002US	Raul-Adrian Cernea, Khandker N Quader, Yan Li, Jian Chen, Yupin Fong	Technique for Reducing Effects of Coupling Between Storage Elements of Adjacent Rows of Memory Cells	US	Issued	11055776	9-Feb-05	7046548	16-May-06
SDK0314.000US	Robert C Chang, Ping Li, Farshid Sabet-Sharghi	Implementation of In System Programming to Update Firmware on Memory Cards	US	Issued	10005740	6-Nov-01	7320126	15-Jan-08
SDK0314.001US	Robert C Chang, Ping Li, Farshid Sabet-Sharghi	Implementation of In System Programming to Update Firmware on Memory Cards	US	Published	11758449	5-Jun-07		
SDK0316.000US	Robert C Chang, Ping Li, Farshid Sabet-Sharghi	Implementation of Storing Secret Information in Data Storage Reader Products	US	Issued	10092049	4-Mar-02	7552345	23-Jun-09

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0317.000US	Khandker N Quader, Feng Pan, Khanh T Nguyen, Long Pham, Alexander Mak	Method And System For Programming And Inhibiting Multi-Level, Non-Volatile Memory Cells	US	Issued	10025749	18-Dec-01	6967872	22-Nov-05
SDK0317.001US	Khandker N Quader, Feng Pan, Khanh T Nguyen, Long Pham, Alexander Mak	Method And System For Programming And Inhibiting Multi-Level, Non-Volatile Memory Cells	US	Issued	10809571	24-Mar-04	6944068	13-Sep-05
SDK0317.002US	Khandker N Quader, Khanh T Nguyen, Feng Pan, Long Pham, Alexander Mak	Method and system for programming and inhibiting multi-level non-volatile memory cells	US	Issued	11223709	9-Sep-05	7095654	22-Aug-06
SDK0318.000US	Robert Wallace	Improved Memory Package With A Controller On One Side Of A Printed Circuited Board And Memory On Another Side Of The Circuit Board	US	Issued	10112968	28-Mar-02	6639309	28-Oct-03
SDK0318.001US	Robert Wallace	Memory Package	US	Issued	10649312	26-Aug-03	6797538	28-Sep-04
SDK0318.002US	Robert Wallace	Memory Package	US	Issued	10921549	19-Aug-04	7064003	20-Jun-06
SDK0318.003US	Robert Wallace	Memory Package	US	Issued	11339866	25-Jan-06	7429781	30-Sep-08
SDK0321.000US	Carlos J. Gonzalez, Mark Sompel, Kevin Conley	Adaptive Mode Switching of Flash Memory Address Mapping Based on Host Usage Characteristics	US	Issued	10750190	30-Dec-03	7631138	8-Dec-09
SDK0321.001US	Carlos J. Gonzalez, Mark Sompel, Kevin Conley	Adaptive Mode Switching of Flash Memory Address Mapping Based on Host Usage Characteristics	US	Published	12609789	30-Oct-09		
SDK0332.000US	Feng Pan, Tat-Kwan Edgar Yu	Implementation of an Inhibit During Soft Programming to Tighten an Erase Voltage Distribution	US	Issued	10068245	6-Feb-02	6661711	9-Dec-03
SDK0333.000US	Yongliang Bill Wang, Raul-Adrian Cernea, Chi-Ming Wang	Voltage Generation Circuitry having Temperature Compensation	US	Issued	10263016	1-Oct-02	6801454	5-Oct-04
SDK0334.001US	Jack H. Yuan	Flash Memory Array With Increased Coupling Between Floating And Control Gates	US	Issued	11132522	18-May-05	7170131	30-Jan-07
SDK0334.002US	Jack H. Yuan	Flash Memory Array With Increased Coupling Between Floating And Control Gates	US	Issued	11668306	29-Jan-07	7517756	14-Apr-09
SDK0335.000US	Robert Wallace	Method and apparatus for maintaining a separation between contacts	US	Issued	10142213	8-May-02	7138583	21-Nov-06
SDK0339.000US	Eliyahou Harari, Bo Eric Ericsson, Robert Wallace	Universal Non-Volatile Memory Card Used With Various Different Standard Cards Containing A Memory Controller	US	Issued	10293985	13-Nov-02	7367503	6-May-08
SDK0339.001US	Eliyahou Harari, Bo Eric Ericsson, Robert Wallace	Universal non-volatile memory card used with various different standard cards containing a memory controller	US	Issued	11075438	7-Mar-05	7114659	3-Oct-06
SDK0339.002US	Eliyahou Harari, Bo Eric Ericsson, Robert Wallace	Universal Non-Volatile Memory Card Used With Various Different Standard Cards Containing A Memory Controller	US	Published	12115408	5-May-08		
SDK0340.000US	George Samachisa	Substrate Electron Injection Techniques for Programming Non-Volatile Charge Storage Memory Cells	US	Issued	11021193	23-Dec-04	6980471	27-Dec-05
SDK0340.001US	George Samachisa	Substrate Electron Injection Techniques for Programming Non-Volatile Charge Storage Memory Cells	US	Issued	11198261	4-Aug-05	7230847	12-Jun-07

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0340.002US	George Samachisa	Substrate Electron Injection Techniques for Programming Non-Volatile Charge Storage Memory Cells and for Controlling Program Disturb	US	Issued	11750469	18-May-07	7443736	28-Oct-08
SDK0341.000US	Robert Wallace, Robert C Miller	Memory Card	US	Issued	29172279	9-Dec-02	D492688	6-Jul-04
SDK0345.000US	Jeffrey Lutze, Chan-Sui Pang	Flash Memory Programming Using Gate Induced Junction Leakage Current	US	Issued	10703717	7-Nov-03	7057931	6-Jun-06
SDK0346.000US	Loc Tu, Jian Chen	Error Recovery for Non-Volatile Memory	US	Issued	10318621	12-Dec-02	6829167	7-Dec-04
SDK0346.001US	Loc Tu, Jian Chen	Error Recovery For Nonvolatile Memory	US	Issued	11003545	3-Dec-04	7099194	29-Aug-06
SDK0347.000US	Carlos J. Gonzalez, Andrew Tomlin	Flash Memory System Startup Operation	US	Issued	10751033	31-Dec-03	7594135	22-Sep-09
SDK0347.001US	Carlos J. Gonzalez, Andrew Tomlin	Flash Memory System Startup Operation	US	Allowed	12484350	15-Jun-09		
SDK0348.001US	Shahzad Khalid	Voltage Buffer For Capacitive Loads	US	Issued	11225312	12-Sep-05	7167041	23-Jan-07
SDK0348.002US	Shahzad Khalid	Voltage Buffer For Capacitive Loads	US	Issued	11620001	4-Jan-07	7471139	30-Dec-08
SDK0349.000US	Carlos J. Gonzalez, Alan D Bryce, Sergey Anatolievich Gorobets, Alan Bennett	Adaptive Deterministic Groupings of Blocks into Multi-Block Units	US	Published	10750157	30-Dec-03		
SDK0349.001US	Carlos J. Gonzalez, Alan D Bryce, Sergey Anatolievich Gorobets, Alan Bennett	Adaptive Deterministic Groupings of Blocks into Multi-Block Units	US	Pending	12512282	30-Jul-09		
SDK0352.000US	Jeffrey A Salazar, Robert Howard, Jonathan R Harris, Daren W Hebold	Memory Card Adapter	US	Issued	29159756	26-Apr-02	D470499	18-Feb-03
SDK0355.000US	Kevin Conley	ZONE BOUNDARY ADJUSTMENT FOR DEFECTS IN NON-VOLATILE MEMORIES	US	Issued	10315451	9-Dec-02	6901498	31-May-05
SDK0355.001US	Kevin Conley	ZONE BOUNDARY ADJUSTMENT FOR DEFECTS IN NON-VOLATILE MEMORIES	US	Issued	11114996	26-Apr-05	7149871	12-Dec-06
SDK0355.002US	Kevin Conley	ZONE BOUNDARY ADJUSTMENT FOR DEFECTS IN NON-VOLATILE MEMORIES	US	Pending	11552227	24-Oct-06		
SDK0356.000US	Daniel Guterman, Stephen J. Gross, Shahzad Khalid, Geoffrey S Gongwer	TRACKING CELLS FOR A MEMORY SYSTEM	US	Issued	10461244	13-Jun-03	7237074	26-Jun-07
SDK0356.001US	Daniel Guterman, Stephen J. Gross, Shahzad Khalid, Geoffrey S Gongwer	TRACKING CELLS FOR A MEMORY SYSTEM	US	Issued	11752008	22-May-07	7760555	20-Jul-10
SDK0356.002US	Daniel Guterman, Stephen J. Gross, Shahzad Khalid, Geoffrey S Gongwer	Data Recovery in a Memory System Using Tracking Cells	US	Issued	11752024	22-May-07	7681094	16-Mar-10
SDK0356.003US	Daniel Guterman, Stephen J. Gross, Shahzad Khalid, Geoffrey S Gongwer	TRACKING CELLS FOR A MEMORY SYSTEM	US	Issued	12763569	20-Apr-10	7916552	29-Mar-11
SDK0356.004US	Daniel Guterman, Stephen J. Gross, Shahzad Khalid, Geoffrey S Gongwer	TRACKING CELLS FOR A MEMORY SYSTEM	US	Pending	13031041	18-Feb-11		
SDK0357.000US	Carlos J. Gonzalez, Kevin Conley	Flash Memory Data Correction And Scrub Techniques	US	Issued	10678345	3-Oct-03	7012835	14-Mar-06
SDK0357.001US	Carlos J. Gonzalez, Kevin Conley	Flash Memory Data Correction And Scrub Techniques	US	Issued	11271553	10-Nov-05	7224607	29-May-07
SDK0357.002US	Carlos J. Gonzalez, Kevin Conley	Flash Memory Data Correction And Scrub Techniques	US	Issued	11748077	14-May-07	7518919	14-Apr-09
SDK0357.003US	Carlos J. Gonzalez, Kevin Conley	Flash Memory Data Correction And Scrub Techniques	US	Allowed	12415158	31-Mar-09		
SDK0357.004US	Carlos J. Gonzalez, Kevin Conley	Flash Memory Data Correction And Scrub Techniques	US	Published	12945000	12-Nov-10		
SDK0358.000US	Geoffrey S Gongwer, Daniel Guterman, Yupin Fong	Smart Verify for Multi-State Memories	US	Issued	10314055	5-Dec-02	7073103	4-Jul-06



FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0358.001US	Geoffrey S Gongwer, Daniel Guterman, Yubin Fong	Smart Verify For Multi-State Memories	US	Issued	11304961	14-Dec-05	7243275	10-Jul-07
SDK0358.002US	Geoffrey S Gongwer, Daniel Guterman, Yubin Fong	Smart Verify For Multi-State Memories	US	Issued	11759872	7-Jun-07	7584391	1-Sep-09
SDK0361.000US	George Matamis, Tuan D. Pham, Henry Chien, Hao Fang	Bitline Direction Shielding To Avoid Cross Coupling Between Adjacent Cells For NAND Flash Memory	US	Issued	10680665	6-Oct-03	7221008	22-May-07
SDK0363.000US	Nima Mokhlesi	Array of non-volatile memory cells with floating gates formed of spacers in substrate trenches	US	Issued	11533313	19-Sep-06	7646054	12-Jan-10
SDK0363.001US	Nima Mokhlesi	Method of Making An Array Of Non-volatile Memory Cells With Floating Gates Formed of Spacers In Substrate Trenches	US	Issued	11533317	19-Sep-06	7696044	13-Apr-10
SDK0365.000US	Stephen J. Gross, Bryan Mee	Method Of Storing Downloadable Firmware On Bulk Media	US	Issued	11319254	27-Dec-05	7546515	9-Jun-09
SDK0365.001US	Stephen J. Gross, Bryan Mee	Storing Downloadable Firmware on Bulk Media	US	Issued	11319888	27-Dec-05	7536627	18-May-09
SDK0366.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Wear-Leveling in Non-Volatile Storage Systems	US	Issued	10281739	28-Oct-02	6985992	10-Jan-06
SDK0366.001US	Robert Wallace	Low-Cost Write Protect Tab for a Non-Volatile Memory Device	US	Issued	10141710	9-May-02	6739515	25-May-04
SDK0367.000US	Henry R Hutton, Farshid Sabet-Sharghi, Robert C.Chang, Jong Guo	Booting from Non-Linear Memory	US	Issued	10263576	2-Oct-02	7082525	25-Jul-06
SDK0367.001US	Henry R Hutton, Farshid Sabet-Sharghi, Robert C.Chang, Jong Guo	Booting From Non-Linear Memory	US	Issued	11420202	24-May-06	7310726	18-Dec-07
SDK0369.000US	Hem Takiar	Stackable Integrated Circuit Package and Method Therefor	US	Issued	10463051	16-Jun-03	6984881	10-Jan-06
SDK0370.000US	Hem Takiar	Method For Efficiently Producing Removable Peripheral Cards	US	Issued	10602373	23-Jun-03	7094633	22-Aug-06
SDK0370.001US	Hem Takiar	Method For Efficiently Producing Removable Peripheral Cards	US	Published	11499039	4-Aug-06		
SDK0371.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Method and Apparatus for Splitting a Logical Block	US	Issued	10281631	28-Oct-02	7039788	2-May-06
SDK0378.000US	Daniel Guterman, Yoram Cedar, Charles Michael Schroter, Milton Lourenco Barrocas, Carlos J. Gonzalez, Kevin Conley	Memory System for Legacy Hosts	US	Issued	11286100	22-Nov-05	7739472	15-Jun-10
SDK0378.001US	Daniel Guterman, Yoram Cedar, Charles Michael Schroter, Milton Lourenco Barrocas, Carlos J. Gonzalez, Kevin Conley	Method for Adapting a Memory System to Operate With a Legacy Host Originally Designed to Operate With a Different Memory System	US	Issued	11285992	22-Nov-05	7747927	29-Jun-10
SDK0388.000US	Simon Stoler, Michael Holtzman, Yosi Pinto, Reuven Elhamias, Meiri Azari	Non-Volatile Memory System With Self Test Capability	US	Issued	10888294	9-Jul-04	7814377	12-Oct-10
SDK0388.001US	Simon Stoler, Michael Holtzman, Yosi Pinto, Meiri Azari, Reuven Elhamias	Non-Volatile Memory System With Self Test Capability	US	Published	12901267	8-Oct-10		
SDK0391.000US	Aviad Zer, Yosi Pinto, Michael Holtzman, Yoram Cedar	Combination Non-Volatile Memory And Input-Output Card With Direct Memory Access	US	Published	10302009	21-Nov-02		
SDK0410.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Method and Apparatus for Grouping Pages with a Block	US	Issued	10281855	28-Oct-02	7254668	7-Aug-07
SDK0411.000US	Jian Chen, Masaaki Higashitani	Use of Voids Between Elements in Semiconductor Structures for Isolation	US	Issued	10443502	21-May-03	7045849	16-May-06

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0411.001US	Jian Chen, Masaaki Higashitani	Use of Voids Between Elements in Semiconductor Structures for Isolation	US	Issued	11210218	22-Aug-05	7569465	4-Aug-09
SDK0413.000US	Raul-Adrian Cernea	Highly Compact Non-Volatile Memory and Method Therefor with Space Efficient	US	Issued	10254221	24-Sep-02	6940753	6-Sep-05
SDK0414.000US	Raul-Adrian Cernea, Yan Li	Non-Volatile Memory And Method With Reduced Bit Line Crosstalk Errors	US	Issued	10254898	24-Sep-02	7443757	28-Oct-08
SDK0415.000US	Raul-Adrian Cernea, Yan Li	Non-Volatile Memory And Method With Reduced Neighboring Field Errors	US	Issued	10254290	24-Sep-02	6987693	17-Jan-06
SDK0415.001US	Raul-Adrian Cernea, Yan Li	Non-Volatile Memory And Method With Reduced Neighboring Field Errors	US	Issued	11235985	26-Sep-05	7239551	3-Jul-07
SDK0415.003US	Raul-Adrian Cernea, Yan Li	Non-Volatile Memory And Method With Reduced Neighboring Field Errors	US	Published	12749292	29-Mar-10		
SDK0416.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Method and Apparatus for Resolving Physical Blocks Associated with a Common Logical Block	US	Issued	10281762	28-Oct-02	7234036	19-Jun-07
SDK0417.000US	Yosi Pinto, Aviad Zer, Amir Tsur, Asher Druck	Efficient Connection Between Modules Of Removable Electronic Circuit Cards	US	Issued	10732149	9-Dec-03	7209995	24-Apr-07
SDK0417.001US	Yosi Pinto, Aviad Zer, Amir Tsur, Asher Druck	Efficient Connection Between Modules Of Removable Electronic Circuit Cards	US	Issued	11303220	15-Dec-05	7467249	16-Dec-08
SDK0420.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Maintaining Erase Counts in Non-Volatile Storage Systems	US	Issued	10281696	28-Oct-02	6831865	14-Dec-04
SDK0422.000US	Feng Pan	High Voltage Ripple Reduction	US	Issued	10328686	23-Dec-02	6734718	11-May-04
SDK0422.001US	Feng Pan	High voltage ripple reduction and substrate protection	US	Issued	10811074	26-Mar-04	7116155	3-Oct-06
SDK0428.000US	Eliyahou Harari, George Samachisa	Memory cell arrays having dual control gates per memory cell charge storage element	US	Allowed	11279725	13-Apr-06		
SDK0431.000US	Khandker N Quader, Raul-Adrian Cernea	Erase Inhibit In Non-Volatile Memories	US	Issued	10671847	25-Sep-03	6958936	25-Oct-05
SDK0431.001US	Khandker N Quader, Raul-Adrian Cernea	Erase Inhibit In Non-Volatile Memories	US	Issued	11223055	8-Sep-05	7379346	27-May-08
SDK0432.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi, Ping Li	Method and Apparatus for Performing Block Caching in a Non-Volatile Memory System	US	Issued	10678672	2-Oct-03	7174440	6-Feb-07
SDK0434.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Hybrid Mapping Implementation with a Non-Volatile Memory System	US	Issued	10676652	1-Oct-03	7188228	6-Mar-07
SDK0434.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Hybrid Mapping Implementation with a Non-Volatile Memory System	US	Issued	11617441	28-Dec-06	7702880	20-Apr-10
SDK0435.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Method and Apparatus for Efficiently Enabling and Out of Sequence Write Process within a Non-Volatile Memory System	US	Issued	10679008	3-Oct-03	7526599	28-Apr-09
SDK0436.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Unusable Block Management within a Non-Volatile Memory System	US	Issued	10682641	9-Oct-03	7171536	30-Jan-07
SDK0437.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi, Sergey Yudin	Power Management Block for Use in a Non-Volatile Memory System	US	Issued	10685624	14-Oct-03	7181611	20-Feb-07
SDK0437.002US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi, Sergey Yudin	Power Management Block for Use in a Non-Volatile Memory System	US	Issued	11616687	27-Dec-06	7809962	5-Oct-10
SDK0438.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Method and Apparatus for Managing the Integrity of Data in Non-Volatile Memory	US	Pending	10679000	2-Oct-03		
SDK0439.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Hybrid Implementation for Error Correction Codes with a Non-Volatile Memory System	US	Pending	10678893	2-Oct-03		

PATENT

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0442.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Dynamic Redundant Area Configuration in a Non-Volatile Memory System	US	Issued	10676407	1-Oct-03	7559004	7-Jul-09
SDK0443.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Adaptive Internal Table Back-up for Non-Volatile Memory System	US	Pending	10696685	28-Oct-03		
SDK0444.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Erase Count Differential Table within a Non-Volatile Memory System	US	Issued	10696228	28-Oct-03	7032087	18-Apr-06
SDK0445.000US	Robert C Chang, Farshid Sabet-Sharghi, Bahman Qawami	Internal Maintenance Schedule Request for Non-Volatile Memory System	US	Issued	10696229	28-Oct-03	7089349	8-Aug-06
SDK0446.000US	Soochok Kee	Integrated Circuit Package having Stacked Integrated Circuits and Method Therefor	US	Issued	10463742	16-Jun-03	7309923	18-Dec-07
SDK0449.000US	Jeffrey Lutze, Tuan D. Pham, Henry Chien, George Matamis	Self-aligned non-volatile memory cell and process for fabrication	US	Issued	10600259	30-Jun-03	7105406	12-Sep-06
SDK0449.001US	Jeffrey Lutze, Tuan D. Pham, Henry Chien, George Matamis	Self-aligned non-volatile memory cell	US	Issued	11469727	1-Sep-06	7504686	17-Mar-09
SDK0450.000US	Ron Barzilai, Reuven Elhamias	Multi-Purpose Non-Volatile Memory Card	US	Issued	10886302	6-Jul-04	7554842	30-Jun-09
SDK0450.001US	Ron Barzilai, Reuven Elhamias	Multi-Purpose Non-Volatile Memory Card	US	Pending	11821564	21-Jun-07		
SDK0453.000US	Eliyahou Harari	Flash Memory Cell Arrays Having Dual Control Gates Per Memory Cell Charge Storage Element	US	Issued	10282747	28-Oct-02	6888755	3-May-05
SDK0453.001US	Eliyahou Harari	Flash Memory Cell Arrays Having Dual Control Gates	US	Issued	10791486	1-Mar-04	7075823	11-Jul-06
SDK0453.002US	Eliyahou Harari	Flash memory cell arrays having dual control gates per memory cell charge storage element	US	Issued	11379004	17-Apr-06	7502261	10-Mar-09
SDK0453.003US	Eliyahou Harari	Flash memory cell arrays having dual control gates per memory cell charge storage element	US	Issued	11379012	17-Apr-06	7303956	4-Dec-07
SDK0453.004US	Eliyahou Harari	Flash memory cell arrays having dual control gates per memory cell charge storage element	US	Issued	11379019	17-Apr-06	7486555	3-Feb-09
SDK0453.005US	Eliyahou Harari	Flash Memory Cell Arrays Having Dual Control Gates Per Memory Cell Charge Storage Element	US	Issued	11422016	2-Jun-06	7638834	29-Dec-09
SDK0453.006US	Eliyahou Harari	Flash Memory Cell Arrays Having Dual Control Gates Per Memory Cell Charge Storage Element	US	Published	12607444	28-Oct-09		
SDK0454.000US	Kevin Conley, Yoram Cedar	System and Method For Use of On-Chip Non-Volatile Memory Write Cache	US	Issued	11021200	21-Dec-04	7882299	1-Feb-11
SDK0455.000US	Sergey Anatolievich Gorobets, Alan Bennett, Peter John Smith, Alan Welsh Sinclair, Kevin Conley, Philip D. Rowall, Rauli-Adrian Cernea	Cyclic Flash Memory Wear Leveling	US	Issued	10990189	15-Nov-04	7441067	21-Oct-08
SDK0456.000US	Rauli-Adrian Cernea	Highly Compact Non-Volatile Memory And Method Therefor With Internal Serial Buses	US	Issued	10254919	24-Sep-02	6891753	10-May-05
SDK0456.001US	Rauli-Adrian Cernea	Highly Compact Non-Volatile Memory and Method Therefor with Internal Serial Buses	US	Issued	11122738	5-May-05	7085159	1-Aug-06
SDK0456.002US	Rauli-Adrian Cernea	Highly Compact Non-Volatile Memory And Method Therefor With Internal Serial Buses	US	Issued	11422719	7-Jun-06	7447070	4-Nov-08
SDK0457.000US	Jeffrey Lutze, Jian Chen, Yan Li, Masaaki Higashitani	SOURCE SIDE SELF BOOSTING TECHNIQUE FOR NON-VOLATILE MEMORY	US	Issued	10379608	5-Mar-03	6859397	22-Feb-05
SDK0457.001US	Jeffrey Lutze, Jian Chen, Yan Li, Masaaki Higashitani	SELF BOOSTING TECHNIQUE	US	Issued	11049802	3-Feb-05	6975537	13-Dec-05
SDK0458.000US	Yan Li, Mehrdad Mofidi, Shahzad Khalid, Rauli-Adrian Cernea	Non-Volatile Memory and Method with Bit Line to Bit Line Coupled Compensation	US	Issued	10667222	17-Sep-03	7064980	20-Jun-06
SDK0458.001US	Rauli-Adrian Cernea, Mehrdad Mofidi, Yan Li, Shahzad Khalid	Non-Volatile Memory And Method With Bit Line To Bit Line Coupled Compensation	US	Issued	11422034	2-Jun-06	7269069	11-Sep-07

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0458.002US	Raul-Adrian Cernea, Yan Li, Mehrdad Mofidi, Shahzad Khalid	Non-Volatile Memory And Method With Bit Line To Bit Line Coupled Compensation	US	Issued	11848385	31-Aug-07	7532514	12-May-09
SDK0461.000US	Sergey Anatolievich Gorobets	Methods for Adaptively Handling Data Writes in Non-Volatile Memories	US	Issued	11261138	27-Oct-05	7509471	24-Mar-09
SDK0461.001US	Sergey Anatolievich Gorobets	Non-Volatile Memory With Adaptive Handling of Data Writes	US	Issued	11261150	27-Oct-05	7631162	8-Dec-09
SDK0462.000US	Jian Chen, Jeffrey Lutze, Yan Li	DETECTING OVER PROGRAMMED MEMORY	US	Issued	10629068	29-Jul-03	6917542	12-Jul-05
SDK0462.001US	Jian Chen, Yan Li, Jeffrey Lutze	DETECTING OVER PROGRAMMED MEMORY AFTER FURTHER PROGRAMMING	US	Issued	10628962	29-Jul-03	6914823	5-Jul-05
SDK0462.002US	Jian Chen, Jeffrey Lutze, Yan Li	DETECTING OVER PROGRAMMED MEMORY	US	Issued	11124709	9-May-05	7215575	8-May-07
SDK0466.000US	Kevin Conley, Robert C Chang, Jason Lin	Flash Storage System With Write/Erase Abort Detection Mechanism	US	Issued	10751096	31-Dec-03	7299314	20-Nov-07
SDK0466.001US	Jason Lin, Kevin Conley, Robert C Chang	Flash Storage System With Write/Erase Abort Detection Mechanism	US	Issued	11936440	7-Nov-07	7669004	23-Feb-10
SDK0467.000US	Alan Welsh Sinclair	Dual Media Storage Device	US	Issued	10772855	4-Feb-04	7136973	14-Nov-06
SDK0467.001US	Alan Welsh Sinclair	Dual Media Storage Device	US	Issued	11537404	29-Sep-06	7302534	27-Nov-07
SDK0470.000US	Kevin Conley, Sergey Anatolievich Gorobets	Pipelined Data Relocation And Improved Chip Architectures	US	Issued	10846289	13-May-04	7490283	10-Feb-09
SDK0470.001US	Sergey Anatolievich Gorobets, Kevin Conley	Pipelined Data Relocation And Improved Chip Architectures	US	Pending	12353185	13-Jan-09		
SDK0471.000US	Daniel Guterman, Nima Mokhlesi, Yupin Fong	Efficient Verification for Course/Fine Programming of Non-Volatile Memory	US	Issued	10766217	27-Jan-04	7139198	21-Nov-06
SDK0471.001US	Nima Mokhlesi, Daniel Guterman, Yupin Fong	Efficient Verification for Course/Fine Programming of Non-Volatile Memory	US	Issued	10766786	27-Jan-04	7002843	21-Feb-06
SDK0471.002US	Nima Mokhlesi, Daniel Guterman, Yupin Fong	CHARGE PACKET METERING WITH COURSE/FINE PROGRAMMING OF NON-VOLATILE MEMORY	US	Issued	10766116	27-Jan-04	7068539	27-Jun-06
SDK0471.003US	Daniel Guterman, Nima Mokhlesi, Yupin Fong	VARIABLE CURRENT SINKING FOR COARSE/FINE PROGRAMMING OF NON-VOLATILE MEMORY	US	Issued	11280716	16-Nov-05	7414887	19-Aug-08
SDK0471.004US	Daniel Guterman, Nima Mokhlesi, Yupin Fong	CHARGE PACKET METERING WITH COURSE/FINE PROGRAMMING OF NON-VOLATILE MEMORY	US	Issued	11429770	8-May-06	7447075	4-Nov-08
SDK0471.005US	Nima Mokhlesi, Daniel Guterman, Yupin Fong	CHARGE PACKET METERING WITH COURSE/FINE PROGRAMMING OF NON-VOLATILE MEMORY	US	Issued	11429769	8-May-06	7453730	18-Nov-08
SDK0471.006US	Daniel Guterman, Nima Mokhlesi, Yupin Fong	Efficient Verification for Coarse Fine Programming of Non-Volatile Memory	US	Issued	11550502	18-Oct-06	7317638	8-Jan-08
SDK0471.007US	Daniel Guterman, Nima Mokhlesi, Yupin Fong	Efficient Verification for Coarse Fine Programming of Non-Volatile Memory	US	Issued	11550499	18-Oct-06	7518928	14-Apr-09
SDK0477.000US	Alan Welsh Sinclair, Peter John Smith	Method and Apparatus for Maintaining Data on Non-Volatile Memory Systems	US	Issued	10897049	21-Jul-04	7395384	1-Jul-08
SDK0478.001US	Hem Takiar	MEMORY CARD WITH RAISED PORTION	US	Issued	10782969	20-Feb-04	7307848	11-Dec-07
SDK0478.002US	Hem Takiar, Robert C Miller	MEMORY CARD WITH AND WITHOUT ENCLOSURES	US	Issued	10851466	20-May-04	7416132	26-Aug-08
SDK0478.003US	Hem Takiar, Michael Patterson	MEMORY CARD WITH CHAMFER	US	Issued	10995989	23-Nov-04	7306161	11-Dec-07
SDK0478.004US	Hem Takiar, Robert C Miller	MEMORY CARD WITH PUSH-PUSH CONNECTOR	US	Issued	10952591	28-Sep-04	7336498	26-Feb-08
SDK0478.005US	Hem Takiar, Robert C Miller	MEMORY CARD WITH ADAPTER	US	Issued	10952609	28-Sep-04	7306160	11-Dec-07
SDK0478.006US	Hem Takiar	Peripheral Card with Hidden Test Pins	US	Issued	11930909	31-Oct-07	7864540	4-Jan-11
SDK0481.000US	Andrew Tomlin	Method For Copying Data in Reprogrammable Non-Volatile Memory	US	Issued	11022350	21-Dec-04	7849381	7-Dec-10

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0481.001US	Andrew Tomlin	Method For Copying Data in Reprogrammable Non-Volatile Memory	US	Pending	12941294	8-Nov-10		
SDK0484.000US	Robert Howard, Jeffrey A Salazar, Jonathan R Harris, Daren W Hebold	Retractable Card Adapter	US	Issued	10133791	26-Apr-02	7092256	15-Aug-06
SDK0484.001US	Jeffrey A Salazar, Robert Howard, Jonathan R Harris, Daren W Hebold	Retractable Card Adapter	US	Issued	11422313	5-Jun-06	7492601	17-Feb-09
SDK0484.002US	Jeffrey A Salazar, Robert Howard, Jonathan R Harris, Daren W Hebold	Retractable Card Adapter	US	Issued	12371859	16-Feb-09	7787243	31-Aug-10
SDK0485.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Maintaining an Average Erase Count in a Non-Volatile Storage System	US	Issued	10281823	28-Oct-02	7035967	25-Apr-06
SDK0485.001US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Maintaining an Average Erase Count in a Non-Volatile Storage System	US	Issued	11362666	25-Feb-06	7287118	23-Oct-07
SDK0486.000US	Robert C Chang, Bahman Qawami, Farshid Sabet-Sharghi	Tracking the Most Frequently Erased Blocks in Non-Volatile Memory Systems	US	Issued	10281670	28-Oct-02	6973531	6-Dec-05
SDK0487.000US	Robert C Chang, Farshid Sabet-Sharghi, Bahman Qawami	Tracking the Least Frequently Erased Blocks in Non-Volatile Memory Systems	US	Issued	10281824	28-Oct-02	7096313	22-Aug-06
SDK0488.000US	Robert C Chang, Farshid Sabet-Sharghi, Bahman Qawami	Method and Apparatus for Managing an Erase Block Count	US	Issued	10281626	28-Oct-02	7103732	5-Sep-06
SDK0489.000US	Robert Wallace, Robert C Miller	Electronic Memory Card	US	Issued	29124803	12-Jun-00	D452864	8-Jan-02
SDK0490.000US	Wesley G. Brewer, Robert Wallace, Robert C Miller	Electronic Memory Card Holder	US	Issued	29132354	6-Nov-00	D453515	12-Feb-02
SDK0491.000US	Sergey Anatolievich Gorobets, Alan Bennett	Cluster Auto-Alignment	US	Issued	11015211	16-Dec-04	7395404	1-Jul-08
SDK0492.000US	Alan Bennett, Sergey Anatolievich Gorobets	Scratch Pad Block	US	Issued	11016285	16-Dec-04	7315916	1-Jan-08
SDK0494.000US	Gerrit Jan Hemink	Self-Boosting System for Flash Memory Cells	US	Issued	10774014	6-Feb-04	7161833	9-Jan-07
SDK0494.001US	Gerrit Jan Hemink, Hironobu Nakao, Shih-Chung Lee	Self-Boosting Method for Flash Memory Cells	US	Issued	11321955	28-Dec-05	7466590	16-Dec-08
SDK0494.002US	Gerrit Jan Hemink	Self-Boosting System for Flash Memory Cells	US	Issued	11609688	12-Dec-06	7471566	30-Dec-08
SDK0494.003US	Gerrit Jan Hemink	Self-Boosting Method for Flash Memory Cells	US	Issued	12276186	21-Nov-08	7773414	10-Aug-10
SDK0496.000US	Jian Chen, Lee Merrill Gavens	OPERATING NON-VOLATILE MEMORY WITHOUT READ DISTURB LIMITATIONS	US	Issued	10805079	19-Mar-04	7177977	13-Feb-07
SDK0500.000US	Daniel Guterman	CONCURRENT PROGRAMMING OF NON-VOLATILE MEMORY	US	Issued	10868147	15-Jun-04	7307884	11-Dec-07
SDK0500.001US	Daniel Guterman	CONCURRENT PROGRAMMING OF NON-VOLATILE MEMORY	US	Issued	11936086	7-Nov-07	7796444	14-Sep-10
SDK0500.002US	Daniel Guterman	CONCURRENT PROGRAMMING OF NON-VOLATILE MEMORY	US	Issued	11936081	7-Nov-07	7821835	26-Oct-10
SDK0500.003US	Daniel Guterman	CONCURRENT PROGRAMMING OF NON-VOLATILE MEMORY	US	Issued	11936084	7-Nov-07	7570518	4-Aug-09
SDK0501.000US	Alan Welsh Sinclair	Adaptive Metablocks	US	Issued	10749189	30-Dec-03	7433993	7-Oct-08
SDK0503.000US	Kevin Conley, Robert C Chang, Wesley G. Brewer, Eric Bone, Yoram Cedar	Memory Card That Supports File System Interoperability	US	Published	10754483	9-Jan-04		
SDK0504.000US	Jian Chen	Method of Reading NAND Memory to Compensate for Coupling Between Storage Elements	US	Issued	10765693	26-Jan-04	7372730	13-May-08
SDK0506.001US	Yongliang Bill Wang, John H Pasternak	Voltage Regulator Using Protected Low Voltage Devices	US	Issued	10814518	31-Mar-04	7164561	16-Jan-07
SDK0507.000US	Kevin Conley, Reuven Elhamias	Flash Controller Cache Architecture	US	Issued	10796575	8-Mar-04	7173863	6-Feb-07

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0507.001US	Kevin Conley, Reuven Elhamias	Flash Controller Cache Architecture	US	Issued	11671394	5-Feb-07	7408834	5-Aug-08
SDK0507.002US	Kevin Conley, Reuven Elhamias	Flash Controller Cache Architecture	US	Published	12138708	13-Jun-08		
SDK0508.000US	Jeffrey Lutze, Tuan D. Pham, Masaaki Higashitani	Self Aligned Non-Volatile Memory Cells and Processes for Fabrication	US	Issued	10799060	12-Mar-04	7183153	27-Feb-07
SDK0508.001US	Jeffrey Lutze, Tuan D. Pham, Masaaki Higashitani	Self Aligned Non-Volatile Memory Cells and Processes for Fabrication	US	Issued	11622634	12-Jan-07	7436019	14-Oct-08
SDK0509.000US	Daniel Guterman	Programming Inhibit for Non-Volatile Memory	US	Issued	10823421	13-Apr-04	7170793	30-Jan-07
SDK0510.000US	Farookh Moogat, Yan Li	Partition of Non-Volatile Memory Array to Reduce Bit Line Capacitance	US	Issued	11078173	11-Mar-05	7313023	25-Dec-07
SDK0511.000US	Gerrit Jan Hemink, Yupin Fong	PROGRAMMING NON-VOLATILE MEMORY	US	Issued	10761620	21-Jan-04	6888758	3-May-05
SDK0511.001US	Gerrit Jan Hemink, Yupin Fong	PROGRAMMING NON-VOLATILE MEMORY	US	Issued	11081134	15-Mar-05	7035146	25-Apr-06
SDK0512.000US	John H Pasternak	Voltage Regulator with Bypass for Multi-Voltage Storage System	US	Issued	10633110	1-Aug-03	7212067	1-May-07
SDK0517.000US	Tuan D. Pham, Masaaki Higashitani	Low-Voltage, Multiple Thin-Gate Oxide and Low-Resistance Gate Electrode	US	Issued	11021693	22-Dec-04	7202125	10-Apr-07
SDK0518.000US	Masaaki Higashitani, Tuan D. Pham	Multi-Thickness Dielectric for Semiconductor Memory	US	Issued	11020402	22-Dec-04	7482223	27-Jan-09
SDK0520.000US	Sergey Anatolievich Gorobets	On-Chip Data Grouping and Alignment	US	Issued	11026549	30-Dec-04	7212440	1-May-07
SDK0520.001US	Sergey Anatolievich Gorobets	On-Chip Data Grouping and Alignment	US	Issued	11690253	21-Mar-07	7502259	10-Mar-09
SDK0521.000US	Sergey Anatolievich Gorobets, Yan Li	Pipelined Programming of Non-Volatile Memories Using Early Data	US	Issued	11013125	14-Dec-04	7120051	10-Oct-06
SDK0521.001US	Sergey Anatolievich Gorobets, Yan Li	Pipelined Programming of Non-Volatile Memories Using Early Data	US	Issued	11462946	7-Aug-06	7301805	27-Nov-07
SDK0522.000US	Sergey Anatolievich Gorobets	Erased Sector Detection Mechanisms	US	Issued	11020345	22-Dec-04	7437653	14-Oct-08
SDK0522.001US	Sergey Anatolievich Gorobets	Erased Sector Detection Mechanisms	US	Published	12208054	10-Sep-08		
SDK0523.000US	Raul-Adrian Cernea, Mehrdad Mofidi, Shahzad Khalid, Yan Li	Non-Volatile Memory And Method With Bit Line Compensation Dependent On Neighboring Operating Modes	US	Issued	10667223	17-Sep-03	6956770	18-Oct-05
SDK0523.001US	Raul-Adrian Cernea, Mehrdad Mofidi, Shahzad Khalid, Yan Li	Non-Volatile Memory And Method With Bit Line Compensation Dependent On Neighboring Operating Modes	US	Issued	11250357	13-Oct-05	7215574	8-May-07
SDK0524.000US	Alan Welsh Sinclair	Ring Bus Structure And it's Use In Flash Memory Systems	US	Published	10915039	9-Aug-04		
SDK0525.000US	Alan Welsh Sinclair	Disk Acceleration Using First and Second Storage Devices	US	Issued	10772789	4-Feb-04	7127549	24-Oct-06
SDK0525.001US	Alan Welsh Sinclair	Mass Storage Accelerator	US	Issued	11541013	29-Sep-06	7310699	18-Dec-07
SDK0527.000US	Jian Chen, Jeffrey Lutze, Kiran Ponnuru, Dat Tran, Jun Wan	Comprehensive Erase Verification for Non-Volatile Memory	US	Issued	10857245	28-May-04	7009889	7-Mar-06
SDK0527.001US	Dat Tran, Kiran Ponnuru, Jian Chen, Jeffrey Lutze, Jun Wan	Comprehensive Erase Verification for Non-Volatile Memory	US	Issued	11316069	21-Dec-05	7512014	31-Mar-09
SDK0527.002US	Dat Tran, Kiran Ponnuru, Jian Chen, Jeffrey Lutze, Jun Wan	Systems for Comprehensive Erase Verification for Non-Volatile Memory	US	Issued	11316475	21-Dec-05	7508720	24-Mar-09
SDK0527.003US	Dat Tran, Kiran Ponnuru, Jian Chen, Jeffrey Lutze, Jun Wan	Comprehensive Erase Verification for Non-Volatile Memory	US	Issued	11316119	21-Dec-05	7463532	9-Dec-08
SDK0527.004US	Dat Tran, Kiran Ponnuru, Jian Chen, Jeffrey Lutze, Jun Wan	SYSTEMS FOR COMPREHENSIVE ERASE VERIFICATION IN NON-VOLATILE MEMORY	US	Issued	11316162	21-Dec-05	7450435	11-Nov-08

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0528.002US	Robert C Chang, Henry R Hutton, Farshid Sabet-Sharghi, Haluk Kent Tanik, Ron Barzilai, Meytal Dam Ari, Mei Yan, Patricia Dwyer, Po Yuan, Bahman Qawami	Media Card Command Pass Through Methods	US	Published	12267100	7-Nov-08		
SDK0529.001US	Alan Welsh Sinclair, Kevin Conley, Carlos J. Gonzalez, Sergey Anatolievich Gorobets	Hybrid Non-Volatile Memory System	US	Published	12572844	2-Oct-09		
SDK0530.000US	Alan Bennett, Alan D Bryce, Sergey Anatolievich Gorobets, Alan Welsh Sinclair, Peter John Smith	Non-volatile memory and method with block management system	US	Issued	10750155	30-Dec-03	7139864	21-Nov-06
SDK0530.004US	Alan D Bryce, Sergey Anatolievich Gorobets, Alan Bennett	Non-Volatile Memory And Method With Control Data Management	US	Published	10917725	13-Aug-04		
SDK0530.005US	Sergey Anatolievich Gorobets, Peter John Smith, Alan Bennett	Non-Volatile Memory And Method With Memory Planes Alignment	US	Issued	12239351	26-Sep-08	7913061	22-Mar-11
SDK0530.006US	Alan Welsh Sinclair, Sergey Anatolievich Gorobets, Alan Bennett, Peter John Smith	Non-Volatile Memory And Method With Non-Sequential Update Block Management	US	Published	12239489	26-Sep-08		
SDK0530.007US	Sergey Anatolievich Gorobets	Non-Volatile Memory and Method with Phased Program Failure Handling	US	Allowed	12248160	9-Oct-08		
SDK0531.000US	Jun Wan, Jeffrey Lutze, Chan-Sui Pang	WORD LINE COMPENSATION IN NON-VOLATILE MEMORY ERASE OPERATIONS	US	Issued	11025620	29-Dec-04	7450433	11-Nov-08
SDK0531.001US	Jun Wan, Jeffrey Lutze, Chan-Sui Pang	WORD-LINE COMPENSATION IN NON-VOLATILE MEMORY ERASE OPERATIONS	US	Issued	12242831	30-Sep-08	7606074	20-Oct-09
SDK0533.000US	Nima Mokhlesi, Jeffrey Lutze	Non-Volatile Memory Cell Using High K Material and Inter-Gate Programming	US	Issued	10762181	21-Jan-04	7154779	26-Dec-06
SDK0533.002US	Nima Mokhlesi, Jeffrey Lutze	Non-Volatile Memory Cell Using High K Material and Inter-Gate Programming	US	Issued	11470932	7-Sep-06	7405968	29-Jul-08
SDK0540.000US	Yan Li, Yupin Fong, Toru Miwa	Non-volatile memory and control with improved partial page program capability	US	Issued	10830824	23-Apr-04	7057939	6-Jun-06
SDK0540.001US	Yan Li, Yupin Fong, Toru Miwa	Non-volatile memory and control with improved partial page program capability	US	Issued	11381972	5-May-06	7280396	9-Oct-07
SDK0540.002US	Yan Li, Yupin Fong, Toru Miwa	Non-volatile memory and control with improved partial page program capability	US	Issued	11867598	4-Oct-07	7453735	18-Nov-08
SDK0542.000US	Jian Chen, Chi-Ming Wang	VARIABLE PROGRAMMING OF NON-VOLATILE MEMORY	US	Issued	10818597	6-Apr-04	7020017	28-Mar-06
SDK0542.001US	Jian Chen, Chi-Ming Wang	VARIABLE READING OF NON-VOLATILE MEMORY	US	Issued	11315817	21-Dec-05	7518910	14-Apr-09
SDK0542.002US	Jian Chen, Chi-Ming Wang	SYSTEMS FOR VARIABLE PROGRAMMING OF NON-VOLATILE MEMORY	US	Issued	11316141	21-Dec-05	7259987	21-Aug-07
SDK0542.003US	Jian Chen, Chi-Ming Wang	SYSTEMS FOR VARIABLE PROGRAMMING OF NON-VOLATILE MEMORY	US	Issued	11770466	28-Jun-07	7489542	10-Feb-09
SDK0545.000US	Farookh Moogat, Yan Li, Alexander Mak	Non-Volatile system with program time control	US	Issued	10896096	20-Jul-04	7110298	19-Sep-06
SDK0545.001US	Farookh Moogat, Yan Li, Alexander Mak	Non-Volatile system with program time control	US	Issued	11462920	7-Aug-06	7262998	28-Aug-07
SDK0546.000US	Kevin Conley, Peter John Smith	Off-Chip Data Relocation	US	Issued	11022462	21-Dec-04	7409473	5-Aug-08
SDK0547.000US	Jeffrey Lutze, Nima Mokhlesi	A SELF ALIGNED SHIELD PLATE FOR LIMITING FLOATING GATE TO FLOATING GATE CAPACITANCE	US	Issued	10778634	13-Feb-04	7355237	8-Apr-08

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0547.001US	Jeffrey Lutze, Nirma Mokhlesi	Method for Forming Non-Volatile Memory with Shield Plate for Limiting Cross Coupling Between Floating Gates	US	Issued	12024787	1-Feb-08	7807533	5-Oct-10
SDK0547.002US	Jeffrey Lutze, Nirma Mokhlesi	Non-Volatile Memory with Epitaxial Regions for Limiting Cross Coupling Between Floating Gates	US	Issued	12024808	1-Feb-08	7834386	16-Nov-10
SDK0548.000US	Edwin J Cuellar, Elyahou Harari, Robert C Miller, Hem Takiar, Robert Wallace	Memory Cards Having Two Standard Sets of Contacts	US	Issued	10826801	16-Apr-04	7152801	26-Dec-06
SDK0548.001US	Edwin J Cuellar, Robert C Miller, Hem Takiar, Robert Wallace, Elyahou Harari	Memory Card With Two Sets Of Contacts	US	Issued	29203693	16-Apr-04	D535297	16-Jan-07
SDK0548.002US	Edwin J Cuellar, Elyahou Harari, Robert C Miller, Hem Takiar, Robert Wallace	Combined memory card and carrier assembly	US	Issued	29203694	16-Apr-04	D531181	31-Oct-06
SDK0548.003US	Robert C Miller, Hem Takiar, Robert Wallace, Edwin J Cuellar, Elyahou Harari	Memory Card With Two Sets Of Contacts	US	Issued	29226505	28-Mar-05	D518059	9-Mar-06
SDK0548.004US	Robert C Miller, Hem Takiar, Robert Wallace, Edwin J Cuellar, Elyahou Harari	Memory Card With Two Sets Of Contacts	US	Issued	29226477	28-Mar-05	D515586	21-Feb-06
SDK0548.005US	Robert C Miller, Hem Takiar, Robert Wallace, Edwin J Cuellar, Elyahou Harari	Memory Card With Two Sets Of Contacts	US	Issued	29226478	28-Mar-05	D515587	21-Feb-06
SDK0548.006US	Edwin J Cuellar, Elyahou Harari, Robert C Miller, Hem Takiar, Robert Wallace	Memory Card And Carrier Assembly	US	Issued	29236910	23-Aug-05	D532788	28-Nov-06
SDK0548.007US	Edwin J Cuellar, Elyahou Harari, Robert C Miller, Hem Takiar, Robert Wallace	Memory Cards Having Two Standard Sets of Contacts	US	Issued	11536974	29-Sep-06	7364090	29-Apr-08
SDK0549.001US	Alan Welsh Sinclair	Enhanced Host Interface	US	Pending	11316578	21-Dec-05		
SDK0553.000US	Feng Pan, Trung Pham	Four Phase Charge Pump Operable without Phase Overlap with Improved Efficiency	US	Issued	10842910	10-May-04	7030683	18-Apr-06
SDK0555.000US	Yupin Fong, Daniel Guteran	REDUCING FLOATING GATE TO FLOATING GATE COUPLING EFFECT	US	Issued	11021872	23-Dec-04	7230851	12-Jun-07
SDK0555.001US	Yupin Fong, Daniel Guteran	REDUCING FLOATING GATE TO FLOATING GATE COUPLING EFFECT	US	Issued	11735265	13-Apr-07	7397698	8-Jul-08
SDK0556.000US	Peter John Smith, Sergey Anatolievich Gorobets	Configurable Ready/Busy Control	US	Published	10857532	27-May-04		
SDK0557.000US	Alan Welsh Sinclair	Data Run Programming	US	Issued	11016271	16-Dec-04	7383375	3-Jun-08
SDK0562.001US	Raul-Adrian Cernea, Yan Li, Shahzad Khalid, Siu Lung Chan	Non-Volatile Memory and Method with Shared Processing for an Aggregate of Read/Write Circuits	US	Issued	11781917	23-Jul-07	7471575	30-Dec-08
SDK0562.002US	Raul-Adrian Cernea, Yan Li, Shahzad Khalid, Siu Lung Chan	Non-Volatile Memory and Method with Shared Processing for an Aggregate of Read/Write Circuits	US	Issued	12342679	23-Dec-08	7817476	19-Oct-10
SDK0562.003US	Raul-Adrian Cernea, Yan Li, Shahzad Khalid, Siu Lung Chan	Non-Volatile Memory and Method with Shared Processing for an Aggregate of Read/Write Circuits	US	Published	12900443	7-Oct-10		



FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0564.000US	Daniel Guterman, Nima Mokhlesi, Yupin Fong	Bitline Governed Approach For Program Control of Non-Volatile Memory	US	Issued	10839806	5-May-04	7020026	28-Mar-06
SDK0564.001US	Daniel Guterman, Nima Mokhlesi, Yupin Fong	Bitline Governed Approach For Program Control of Non-Volatile Memory	US	Issued	11207427	18-Aug-05	7088621	8-Aug-06
SDK0564.002US	Daniel Guterman, Nima Mokhlesi, Yupin Fong	BITLINE GOVERNED APPROACH FOR PROGRAM CONTROL OF NON-VOLATILE MEMORY	US	Issued	11207260	18-Aug-05	7280408	9-Oct-07
SDK0565.000US	Daniel Guterman, Nima Mokhlesi, Yupin Fong	BOOSTING TO CONTROL PROGRAMMING OF NON-VOLATILE MEMORY	US	Issued	10839764	5-May-04	7023733	4-Apr-06
SDK0565.001US	Daniel Guterman, Nima Mokhlesi, Yupin Fong	BOOSTING TO CONTROL PROGRAMMING OF NON-VOLATILE MEMORY	US	Issued	11392901	29-Mar-06	7301812	27-Nov-07
SDK0565.002US	Daniel Guterman, Nima Mokhlesi, Yupin Fong	BOOSTING TO CONTROL PROGRAMMING OF NON-VOLATILE MEMORY	US	Issued	11945203	26-Nov-07	7411827	12-Aug-08
SDK0568.000US	Robert C Miller, Hem Takiar, Joel Jacobs, Robert Howard, Motohide Hatanaka, Robert Wallace, Edwin J Cuellar, Eliyahou Harari	Memory Card With Two Standard Sets of Contacts and a Hinged Contact Covering Mechanism	US	Issued	10826796	16-Apr-04	7487265	3-Feb-09
SDK0568.001US	Robert C Miller, Hem Takiar, Joel Jacobs, Robert Howard, Motohide Hatanaka, Robert Wallace, Edwin J Cuellar, Eliyahou Harari, Matt Peterson	Memory Card With Integral Covered Second Device Connector For Use With Computing Devices Without A Memory Card Slot	US	Issued	11196160	2-Aug-05	7310692	18-Dec-07
SDK0568.002US	Edwin J Cuellar, Eliyahou Harari, Robert C Miller, Hem Takiar, Robert Wallace	Memory Card With a Contact Covering Lid	US	Issued	29235538	2-Aug-05	D542797	15-May-07
SDK0568.003US	Warren Middlekauf, Robert C Miller	Memory Card with Latching Mechanism for Hinged Cover	US	Issued	11462430	4-Aug-06	7710736	4-May-10
SDK0568.004US	Robert C Miller, Hem Takiar, Joel Jacobs, Robert Howard, Motohide Hatanaka, Robert Wallace, Edwin J Cuellar, Eliyahou Harari, Matt Peterson	Memory card with contacts, device connector, and a connector covering mechanism	US	Issued	11419172	18-May-06	7340540	4-Mar-08
SDK0568.005US	Robert C Miller, Hem Takiar, Joel Jacobs, Robert Howard, Motohide Hatanaka, Robert Wallace, Edwin J Cuellar, Eliyahou Harari	Memory Card With Two Standard Sets of Contacts and a Contact Covering Mechanism	US	Issued	11461731	1-Aug-06	7355860	8-Apr-08
SDK0568.006US	Robert C Miller, Hem Takiar, Joel Jacobs, Robert Howard, Motohide Hatanaka, Robert Wallace, Edwin J Cuellar, Eliyahou Harari, Matt Peterson	Memory Card With Two Standard Sets Of Contacts And A Contact Covering Mechanism	US	Issued	11938950	13-Nov-07	7554813	30-Jun-09
SDK0568.007US	Robert C Miller, Hem Takiar, Joel Jacobs, Robert Howard, Motohide Hatanaka, Robert Wallace, Edwin J Cuellar, Eliyahou Harari	Memory Card With Two Standard Sets of Contacts and a Hinged Contact Covering Mechanism	US	Issued	12361165	28-Jan-09	7822883	26-Oct-10
SDK0569.000US	Alan Welsh Sinclair, Peter John Smith	Direct Data File Storage in Flash Memories	US	Issued	11060249	16-Feb-05	7877359	25-Jan-11
SDK0569.005US	Alan Welsh Sinclair, Peter John Smith	Direct File data programming and deletion in flash memories	US	Published	12775639	7-May-10		
SDK0569.006US	Alan Welsh Sinclair	Direct data file storage implementation techniques in flash memories	US	Published	12774109	5-May-10		

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0570.002US	Baruch Boris Cohen, Muhammed Rijwane ul-Islam, Matthew Davidson	In Stream Data Encryption / Decryption and Error Correction Method	US	Published	11313447	20-Dec-05		
SDK0570.003US	Michael Holtzman, Baruch Boris Cohen, Matthew Davidson, Muhammed Rijwane ul-Islam	Memory System with in Stream Data Encryption / Decryption and Error Correction	US	Published	11313428	20-Dec-05		
SDK0571.001US	Fabrice Jogand-Coulomb, Michael Holtzman, Bahman Qawami, Ron Barzilai, Hagai Bar-EI	Method for Versatile Content Control	US	Published	11314411	20-Dec-05		
SDK0571.002-a-US	Fabrice Jogand-Coulomb, Michael Holtzman, Bahman Qawami, Ron Barzilai, Hagai Bar-EI	HOST DEVICE AND METHOD FOR PROTECTING DATA STORED IN A STORAGE DEVICE	US	Published	12624036	23-Nov-09		
SDK0571.002US	Fabrice Jogand-Coulomb, Michael Holtzman, Bahman Qawami, Ron Barzilai, Hagai Bar-EI	Memory system with versatile content control	US	Published	11314410	20-Dec-05		
SDK0571.005US	Fabrice Jogand-Coulomb, Michael Holtzman, Bahman Qawami, Ron Barzilai	Method For Creating Control Structure for Versatile Content Control	US	Published	11313538	20-Dec-05		
SDK0571.007US	Fabrice Jogand-Coulomb, Michael Holtzman, Bahman Qawami, Ron Barzilai	Method For Versatile Content Control with Partitioning	US	Published	11314052	20-Dec-05		
SDK0571.010US	Michael Holtzman, Ron Barzilai, Rotem Sela, Fabrice Jogand-Coulomb	Content Control Method Using Certificate Chains	US	Published	11557028	6-Nov-06		
SDK0571.011US	Michael Holtzman, Ron Barzilai, Rotem Sela, Fabrice Jogand-Coulomb	Content Control System Using Certificate Chains	US	Published	11557010	6-Nov-06		
SDK0571.012-1US	Rotem Sela, Ron Barzilai, Michael Holtzman, Avi Shmuel, Jason Lin	Content Control Method Using Certificate Revocation Lists	US	Published	12641160	17-Dec-09		
SDK0571.012US	Michael Holtzman, Ron Barzilai, Rotem Sela, Fabrice Jogand-Coulomb	Content Control Method Using Certificate Revocation Lists	US	Published	11557006	6-Nov-06		
SDK0571.013US	Michael Holtzman, Ron Barzilai, Rotem Sela, Fabrice Jogand-Coulomb	Content Control System Using Certificate Revocation Lists	US	Published	11557026	6-Nov-06		
SDK0571.014US	Michael Holtzman, Ron Barzilai, Fabrice Jogand-Coulomb	Content Control Method Using Versatile Control Structure	US	Published	11557049	6-Nov-06		
SDK0571.016US	Michael Holtzman, Ron Barzilai, Fabrice Jogand-Coulomb	Method for Controlling Information Supplied from Memory Device	US	Published	11557052	6-Nov-06		
SDK0571.017US	Michael Holtzman, Ron Barzilai, Rotem Sela, Fabrice Jogand-Coulomb	System for Controlling Information Supplied from Memory Device	US	Published	11557051	6-Nov-06		
SDK0571.018US	Michael Holtzman, Ron Barzilai, Fabrice Jogand-Coulomb	Control Method Using Identity Objects	US	Published	11557041	6-Nov-06		
SDK0571.019US	Michael Holtzman, Ron Barzilai, Fabrice Jogand-Coulomb	Control System Using Identify Objects	US	Published	11557039	6-Nov-06		
SDK0572.001US	Michael Holtzman, Baruch Boris Cohen, Ron Barzilai	Secure memory card with life cycle phases	US	Published	11053273	7-Feb-05		
SDK0572.002US	Michael Holtzman, Ron Barzilai, Baruch Boris Cohen, Hagai Bar-EI, David Deitcher	Methods Used in a Secure Memory Card With Life Cycle Phases	US	Published	11317390	22-Dec-05		
SDK0572.003US	Michael Holtzman, Ron Barzilai, Baruch Boris Cohen, Hagai Bar-EI, David Deitcher	Secure Memory Card with Life Cycle Phases	US	Published	11317862	22-Dec-05		
SDK0574.000US	Carlos J. Gonzalez, Jian Chen, Daniel Guterman	Delivery of a Message to a User of a Portable Data Storage Device as a Condition of Its Use	US	Issued	11035701	14-Jan-05	7392358	24-Jun-08

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0575.000US	Yongliang Bill Wang, John H Pasternak	VOLTAGE REGULATOR WITH BYPASS MODE	US	Issued	11042610	25-Jan-05	7391193	24-Jun-08
SDK0576.000US	Eric Bone, Wesley G. Brewer, Jeffrey A Salazar	Media Device	US	Issued	29206850	2-Jun-04	D508495	16-Aug-05
SDK0577.000US	Eric Bone, Wesley G. Brewer, Jeffrey A Salazar, Glenn Alwyn Wong	Storage Device	US	Issued	29206851	2-Jun-04	D511519	15-Nov-05
SDK0578.000US	Andrew Tomlin, Carlos J. Gonzalez	Situation Sensitive Memory Performance	US	Issued	11196161	2-Aug-05	7502921	10-Mar-09
SDK0578.001US	Andrew Tomlin, Carlos J. Gonzalez	Situation Sensitive Memory Performance	US	Issued	12364334	2-Feb-09	7877593	25-Jan-11
SDK0581.000US	Robert Wallace, Edwin J Cuellar, Eliyahou Harari, Yoram Cedar	Portable Memory Devices With Removable Caps That Effect Operation Of The Devices When Attached	US	Published	10888599	8-Jul-04		
SDK0582.000US	Wesley G. Brewer, Hem Taklar, Robert C Miller	Packaged Memory Devices With Various Unique Physical Appearances	US	Issued	10902899	29-Jul-04	7303127	4-Dec-07
SDK0583.000US	Gerrit Jan Hemink, Shinji Sato	Non-Volatile memory with asymmetrical doping profile	US	Issued	10952689	28-Sep-04	7294882	13-Nov-07
SDK0583.001US	Gerrit Jan Hemink, Shinji Sato	Non-Volatile memory with asymmetrical doping profile	US	Issued	11469281	31-Aug-06	7534690	19-May-09
SDK0583.002US	Gerrit Jan Hemink, Shinji Sato	Method for Angular Doping of Source and Drain Regions for Odd and Even NAND Blocks	US	Issued	12419637	7-Apr-09	7790562	7-Sep-10
SDK0583.003US	Gerrit Jan Hemink, Shinji Sato	Method for Angular Doping of Source and Drain Regions for Odd and Even NAND Blocks	US	Issued	12835468	13-Jul-10	7902031	8-Mar-11
SDK0583.004US	Gerrit Jan Hemink, Shinji Sato	Method for Angular Doping of Source and Drain Regions for Odd and Even NAND Blocks	US	Pending	13024663	10-Feb-11		
SDK0584.000US	Wesley G. Brewer, Eric Bone	Customized Non-Volatile Memory Device Packages	US	Issued	10909575	29-Jul-04	7485499	3-Feb-09
SDK0584.001US	Wesley G. Brewer, Eric Bone	Customized Non-Volatile Memory Device Packages	US	Issued	11624610	18-Jan-07	7482680	27-Jan-09
SDK0594.000US	Robert Wallace	MULTIPLE DIE INTEGRATED CIRCUIT PACKAGE	US	Issued	11264112	1-Nov-05	7511371	31-Mar-09
SDK0594.001US	Robert Wallace	METHODS FOR A MULTIPLE DIE INTEGRATED CIRCUIT PACKAGE	US	Issued	11264556	1-Nov-05	7352058	1-Apr-08
SDK0594.002US	Robert Wallace	METHODS FOR A MULTIPLE DIE INTEGRATED CIRCUIT PACKAGE	US	Issued	11931092	31-Oct-07	7514297	7-Apr-09
SDK0594.003US	Robert Wallace	MULTIPLE DIE INTEGRATED CIRCUIT PACKAGE	US	Allowed	12203496	3-Sep-08		
SDK0594.004US	Robert Wallace	METHODS FOR A MULTIPLE DIE INTEGRATED CIRCUIT PACKAGE	US	Published	12430388	27-Apr-09		
SDK0596.000US	Kevin Conley, Alan Welsh Sinclair, Peter John Smith	FAT Analysis for Optimized Sequential Cluster Management	US	Published	11022369	23-Dec-04		
SDK0597.000US	Jack H. Yuan	SELF ALIGNED TRENCH FILLING WITH HIGH COUPLING RATIO	US	Issued	10996030	23-Nov-04	7381615	3-Jun-08
SDK0597.001US	Jack H. Yuan	SELF ALIGNED TRENCHES WITH GROWN DIELECTRIC FOR HIGH COUPLING RATIO IN SEMICONDUCTOR DEVICES	US	Issued	12120134	13-May-08	7615820	10-Nov-09
SDK0599.000US	Gerrit Jan Hemink	High Speed Programming System with Reduced Over Programming	US	Issued	10990702	16-Nov-04	7092290	15-Aug-06
SDK0601.000US	Alan Welsh Sinclair	Method of Storing Transformed Units of Data in a Memory System Having Fixed Sized Storage Blocks	US	Issued	11250299	13-Oct-05	7529905	5-May-09
SDK0601.001US	Alan Welsh Sinclair	Memory System Storing Transformed Units of Data in Fixed Sized Storage Blocks	US	Issued	11250794	13-Oct-05	7814262	12-Oct-10

PATENT

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0602.000US	Carlos J. Gonzalez, Daniel Guterman	Methods of cell population distribution assisted read margining	US	Issued	11535853	27-Sep-06	7886204	8-Feb-11
SDK0602.001US	Carlos J. Gonzalez, Daniel Guterman	Memory with cell population distribution assisted read margining	US	Issued	11535879	27-Sep-06	7716538	11-May-10
SDK0602.002US	Carlos J. Gonzalez, Daniel Guterman	Methods of cell population distribution assisted read margining	US	Pending	12984505	4-Jan-11		
SDK0603.000US	Kevin Conley, Yoram Cedar	Retargetable Memory Cell Redundancy Methods	US	Issued	11270198	8-Nov-05	7379330	27-May-08
SDK0603.001US	Kevin Conley, Yoram Cedar	Memory With Retargetable Memory Cell Redundancy	US	Issued	11270410	8-Nov-05	7447066	4-Nov-08
SDK0604.000US	Jian Chen	COMPENSATING FOR COUPLING DURING READ OPERATIONS ON NON-VOLATILE MEMORY	US	Issued	11099133	5-Apr-05	7196928	27-Mar-07
SDK0604.001US	Jian Chen	COMPENSATING FOR COUPLING DURING READ OPERATIONS ON NON-VOLATILE MEMORY	US	Issued	11616736	27-Dec-06	7301810	27-Nov-07
SDK0604.002US	Jian Chen	COMPENSATING FOR COUPLING DURING READ OPERATIONS ON NON-VOLATILE MEMORY	US	Issued	11616741	27-Dec-06	7301813	27-Nov-07
SDK0604.003US	Jian Chen	COMPENSATING FOR COUPLING DURING READ OPERATIONS ON NON-VOLATILE MEMORY	US	Issued	11616745	27-Dec-06	7315477	1-Jan-08
SDK0607.000US	Yan Li	Data Recovery Methods in Multi-State Memory After Program Fail	US	Issued	11304783	14-Dec-05	7345928	18-Mar-08
SDK0607.001US	Yan Li	Multi-State Memory Having Data Recovery After Program Fail	US	Issued	11304960	14-Dec-05	7420847	2-Sep-08
SDK0608.000US	Alan Welsh Sinclair	Logically-Addressed File Storage Methods	US	Issued	11302764	13-Dec-05	7877540	25-Jan-11
SDK0608.001US	Alan Welsh Sinclair	Logically-Addressed File Storage Systems	US	Published	11300568	13-Dec-05		
SDK0611.000US	Yan Li, Seungpil Lee, Siu Lung Chan	Non-volatile memory and method with power-saving read and program-verify operations	US	Issued	11083514	16-Mar-05	7251160	31-Jul-07
SDK0611.001US	Yan Li, Seungpil Lee, Siu Lung Chan	Non-volatile memory and method with power-saving read and program-verify operations	US	Allowed	11534297	22-Sep-06		
SDK0611.002US	Yan Li, Seungpil Lee, Siu Lung Chan	Non-volatile memory and method with power-saving read and program-verify operations	US	Issued	11534307	22-Sep-06	7570513	4-Aug-09
SDK0612.000US	Jian Chen	CONTINUED VERIFICATION IN NON-VOLATILE MEMORY WRITE OPERATIONS	US	Issued	11322011	29-Dec-05	7307887	11-Dec-07
SDK0612.001US	Jian Chen	SYSTEMS FOR CONTINUED VERIFICATION IN NON-VOLATILE MEMORY WRITE OPERATIONS	US	Issued	11322001	29-Dec-05	7352629	1-Apr-08
SDK0613.000US	Fabrice Jogand-Coulomb, Bahman Qawami, Farshid Sabet-Sharghi, Carlos J. Gonzalez	Portable Mass Storage Device with Virtual Machine Activation	US	Issued	11463256	8-Aug-06	7725614	25-May-10
SDK0613.002US	Fabrice Jogand-Coulomb, Bahman Qawami, Farshid Sabet-Sharghi, Carlos J. Gonzalez	Portable Mass Storage Device with Virtual Machine Activation	US	Pending	12762786	19-Apr-10		
SDK0617.000US	Jun Wan, Jeffrey Lutze, Masaaki Higashitani, Gerrit Jan Hemink, Ken Oowada, Jian Chen, Geoffrey S Gongwer	SELECTIVE APPLICATION OF PROGRAM INHIBIT SCHEMES IN NON-VOLATILE MEMORY	US	Issued	11127743	12-May-05	7295478	13-Nov-07
SDK0617.001US	Jun Wan, Jeffrey Lutze, Masaaki Higashitani, Gerrit Jan Hemink, Ken Oowada, Jian Chen, Geoffrey S Gongwer	SELECTIVE PROGRAM VOLTAGE RAMP RATES IN NON-VOLATILE MEMORY	US	Issued	11866261	2-Oct-07	7447086	4-Nov-08

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0618.000US	Hao Fang, Gerrit Jan Hemink, Tuan D. Pham, Masaaki Higashitani	Methods for Active Boosting to Minimize Capacitive Coupling Effect Between Adjacent Gates of Flash Memory Devices	US	Issued	11319260	27-Dec-05	7362615	22-Apr-08
SDK0618.001US	Tuan D. Pham, Masaaki Higashitani, Hao Fang, Gerrit Jan Hemink	Active Boosting to Minimize Capacitive Coupling Effect Between Adjacent Gates of Flash Memory Devices	US	Issued	11319908	27-Dec-05	7436703	14-Oct-08
SDK0618.002US	Tuan D. Pham, Masaaki Higashitani, Hao Fang, Gerrit Jan Hemink	Methods for Active Boosting to Minimize Capacitive Coupling Effect Between Adjacent Gates of Flash Memory Devices	US	Issued	12031640	14-Feb-08	7672165	2-Mar-10
SDK0619.000US	Jian Chen, Raul-Adrian Cernea, Gerrit Jan Hemink	COMPENSATING FOR COUPLING IN NON-VOLATILE STORAGE	US	Issued	11099239	5-Apr-05	7196946	27-Mar-07
SDK0619.001US	Jian Chen, Raul-Adrian Cernea, Gerrit Jan Hemink	COMPENSATING FOR COUPLING IN NON-VOLATILE STORAGE	US	Issued	11675632	16-Feb-07	7321509	22-Jan-08
SDK0620.000US	Raul-Adrian Cernea	COMPENSATION CURRENTS IN NON-VOLATILE MEMORY READ OPERATIONS	US	Issued	11157033	20-Jun-05	7193898	20-Mar-07
SDK0620.001US	Raul-Adrian Cernea	Non-Volatile Memory Read Operations Using Compensation Currents	US	Issued	11675545	15-Feb-07	7656713	2-Feb-10
SDK0620.002US	Raul-Adrian Cernea	COMPENSATION CURRENTS IN NON-VOLATILE MEMORY READ OPERATIONS	US	Issued	11675551	15-Feb-07	7379343	27-May-08
SDK0620.003US	Raul-Adrian Cernea	Time Dependent Compensation Currents in Non-Volatile Memory Read Operations	US	Issued	12108710	24-Apr-08	7535769	19-May-09
SDK0621.000US	Alan Welsh Sinclair	Method and System for Dual Mode Access for Storage Devices	US	Issued	11196168	3-Aug-05	7627733	1-Dec-09
SDK0622.000US	Sergey Anatolievich Gorobets, Reuven Elharmias, Carlos J. Gonzalez, Kevin Conley	Corrected Data Storage and Handling Methods	US	Issued	11253531	18-Oct-05	7173852	6-Feb-07
SDK0624.000US	Yan Li, Jian Chen	READ OPERATION FOR NON-VOLATILE STORAGE THAT INCLUDES COMPENSATION FOR COUPLING	US	Issued	11099049	5-Apr-05	7187585	6-Mar-07
SDK0624.001US	Yan Li, Jian Chen	READ OPERATION FOR NON-VOLATILE STORAGE THAT INCLUDES COMPENSATION FOR COUPLING	US	Issued	11616757	27-Dec-06	7301808	27-Nov-07
SDK0624.002US	Yan Li, Jian Chen	READ OPERATION FOR NON-VOLATILE STORAGE THAT INCLUDES COMPENSATION FOR COUPLING	US	Issued	11616762	27-Dec-06	7301816	27-Nov-07
SDK0624.003US	Yan Li, Jian Chen	Read Operation for Non-Volatile Storage that Includes Compensation for Coupling	US	Issued	11616764	27-Dec-06	7321510	22-Jan-08
SDK0624.004US	Yan Li, Jian Chen	READ OPERATION FOR NON-VOLATILE STORAGE THAT INCLUDES COMPENSATION FOR COUPLING	US	Issued	11616769	27-Dec-06	7301839	27-Nov-07
SDK0624.005US	Yan Li, Jian Chen	READ OPERATION FOR NON-VOLATILE STORAGE THAT INCLUDES COMPENSATION FOR COUPLING	US	Issued	11616778	27-Dec-06	7414886	19-Aug-08
SDK0625.000US	Nima Mokhlesi	Reverse reading in non-volatile memory with compensation for coupling	US	Issued	11537548	29-Sep-06	7684247	23-Mar-10
SDK0625.001US	Nima Mokhlesi	System for reverse reading in non-volatile memory with compensation for coupling	US	Issued	11537556	29-Sep-06	7447076	4-Nov-08
SDK0625.002US	Nima Mokhlesi	Reverse reading in non-volatile memory with compensation for coupling	US	Published	12710984	23-Feb-10		
SDK0626.000US	Fabrice Jogand-Coulomb, Bahman Qawami, Farshid Sabet-Sharghi	System and method for Generating Revenue Based on Digital Content Distribution	US	Pending	11137916	26-May-05		
SDK0626.001US	Fabrice Jogand-Coulomb, Bahman Qawami, Farshid Sabet-Sharghi	System and method for Generating Revenue Based on Digital Content Distribution	US	Published	12726955	18-Mar-10		
SDK0627.000US	Sergey Anatolievich Gorobets, Alan Welsh Sinclair	Dual Mode Access for Non-Volatile Storage Devices	US	Issued	11314842	21-Dec-05	7793068	7-Sep-10

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0628.001US	Oktaý Rasizade, Bahman Qawami, Fabrice Jogand-Coulomb, Robert C Chang, Farshid Sabet-Sharshi	Method for managing keys and/or rights objects	US	Published	11283221	18-Nov-05		
SDK0628.002US	Oktaý Rasizade, Bahman Qawami, Fabrice Jogand-Coulomb, Robert C Chang, Farshid Sabet-Sharshi	Method for managing keys and/or rights objects	US	Published	12774174	5-May-10		
SDK0630.000US	Gerrit Jan Hemink	FASTER PROGRAMMING OF HIGHER LEVEL STATES IN MULTI-LEVEL CELL FLASH MEMORY	US	Issued	11099259	5-Apr-05	7173859	6-Feb-07
SDK0631.000US	Tuan D. Pham, Masaaki Higashitani	Integration process flow for flash devices with low gap fill aspect ratio	US	Issued	11254142	18-Oct-05	7541240	2-Jun-09
SDK0632.000US	Daniel Guterman	ALTERNATIVE ROW- BASED READING AND WRITING FOR NON-VOLATILE MEMORY	US	Issued	11321259	29-Dec-05	7349260	25-Mar-08
SDK0632.001US	Daniel Guterman	SYSTEMS FOR ALTERNATIVE ROW- BASED READING AND WRITING FOR NON-VOLATILE MEMORY	US	Issued	11321346	29-Dec-05	7443726	28-Oct-08
SDK0632.002US	Daniel Guterman	ALTERNATIVE ROW- BASED READING AND WRITING FOR NON-VOLATILE MEMORY	US	Issued	11933348	31-Oct-07	7573747	11-Aug-09
SDK0632.003US	Daniel Guterman	ALTERNATIVE ROW- BASED READING AND WRITING FOR NON-VOLATILE MEMORY	US	Issued	12538773	10-Aug-09	7889560	15-Feb-11
SDK0634.001US	Michael Holtzman, Ron Barzilai, Reuven Elhamias, Niv Cohen	Method of Hardware Driver Integrity Check of Memory Card Controller Firmware	US	Issued	11284623	21-Nov-05	7536540	19-May-09
SDK0634.002US	Michael Holtzman, Ron Barzilai, Reuven Elhamias, Niv Cohen	Hardware Driver Integrity Check of Memory Card Controller Firmware	US	Pending	11285600	21-Nov-05		
SDK0635.000US	Jian Chen	REVERSE COUPLING EFFECT WITH TIMING INFORMATION	US	Issued	11272335	10-Nov-05	7289348	30-Oct-07
SDK0635.001US	Jian Chen	REVERSE COUPLING EFFECT WITH TIMING INFORMATION FOR NON-VOLATILE MEMORY	US	Issued	11271241	10-Nov-05	7289344	30-Oct-07
SDK0635.002US	Jian Chen	REVERSE COUPLING EFFECT WITH TIMING INFORMATION	US	Issued	11858902	21-Sep-07	7583551	1-Sep-09
SDK0636.000US	Sergey Anatolievich Gorobets, Kevin Conley	Methods of End of Life Calculation for Non-Volatile Memories	US	Issued	11383384	15-May-06	7523013	21-Apr-09
SDK0636.001US	Sergey Anatolievich Gorobets, Kevin Conley	Non-Volatile Memory System with End of Life Calculation	US	Issued	11383397	15-May-06	7778077	17-Aug-10
SDK0639.000US	Robert C Chang, Michael Holtzman, Farshid Sabet-Sharghi, Paul McAvoy, Bahman Qawami	Method and System for Managing Partitions in a Storage Device	US	Issued	11170221	29-Jun-05	7457910	25-Nov-08
SDK0639.001US	Robert C Chang, Michael Holtzman, Farshid Sabet-Sharghi, Paul McAvoy, Bahman Qawami	Method for Managing Partitions in a Storage Device	US	Issued	12252863	16-Oct-08	7853772	14-Dec-10
SDK0642.000US	Gerrit Jan Hemink, Ken Oowada	METHOD FOR PROGRAMMING NON-VOLATILE MEMORY WITH REDUCED PROGRAM DISTURB USING MODIFIED PASS VOLTAGES	US	Issued	11313023	19-Dec-05	7355889	8-Apr-08
SDK0642.001US	Gerrit Jan Hemink, Ken Oowada	APPARATUS FOR PROGRAMMING NON-VOLATILE MEMORY WITH REDUCED PROGRAM DISTURB USING MODIFIED PASS VOLTAGES	US	Issued	11312925	19-Dec-05	7355888	8-Apr-08
SDK0645.000US	Alan Welsh Sinclair	Mass Data Storage System	US	Pending	11196826	3-Aug-05		
SDK0648.000US	Jun Wan, Jeffrey Lutze	Method for Programming Non-Volatile Memory with Self-Adjusting Maximum Program Loop	US	Issued	11194439	1-Aug-05	7230854	12-Jun-07

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0648.001US	Jun Wan, Jeffrey Lutze	SYSTEM FOR PROGRAMMING NON-VOLATILE MEMORY WITH SELF-ADJUSTING MAXIMUM PROGRAM LOOP	US	Issued	11194827	1-Aug-05	7023737	4-Apr-06
SDK0648.002US	Jun Wan, Jeffrey Lutze	SYSTEM FOR PROGRAMMING NON-VOLATILE MEMORY WITH SELF-ADJUSTING MAXIMUM PROGRAM LOOP	US	Issued	11394441	31-Mar-06	7206231	17-Apr-07
SDK0650.000US	Hem Takiar, Shrikar Bhagath, Chin-Tien Hoge Chlu	METHOD OF MANUFACTURING FLASH MEMORY CARDS	US	Issued	11265337	2-Nov-05	7485501	3-Feb-09
SDK0651.000US	Hem Takiar, Robert C Miller, Warren Middlekauf, Michael Patterson, Shrikar Bhaazath	SYSTEM-IN-A-PACKAGE BASED FLASH MEMORY CARD	US	Published	11133576	19-May-05		
SDK0651.001US	Hem Takiar, Robert C Miller, Warren Middlekauf, Michael Patterson, Shrikar Bhaazath	SIP BASED FLASH MEMORY CARD	US	Issued	29230382	19-May-05	D548740	14-Aug-07
SDK0652.000US	Yan Li, Long Pham	METHOD FOR PROGRAMMING OF MULTI-STATE NON-VOLATILE MEMORY USING SMART VERIFY	US	Issued	11260658	27-Oct-05	7301817	27-Nov-07
SDK0652.001US	Yan Li, Long Pham	APPARATUS FOR PROGRAMMING OF MULTI-STATE NON-VOLATILE MEMORY USING SMART VERIFY	US	Issued	11259799	27-Oct-05	7366022	29-Apr-08
SDK0652.002US	Yan Li, Long Pham	METHOD FOR PROGRAMMING OF MULTI-STATE NON-VOLATILE MEMORY USING SMART VERIFY	US	Issued	11862157	26-Sep-07	7492634	17-Feb-09
SDK0653.000US	Jeffrey Lutze	STARTING PROGRAM VOLTAGE SHIFT WITH CYCLING OF NON-VOLATILE MEMORY	US	Issued	11144264	3-Jun-05	7339834	4-Mar-08
SDK0653.001US	Jeffrey Lutze	STARTING PROGRAM VOLTAGE SHIFT WITH CYCLING OF NON-VOLATILE MEMORY	US	Issued	12018275	23-Jan-08	7633812	15-Dec-09
SDK0653.002US	Jeffrey Lutze	STARTING PROGRAM VOLTAGE SHIFT WITH CYCLING OF NON-VOLATILE MEMORY	US	Issued	12018279	23-Jan-08	7630254	8-Dec-09
SDK0653.003US	Jeffrey Lutze	STARTING PROGRAM VOLTAGE SHIFT WITH CYCLING OF NON-VOLATILE MEMORY	US	Published	12572069	1-Oct-09		
SDK0654.000US	Jun Wan, Jeffrey Lutze	Last-First Mode and Method for Programming of Non-Volatile Memory with Reduced Program Disturb	US	Issued	11223623	9-Sep-05	7218552	15-May-07
SDK0654.001US	Jun Wan, Jeffrey Lutze	Last-First Mode and Apparatus for Programming of Non-Volatile Memory with Reduced Program Disturb	US	Issued	11223273	9-Sep-05	7170788	30-Jan-07
SDK0655.000US	Alan Bennett, Alan D Bryce, Sergey Anatolievich Gorobets	Methods For The Management Of Erase Operations In Non-Volatile Memories	US	Issued	11273774	14-Nov-05	7624239	24-Nov-09
SDK0655.001US	Alan Bennett, Alan D Bryce, Sergey Anatolievich Gorobets	Structures for the Management of Erase Operations in Non-Volatile Memories	US	Issued	11273773	14-Nov-05	7783845	24-Aug-10
SDK0656.001US	Alan Bennett, Sergey Anatolievich Gorobets, Andrew Tomlin, Charles Michael Schrafer	Scheduling of Housekeeping Operations in Flash Memory Systems	US	Issued	11312985	19-Dec-05	7315917	1-Jan-08
SDK0656.002US	Alan Bennett, Sergey Anatolievich Gorobets, Andrew Tomlin, Charles Michael Schrafer	Scheduling of Housekeeping Operations in Flash Memory Systems	US	Issued	11949618	3-Dec-07	7565478	21-Jul-09
SDK0656.003US	Alan Bennett, Sergey Anatolievich Gorobets, Andrew Tomlin, Charles Michael Schrafer	Scheduling of Housekeeping Operations in Flash Memory Systems	US	Published	12493500	30-Jun-09		

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0659.001US	Masaaki Higashitani	Erasing Non-Volatile Memory Utilizing Changing Word Line Conditions to Compensate to Slower Erasing Memory Cells	US	Issued	11295755	6-Dec-05	7430138	30-Sep-08
SDK0659.002US	Masaaki Higashitani	Systems for erasing non-volatile memory utilizing changing word line conditions to compensate for slower erasing memory cells	US	Issued	11296032	6-Dec-05	7403428	22-Jul-08
SDK0660.002US	Sergey Anatolievich Gorobets	Non-Volatile Memory And Method With Memory Allocation For a Directly Mapped File Storage System	US	Published	12701428	5-Feb-10		
SDK0661.000US	Sergey Anatolievich Gorobets, Alan Welsh Sinclair	Method and System for Accessing Non-Volatile Storage Devices	US	Issued	11313567	21-Dec-05	7747837	29-Jun-10
SDK0661.001US	Sergey Anatolievich Gorobets, Alan Welsh Sinclair	Method and System for Accessing Non-Volatile Storage Devices	US	Issued	11313633	21-Dec-05	7769978	3-Aug-10
SDK0661.002US	Sergey Anatolievich Gorobets, Alan Welsh Sinclair	Method and System for Dual Mode Access For Non-Volatile Memories With Versions of File Data Identified By Identical File ID and File Offset Stored in Identical Location Within A Memory Page	US	Published	12714237	26-Feb-10		
SDK0662.002US	Sergey Anatolievich Gorobets	Non-Volatile Memories With Versions of File Data Identified By Identical File ID and File Offset Stored in Identical Location Within A Memory Page	US	Published	12404260	13-Mar-09		
SDK0663.002US	Sergey Anatolievich Gorobets	Non-Volatile Memories And Method With Adaptive File Handling In A Directly Mapped File Storage	US	Published	12405140	16-Mar-09		
SDK0664.000US	Gerrit Jan Hemink, Teruhiko Kamei	ERASING NON-VOLATILE MEMORY USING INDIVIDUAL VERIFICATION AND ADDITIONAL ERASING OF SUBSETS OF MEMORY CELLS	US	Issued	11296055	6-Dec-05	7403424	22-Jul-08
SDK0664.001US	Gerrit Jan Hemink, Teruhiko Kamei	ERASING NON-VOLATILE MEMORY USING INDIVIDUAL VERIFICATION AND ADDITIONAL ERASING OF SUBSETS OF MEMORY CELLS	US	Issued	11296028	6-Dec-05	7400537	15-Jul-08
SDK0664.002US	Gerrit Jan Hemink, Teruhiko Kamei	SOFT PROGRAMMING NON-VOLATILE MEMORY UTILIZING INDIVIDUAL VERIFICATION AND ADDITIONAL SOFT PROGRAMMING OF SUBSETS OF MEMORY CELLS	US	Issued	11295747	6-Dec-05	7486564	3-Feb-09
SDK0664.003US	Gerrit Jan Hemink, Teruhiko Kamei	SYSTEMS FOR SOFT PROGRAMMING NON-VOLATILE MEMORY UTILIZING INDIVIDUAL VERIFICATION AND ADDITIONAL SOFT PROGRAMMING OF SUBSETS OF MEMORY CELLS	US	Issued	11296071	6-Dec-05	7408804	5-Aug-08
SDK0664.004US	Gerrit Jan Hemink, Teruhiko Kamei	Erasing Non-Volatile Memory Using Individual Verification and Additional Erasing of Subsets of Memory Cells	US	Issued	12040632	29-Feb-08	7606100	20-Oct-09
SDK0665.001US	Kevin Conley, E. Earle Thompson	In-line cache using nonvolatile memory between host and disk device	US	Issued	11267534	4-Nov-05	7634585	15-Dec-09
SDK0666.001US	Reuven Elhamias	Memory cards with end of life recovery and resizing	US	Issued	11536192	28-Sep-06	7596656	29-Sep-09
SDK0667.000US	Hem Takiar, Shrikar Bhagath	METHOD OF FABRICATING LEADFRAME BASED FLASH MEMORY CARDS INCLUDING SINGULATION BY STRAIGHT LINE CUTS	US	Issued	11321350	29-Dec-05	7488620	10-Feb-09
SDK0667.001US	Hem Takiar, Shrikar Bhagath	LEADFRAME BASED FLASH MEMORY CARDS	US	Issued	12363311	30-Jan-09	7795715	14-Sep-10
SDK0668.000US	Hem Takiar, Shrikar Bhagath	TEST PADS ON FLASH MEMORY CARDS	US	Issued	11321663	29-Dec-05	7259028	21-Aug-07



FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0668.001US	Hem Takiar, Shrikar Bhagath	TEST PADS ON FLASH MEMORY CARDS	US	Issued	11777027	12-Jul-07	7495255	24-Feb-09
SDK0669.000US	Hem Takiar, Manickam Thavarajah, Ken Jian Ming Wang, Chih Chin Liao, Andre McKenzie, Shrikar Bhagath, Han-Shiao Joyce Chen, Tien-Hoang Chiu, Kevin Conley	STRIP FOR INTEGRATED CIRCUIT PACKAGES HAVING A MAXIMIZED USABLE AREA	US	Published	11321426	29-Dec-05		
SDK0670.000US	Kevin Conley	Initialization of Flash Storage Via an Embedded Controller	US	Issued	11250094	13-Oct-05	7640424	29-Dec-09
SDK0670.001US	Kevin Conley	Initialization of Flash Storage Via an Embedded Controller	US	Published	12621011	18-Nov-09		
SDK0672.000US	Chin-Tien Hope Chiu, Chih Chin Liao, Ken Jian Ming Wang, Han-Shiao Joyce Chen, Cheeman Yu, Hem Takiar	RIGID WAVE PATTERN DESIGN ON CHIP CARRIER SUBSTRATE AND PRINTED CIRCUIT BOARD FOR SEMICONDUCTOR AND ELECTRONIC SUB-SYSTEM PACKAGING	US	Issued	11106699	14-Apr-05	7355283	8-Apr-08
SDK0672.001US	Chin-Tien Hope Chiu, Chih Chin Liao, Ken Jian Ming Wang, Han-Shiao Joyce Chen, Cheeman Yu, Hem Takiar	RIGID WAVE PATTERN DESIGN ON CHIP CARRIER SUBSTRATE AND PRINTED CIRCUIT BOARD FOR SEMICONDUCTOR AND ELECTRONIC SUB-SYSTEM PACKAGING	US	Published	11930816	31-Oct-07		
SDK0673.000US	Alan Welsh Sinclair	Scheduling of Reclaim Operations in Non-Volatile Memory	US	Issued	11259423	25-Oct-05	7409489	5-Aug-08
SDK0673.001US	Alan Welsh Sinclair	Non-Volatile Memory With Scheduled Reclaim Operations	US	Allowed	11259439	25-Oct-05		
SDK0674.000US	Dave Sanjay, Jeffrey A Salazar, Yishai Kagan, Daniel Kennedy, Robert Howard, Matt Peterson, Joel Jacobs, Kevin Conley, Mike Morganstern, Wesley G. Brewer, Robert C Miller, Jean-Christophe Klein	PORTABLE MEMORY STORAGE DEVICE WITH BIOMETRIC IDENTIFICATION SECURITY	US	Published	11076221	9-Mar-05		
SDK0674.001US	Jeffrey A Salazar, Wesley G. Brewer, Suresh Upadhyayula, Dave Sanjay, Daniel Kennedy, Robert C Miller, Robert Howard, Matt Peterson, Joel Jacobs, Kevin Conley, Mike Morganstern	PORTABLE MEMORY STORAGE DEVICE	US	Issued	29224969	9-Mar-05	D524817	11-Jul-06
SDK0675.000US	Nima Mokhlesi	Method for Non-Real Time Reprogramming of Non-Volatile Memory to Achieve Tighter Distribution of Threshold Voltages	US	Issued	11425790	22-Jun-06	7486561	3-Feb-09
SDK0675.001US	Nima Mokhlesi	System for Non-Real Time Reprogramming of Non-Volatile Memory to Achieve Tighter Distribution of Threshold Voltages	US	Issued	11425794	22-Jun-06	7489549	10-Feb-09
SDK0675.002US	Nima Mokhlesi	Method for Non-Real Time Reprogramming of Non-Volatile Memory to Achieve Tighter Distribution of Threshold Voltages	US	Issued	12344779	29-Dec-08	7633802	15-Dec-09
SDK0676.000US	Hem Takiar, Suresh Upadhyayula	METHOD OF ASSEMBLING SEMICONDUCTOR DEVICES WITH LEADS	US	Issued	11129637	13-May-05	7384817	10-Jun-08
SDK0676.001US	Hem Takiar, Suresh Upadhyayula	METHOD OF ASSEMBLING SEMICONDUCTOR DEVICES WITH LEADS	US	Issued	12108837	24-Apr-08	7812356	12-Oct-10
SDK0676.002US	Hem Takiar, Suresh Upadhyayula	METHOD OF ASSEMBLING SEMICONDUCTOR DEVICES WITH LEADS	US	Pending	12901787	11-Oct-10		
SDK0677.000US	Christina Day, Peter Galat, Robert C Miller	FLASH MEMORY CARD WITH TRANSLUCENT COVER	US	Issued	29230385	19-May-05	D537824	6-Mar-07

PATENT

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0677.001US	Christina Day, Peter Galat, Robert C Miller	FLASH MEMORY CARD WITH TRANSLUCENT COVER	US	Issued	29244040	2-Dec-05	D554123	30-Oct-07
SDK0678.000US	Emilio Yero	Cycle count storage methods	US	Issued	11404672	13-Apr-06	7451264	11-Nov-08
SDK0678.001US	Emilio Yero	Cycle count storage systems	US	Issued	11404454	13-Apr-06	7467253	16-Dec-08
SDK0679.000US	Raul-Adrian Cernea, Yan Li	Use of Data Latches in Multi-Phase Programming of Non-Volatile Memories	US	Issued	11097517	1-Apr-05	7158421	2-Jan-07
SDK0679.001US	Yan Li, Raul-Adrian Cernea	Use of Data Latches in Multi-Phase Programming of Non-Volatile Memories	US	Issued	11566583	4-Dec-06	7508721	24-Mar-09
SDK0680.000US	Reuven Elhamias, Vivek Mani, Niv Cohen	System and method for write failure recovery	US	Issued	11397416	3-Apr-06	7835518	16-Nov-10
SDK0681.000US	Cheeman Yu, Chih Chin Liao, Hem Takiar	HIGH DENSITY THREE DIMENSIONAL SEMICONDUCTOR DIE PACKAGE	US	Issued	11264889	2-Nov-05	7663216	16-Feb-10
SDK0681.001US	Cheeman Yu, Chih Chin Liao, Hem Takiar	HIGH DENSITY THREE DIMENSIONAL SEMICONDUCTOR DIE PACKAGE	US	Published	12648697	29-Dec-09		
SDK0682.000US	Raul-Adrian Cernea, Siu Lung Chan	Non-Volatile Memory and Method with Compensation for Source Line Bias Errors	US	Issued	11097038	1-Apr-05	7173854	6-Feb-07
SDK0682.001US	Raul-Adrian Cernea, Siu Lung Chan	Non-Volatile Memory and Method with Control Compensation for Source Line Bias Errors	US	Issued	11097502	1-Apr-05	7170784	30-Jan-07
SDK0682.002US	Raul-Adrian Cernea, Siu Lung Chan	Non-Volatile Memory and Method with Control Compensation for Source Line Bias Errors	US	Issued	11624617	18-Jan-07	7391645	24-Jun-08
SDK0682.003US	Raul-Adrian Cernea, Siu Lung Chan	Non-Volatile Memory and Method with Control Compensation for Source Line Bias Errors	US	Issued	11624627	18-Jan-07	7391646	24-Jun-08
SDK0682.004US	Raul-Adrian Cernea, Siu Lung Chan	Non-Volatile Memory and Method with Control Compensation for Source Line Bias Errors	US	Issued	12143015	20-Jun-08	7499324	3-Mar-09
SDK0683.000US	Emilio Yero, Yan Li	Use of Data Latches in Cache Operations of Non-Volatile Memories	US	Issued	11097590	1-Apr-05	7206230	17-Apr-07
SDK0683.001US	Yan Li, Emilio Yero	Use of Data Latches in Cache Operations of Non-Volatile Memories	US	Issued	11619513	3-Jan-07	7577037	18-Aug-09
SDK0683.002US	Emilio Yero, Yan Li	Use of Data Latches in Cache Operations of Non-Volatile Memories	US	Allowed	12495200	30-Jun-09		
SDK0684.001US	Gidon Elazar, Dan Harkabi, Nehemiah Weingarten	Apparatus for transferring licensed digital content between users	US	Published	11531455	13-Sep-06		
SDK0686.000US	Masaaki Higashitani	Method of Forming Low Resistance Void-Free Contacts	US	Issued	11296022	6-Dec-05	7615448	10-Nov-09
SDK0686.001US	Masaaki Higashitani	Low Resistance Void-Free Contacts	US	Issued	11296235	6-Dec-05	7737483	15-Jun-10
SDK0689.000US	Masaaki Higashitani	Methods of Forming Flash Devices With Shared Word Lines	US	Issued	11316474	21-Dec-05	7655536	2-Feb-10
SDK0689.001US	Masaaki Higashitani	Flash Devices With Shared Word Lines	US	Issued	11316654	21-Dec-05	7495294	24-Feb-09
SDK0689.002US	Masaaki Higashitani	Methods of Forming Flash Devices With Shared Word Lines	US	Published	12641113	17-Dec-09		
SDK0691.000US	Siu Lung Chan	Methods for Improved Program-Verify Operations in Non-Volatile Memories	US	Issued	11323596	29-Dec-05	7224614	29-May-07
SDK0691.001US	Siu Lung Chan	Non-Volatile Memory With Improved Program-Verify Operations	US	Issued	11323577	29-Dec-05	7310255	18-Dec-07
SDK0691.002US	Siu Lung Chan	Methods for Improved Program-Verify Operations in Non-Volatile Memories	US	Issued	11753327	24-May-07	7447081	4-Nov-08
SDK0691.003US	Siu Lung Chan	Non-Volatile Memory With Improved Program-Verify Operations	US	Issued	11955624	13-Dec-07	7609556	27-Oct-09

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0692.000US	Yan Li, Yupin Fong	Compensating For Coupling Based on Sensing A Neighbor Using Coupling	US	Issued	11458997	20-Jul-06	7522454	21-Apr-09
SDK0692.001US	Yan Li, Yupin Fong	System that Compensates For Coupling Based on Sensing A Neighbor Using Coupling	US	Issued	11459000	20-Jul-06	7443729	28-Oct-08
SDK0692.002US	Yan Li	Compensating for Coupling During Programming	US	Issued	11459001	20-Jul-06	7885119	8-Feb-11
SDK0692.003US	Yan Li	System that Compensates For Coupling During Programming	US	Issued	11459002	20-Jul-06	7400535	15-Jul-08
SDK0692.004US	Yan Li	Method For Configuring Compensation	US	Issued	11458996	20-Jul-06	7506113	17-Mar-09
SDK0692.005US	Yan Li	System For Configuring Compensation	US	Issued	11458995	20-Jul-06	7495953	24-Feb-09
SDK0692.006US	Yan Li	System that Compensates For Coupling During Programming	US	Issued	12124434	21-May-08	7480179	20-Jan-09
SDK0692.007US	Yan Li	System that Compensates For Coupling During Programming	US	Issued	12124440	21-May-08	7630248	8-Dec-09
SDK0692.008US	Yan Li, Yupin Fong	System that Compensates For Coupling Based on Sensing A Neighbor Using Coupling	US	Allowed	12205916	8-Sep-08		
SDK0692.009US	Yan Li, Yupin Fong	System that Compensates For Coupling Based on Sensing A Neighbor Using Coupling	US	Issued	12205918	8-Sep-08	7602647	13-Oct-09
SDK0692.010US	Yan Li, Yupin Fong	System that Compensates For Coupling Based on Sensing A Neighbor Using Coupling	US	Issued	12205919	8-Sep-08	7616480	10-Nov-09
SDK0692.011US	Yan Li	Compensating for Coupling During Programming	US	Published	12620508	17-Nov-09		
SDK0693.000US	Jeffrey Lutze, Yan Li, Siu Lung Chan	METHOD FOR CONTROLLED PROGRAMMING OF NON-VOLATILE MEMORY EXHIBITING BIT LINE COUPLING	US	Issued	11250735	14-Oct-05	7286406	23-Oct-07
SDK0693.001US	Jeffrey Lutze, Yan Li, Siu Lung Chan	APPARATUS FOR CONTROLLED PROGRAMMING OF NON-VOLATILE MEMORY EXHIBITING BIT LINE COUPLING	US	Issued	11251458	14-Oct-05	7206235	17-Apr-07
SDK0694.001US	Alan Welsh Sinclair, Barry Wright	Data consolidation and garbage collection in direct data file storage memories	US	Issued	11461997	2-Aug-06	7610437	27-Oct-09
SDK0694.002US	Alan Welsh Sinclair, Barry Wright	Flash Memory Systems With Direct Data File Storage Utilizing Data Consolidation and Garbage Collection	US	Issued	11462001	2-Aug-06	7562181	14-Jul-09
SDK0694.003US	Alan Welsh Sinclair, Barry Wright	Data operations in flash memories utilizing direct data file storage	US	Issued	11462007	2-Aug-06	7590794	15-Sep-09
SDK0694.004US	Alan Welsh Sinclair, Barry Wright	Flash memory systems utilizing direct data file storage	US	Issued	11462013	2-Aug-06	7590795	15-Sep-09
SDK0695.000US	Shouchang Tsao, Yan Li	Method for Power-Saving Multi-Pass Sensing in Non-Volatile Memory	US	Issued	11322427	29-Dec-05	7447094	4-Nov-08
SDK0695.001US	Shouchang Tsao, Yan Li	Non-Volatile Memory With Power-Saving Multi-Pass Sensing	US	Issued	11323569	29-Dec-05	7733704	8-Jun-10
SDK0695.002US	Shouchang Tsao, Yan Li	Non-Volatile Memory With Power-Saving Multi-Pass Sensing	US	Allowed	12763365	20-Apr-10		
SDK0696.000US	Hem Takiar, Shrikar Bhagath, Ken Jian Ming Wang	METHOD OF REDUCING WARPAGE IN A OVER-MOLDED IC PACKAGE	US	Pending	11171095	30-Jun-05		
SDK0696.001US	Hem Takiar, Shrikar Bhagath, Ken Jian Ming Wang	APPARATUS HAVING REDUCED WARPAGE IN AN OVER-MOLDED IC PACKAGE	US	Pending	11170883	30-Jun-05		
SDK0697.000US	Fabrice Jogand-Coulomb	Method for Program Code Execution	US	Published	11316008	22-Dec-05		
SDK0697.001US	Fabrice Jogand-Coulomb	System for Program Code Execution	US	Published	11315113	22-Dec-05		

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0698.000US	Yosi Pinto, Geoffrey S Gongwer, Oren Honen	Methods for Writing Non-Volatile Memories for Increased Endurance	US	Issued	11321217	28-Dec-05	7245556	17-Jul-07
SDK0699.000US	Chin-Tien Hope Chiu, Jack Chang Chien, Meng-Ju Tsai, Cheeman Yu, Hem Takiar	SEMICONDUCTOR DIE PACKAGE INCLUDING CONSTRUCTION FOR PREVENTING DELAMINATION AND/OR CRACKING OF THE SEMICONDUCTOR DIE	US	Pending	11265336	2-Nov-05		
SDK0700.001US	Bahman Qawami, Fabrice Jogand-Coulomb, Farshid Sabet-Sharghi, Michael Holtzman, Pascal Caillon, Pedro Vargas, Po Yuan, Robert C Chang, Patricia Dwyer, Paul McAvoy	Method for Secure Storage and Delivery of Media Content	US	Pending	11322812	30-Dec-05		
SDK0700.003US	Fabrice Jogand-Coulomb, Michael Holtzman	Method for Secure Storage and Delivery of Media Content	US	Published	12696985	29-Jan-10		
SDK0700.004US	Fabrice Jogand-Coulomb, Michael Holtzman, Paul McAvoy, Po Yuan, Robert C Chang	Method for Secure Storage and Delivery of Media Content	US	Published	12697001	29-Jan-10		
SDK0700.005US	Fabrice Jogand-Coulomb, Michael Holtzman, Paul McAvoy, Po Yuan, Robert C Chang	Method for Secure Storage and Delivery of Media Content	US	Published	12697034	29-Jan-10		
SDK0700.006US	Bahman Qawami, Fabrice Jogand-Coulomb, Farshid Sabet-Sharghi, Michael Holtzman, Pascal Caillon, Patricia Dwyer, Paul McAvoy, Pedro Vargas, Po Yuan, Robert C Chang	Method for Secure Storage and Delivery of Media Content	US	Published	12697055	29-Jan-10		
SDK0700.007US	Fabrice Jogand-Coulomb, Paul McAvoy, Po Yuan, Robert C Chang	Method for Secure Storage and Delivery of Media Content	US	Published	12697078	29-Jan-10		
SDK0701.000US	Jack H. Yuan	SELF ALIGNED TRENCH FILING FOR NARROW GAP ISOLATION REGIONS	US	Issued	11251386	14-Oct-05	7416956	26-Aug-08
SDK0701.001US	Jack H. Yuan	MEMORY WITH SELF ALIGNED TRENCHES FOR NARROW GAP ISOLATION REGIONS	US	Issued	11251400	14-Oct-05	7402886	22-Jul-08
SDK0704.000US	Paul McAvoy	NARROW GAP ISOLATION REGIONS	US	Pending	11618870	31-Dec-06		
SDK0704.001US	Paul McAvoy	Portable Multi-Platform Booting	US	Allowed	11618872	31-Dec-06		
SDK0705.000US	Sergey Anatolievich Gorobets, Peter John Smith, Alan Bennett	Non-Volatile Memory and Method With Multi-Stream Update Tracking	US	Issued	11192220	27-Jul-05	7366826	29-Apr-08
SDK0705.001US	Sergey Anatolievich Gorobets, Peter John Smith, Alan Bennett	Non-Volatile Memory and Method With Improved Indexing for Scratch Pad and Update Blocks	US	Issued	11192386	27-Jul-05	7386655	10-Jun-08
SDK0705.002US	Peter John Smith, Sergey Anatolievich Gorobets, Alan Bennett	Non-Volatile Memory and Method With Multi-Stream Updating	US	Issued	11191686	27-Jul-05	7412560	12-Aug-08
SDK0705.003US	Peter John Smith, Sergey Anatolievich Gorobets, Alan Bennett	Non-Volatile Memory and Method With Multi-Stream Updating	US	Published	12189396	11-Aug-08		
SDK0706.000US	Sergey Anatolievich Gorobets	Interfacing Systems Operating Through a Logical Address Space and on a Direct Data File Basis	US	Issued	11196869	3-Aug-05	7480766	20-Jan-09
SDK0707.000US	Vincent (Ming Hsun) Lee, Chih Chin Liao, Cheeman Yu, Hem Takiar	DIE PACKAGE WITH ASYMMETRIC LEADFRAME CONNECTION	US	Issued	11170897	30-Jun-05	7375415	20-May-08
SDK0707.001US	Vincent (Ming Hsun) Lee, Chih Chin Liao, Cheeman Yu, Hem Takiar	DIE PACKAGE WITH ASYMMETRIC LEADFRAME CONNECTION	US	Published	12060418	1-Apr-08		

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0709.001US	Yoram Cedar, Carlos J. Gonzalez	Reader for one time password generating device	US	Pending	11467070	24-Aug-06		
SDK0714.000US	Feng Pan	Voltage Regulation with Active Supplemental Current for Output Stabilization	US	Issued	11303569	16-Dec-05	7372320	13-May-08
SDK0715.000US	Hem Takiar, Ken Jian Ming Wang, Chih Chin Liao, Joyce Chen	SUBSTRATE HAVING MINIMUM KERF WIDTH	US	Published	11301715	13-Dec-05		
SDK0716.000US	Chienman Yu, Ken Jian Ming Wang, Chin-Tien Hope Chiu, Chih Chin Liao, Han-Shiao Joyce Chen	SUBSTRATE WARPAGE CONTROL AND CONTINUOUS ELECTRICAL ENHANCEMENT	US	Issued	11171819	30-Jun-05	7538438	26-May-09
SDK0723.000US	Joerg Ferchau	Method for Managing Appliances	US	Issued	11292130	1-Dec-05	7353073	1-Apr-08
SDK0723.001US	Joerg Ferchau	System for Managing Appliances	US	Issued	11292132	1-Dec-05	7739078	15-Jun-10
SDK0724.001US	Carlos J. Gonzalez, Joerg Ferchau, Fabrice Jogand-Coulomb	Method Used in a Mass Storage Device With Automated Credentials Loading	US	Issued	11319259	27-Dec-05	7743409	22-Jun-10
SDK0724.002US	Carlos J. Gonzalez, Joerg Ferchau, Fabrice Jogand-Coulomb	Mass Storage Device With Automated Credentials Loading	US	Issued	11319835	27-Dec-05	7748031	29-Jun-10
SDK0724.003US	Carlos J. Gonzalez, Fabrice Jogand-Coulomb, Joerg Ferchau	Mass Storage Device With Automated Credentials Loading	US	Pending	12714228	26-Feb-10		
SDK0726.001US	Michael Holtzman, Hagai Bar-Ei, Baruch Boris Cohen, David Deitcher, Aviram Veruchami	In Stream Data Encryption / Decryption Method	US	Published	11314030	20-Dec-05		
SDK0727.000US	Chih Chin Liao, Ken Jian Ming Wang, Joyce Chen, Chin-Tien Hope Chiu, Jack Chang Chien, Shrikar Bhagath, Cheeman Yu, Hem Takiar	PADLESS SUBSTRATE FOR SURFACE MOUNTED COMPONENTS	US	Published	11280419	16-Nov-05		
SDK0729.001US	Sergey Anatolievich Gorobets	Error Correction Coding for Multiple-Sector Pages in Flash Memory Devices	US	Issued	11383844	17-May-06	7809994	5-Oct-10
SDK0730.000US	Edwin J Cuellar, Ning Liu, Robert C Miller	Memory Device With Latching Cap for USB Plug	US	Issued	11273729	14-Nov-05	7341464	11-Mar-08
SDK0731.000US	Hem Takiar, Cheeman Yu, Ken Jian Ming Wang, Chin-Tien Hope Chiu, Han-Shiao Joyce Chen, Chih Chin Liao	ROUNDED CONTACT FINGERS ON SUBSTRATE/PCB FOR CRACK PREVENTION	US	Issued	11321931	29-Dec-05	7806731	5-Oct-10
SDK0731.001US	Hem Takiar, Cheeman Yu, Ken Jian Ming Wang, Chin-Tien Hope Chiu, Han-Shiao Joyce Chen, Chih Chin Liao	HIDDEN PLATING TRACES	US	Issued	11321929	29-Dec-05	7592699	22-Sep-09
SDK0731.002US	Hem Takiar, Cheeman Yu, Ken Jian Ming Wang, Chin-Tien Hope Chiu, Han-Shiao Joyce Chen, Chih Chin Liao	HIDDEN PLATING TRACES	US	Pending	12493582	29-Jun-09		
SDK0733.002US	Michael Holtzman, Rony Shapiro, Hagai Bar-Ei, Ronen Greenspan	Methods Used in a Secure Yet Flexible System Architecture for Secure Devices With Flash Mass Storage Memory	US	Allowed	11317341	22-Dec-05		
SDK0733.004US	Michael Holtzman, Hagai Bar-Ei, Ronen Greenspan, Rony Shapiro	Secure Yet Flexible System Architecture for Secure Devices With Flash Mass Storage Memory	US	Published	12122412	16-May-08		
SDK0735.001US	Carlos J. Gonzalez, Edwin J Cuellar, Susan Cannon	System and Method for Displaying Advertising Using Flash Memory Storage Devices	US	Published	11314844	21-Dec-05		
SDK0735.002US	Carlos J. Gonzalez, Edwin J Cuellar, Susan Cannon	System and Method for Displaying Advertising Using Flash Memory Storage Devices	US	Published	11313568	21-Dec-05		
SDK0736.000US	Kevin Conley	Voice Controlled Portable Memory Storage Device	US	Published	11314476	21-Dec-05		

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0736.001US	Kevin Conley	Voice Controlled Portable Memory Storage Device	US	Published	11313841	21-Dec-05		
SDK0737.000US	Kevin Conley	Voice Controlled Portable Memory Storage Device	US	Issued	11314522	21-Dec-05	7917949	29-Mar-11
SDK0737.001US	Kevin Conley	Voice Controlled Portable Memory Storage Device	US	Published	11314933	21-Dec-05		
SDK0738.000US	Gerrit Jan Hemink	Verify Operation for Non-Volatile Storage Using Different Voltages	US	Issued	11421667	1-Jun-06	7440331	21-Oct-08
SDK0738.001US	Gerrit Jan Hemink	Systems for Verifying Non-Volatile Storage Using Different Voltages	US	Issued	11421682	1-Jun-06	7457163	25-Nov-08
SDK0738.002US	Gerrit Jan Hemink	System for Verifying Non-Volatile Storage Using Different Voltages	US	Issued	12203544	3-Sep-08	7843739	30-Nov-10
SDK0738.003US	Gerrit Jan Hemink	System for Verifying Non-Volatile Storage Using Different Voltages	US	Published	12910361	22-Oct-10		
SDK0739.001US	Michael Holtzman, Fabrice Jogand-Coulomb	Methods of Upgrading a Memory Card that has Security Mechanisms that prevent copying of Secure Content and Applications	US	Published	11765583	20-Jun-07		
SDK0740.000US	Yishai Kagan, Rizwan Ahmed, Farookh Moogat	Method of High-Performance Flash Memory Data Transfer	US	Issued	11379895	24-Apr-06	7366028	29-Apr-08
SDK0740.001US	Yishai Kagan, Rizwan Ahmed, Farookh Moogat	High-Performance Flash Memory Data Transfer	US	Issued	11379910	24-Apr-06	7345926	18-Mar-08
SDK0741.000US	Jun Wan, Jeffrey Lutze, Jian Chen, Yan Li, Alexander Mak	Retention Margin Program Verification	US	Issued	11617546	28-Dec-06	7652918	26-Jan-10
SDK0741.001US	Jun Wan, Jeffrey Lutze, Jian Chen, Yan Li, Alexander Mak	Retention Margin Program Verification	US	Issued	11617541	28-Dec-06	7616499	10-Nov-09
SDK0742.000US	Teruhiko Kamei	Programming Differently Sized Margins and Sensing with Compensations at Select States for Improved Read Operation in Non-Volatile Memory	US	Issued	11425111	19-Jun-06	7606084	20-Oct-09
SDK0742.001US	Teruhiko Kamei	Systems for Programming Differently Sized Margins and Sensing with Compensation at Select States for Improved Read Operation in Non-Volatile Memory	US	Issued	11425116	19-Jun-06	7352628	1-Apr-08
SDK0742.002US	Teruhiko Kamei	Systems for Programming Differently Sized Margins and Sensing with Compensations at Select States for Improved Read Operation in Non-Volatile Memory	US	Issued	12038407	27-Feb-08	7480176	20-Jan-09
SDK0742.003US	Teruhiko Kamei	Systems for Programming Differently Sized Margins and Sensing with Compensations at Select States for Improved Read Operation in Non-Volatile Memory	US	Issued	12038421	27-Feb-08	7602652	13-Oct-09
SDK0743.000US	Teruhiko Kamei	READING NON-VOLATILE STORAGE WITH EFFICIENT SETUP	US	Issued	11305588	16-Dec-05	7545675	9-Jun-09
SDK0743.001US	Teruhiko Kamei	SYSTEM FOR READING NON-VOLATILE STORAGE WITH EFFICIENT SETUP	US	Issued	11303193	16-Dec-05	7369437	6-May-08
SDK0743.002US	Teruhiko Kamei	READING NON-VOLATILE STORAGE WITH EFFICIENT SETUP	US	Issued	12435127	4-May-09	7733701	8-Jun-10
SDK0746.000US	Dan Harkabi, Gidon Elazar, Nehemiah Weingarten, Josef Guy Hefetz, Elisha Avraham Tal	Memory Package With Removable Cap	US	Issued	29244951	14-Dec-05	D534167	26-Dec-06

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0747.000US	Yupin Fong, Jun Wan, Jeffrey Lutz	REDUCING READ DISTURB FOR NON-VOLATILE STORAGE	US	Issued	11295776	6-Dec-05	7349258	25-Mar-08
SDK0747.001US	Yupin Fong, Jun Wan, Jeffrey Lutz	SYSTEM FOR REDUCING READ DISTURB FOR NON-VOLATILE STORAGE	US	Issued	11296087	6-Dec-05	7262994	28-Aug-07
SDK0747.002US	Yupin Fong, Jun Wan, Jeffrey Lutz	SYSTEM FOR REDUCING READ DISTURB FOR NON-VOLATILE STORAGE	US	Issued	11681188	2-Mar-07	7295473	13-Nov-07
SDK0747.003US	Yupin Fong, Jun Wan, Jeffrey Lutz	REDUCING READ DISTURB FOR NON-VOLATILE STORAGE	US	Issued	12021729	29-Jan-08	7440318	21-Oct-08
SDK0747.004US	Yupin Fong, Jun Wan, Jeffrey Lutz	REDUCING READ DISTURB FOR NON-VOLATILE STORAGE	US	Issued	12021741	29-Jan-08	7447065	4-Nov-08
SDK0747.005US	Yupin Fong, Jun Wan, Jeffrey Lutz	REDUCING READ DISTURB FOR NON-VOLATILE STORAGE	US	Issued	12021761	29-Jan-08	7495956	24-Feb-09
SDK0750.000US	Loc Tu, Jeffrey Lutz, Jun Wan, Jian Chen	Trimming of Analog Voltage in Flash Memory Devices	US	Issued	11331479	12-Jan-06	7457178	25-Nov-08
SDK0750.001US	Loc Tu, Jeffrey Lutz, Jun Wan, Jian Chen	Flash Memory Devices with Trimmed Analog Voltages	US	Issued	11332567	12-Jan-06	7254071	7-Aug-07
SDK0752.000US	Farookh Moogat, Raul-Adrian Cernea, Shouchang Tsao, Tomoharu Tanaka	Method for column redundancy using data lathes in solid-state memories	US	Issued	11389655	24-Mar-06	7394690	1-Jul-08
SDK0752.001US	Farookh Moogat, Raul-Adrian Cernea, Shouchang Tsao, Tomoharu Tanaka	Non-Volatile Memory with Redundancy Data Buffered in Data Latches for Defective Locations	US	Issued	11388408	24-Mar-06	7224605	29-May-07
SDK0752.002US	Farookh Moogat, Raul-Adrian Cernea, Shouchang Tsao, Tomoharu Tanaka	Method for column redundancy using data lathes in solid-state memories	US	Issued	12163017	27-Jun-08	7663950	16-Feb-10
SDK0753.000US	Yan Li, Fanglein Zhang, Toru Miwa, Farookh Moogat	Variable program voltage increment values in non-volatile memory program operations	US	Issued	11548264	10-Oct-06	7474561	6-Jan-09
SDK0753.001US	Yan Li, Fanglein Zhang, Toru Miwa, Farookh Moogat	Systems utilizing variable program voltage increment values in non-volatile memory program operations	US	Issued	11548267	10-Oct-06	7450426	11-Nov-08
SDK0754.000US	Vincent (Ming Hsun) Lee, Cheeman Yu, Hem Takiar	COL-TSOP WITH NON-CONDUCTIVE MATERIAL FOR REDUCING PACKAGE CAPACITANCE	US	Issued	11354441	15-Feb-06	7728411	1-Jun-10
SDK0755.000US	Joerg Ferchau, Carlos J. Gonzalez	U3 Adapter	US	Issued	11408714	21-Apr-06	7447821	4-Nov-08
SDK0755.001US	Joerg Ferchau, Carlos J. Gonzalez	Method for U3 Adapter	US	Issued	11408553	21-Apr-06	7516261	7-Apr-09
SDK0756.000US	Hem Takiar, Warren Middlekauf, Robert C Miller	SIP Module with a Single Sided Lid	US	Issued	11356279	15-Feb-06	7193161	20-Mar-07
SDK0757.000US	Fabrice Jogand-Coulomb	Personal License Server and Methods for Use Thereof	US	Published	12070689	19-Feb-08		
SDK0758.000US	Henry Chien, George Matamis, Tuan D. Pham, Masaaki Higashitani, Hidetaka Horiuchi, Jeffrey Lutz, Nima Mokhlesi, Yupin Fong	Methods of reducing coupling between floating gates in non-volatile memory	US	Issued	11534135	21-Sep-06	7615445	10-Nov-09
SDK0758.002US	Henry Chien, George Matamis, Tuan D. Pham, Masaaki Higashitani, Hidetaka Horiuchi, Jeffrey Lutz, Nima Mokhlesi, Yupin Fong	Method of reducing coupling between floating gates in non-volatile memory	US	Issued	12567257	25-Sep-09	7910434	22-Mar-11
SDK0758.003US	Henry Chien, George Matamis, Tuan D. Pham, Masaaki Higashitani, Hidetaka Horiuchi, Jeffrey Lutz, Nima Mokhlesi, Yupin Fong	Method of reducing coupling between floating gates in non-volatile memory	US	Pending	13010563	20-Jan-11		

**PATENT**

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0761.001US	Nima Mokhlesi	SYSTEM FOR PERFORMING READ OPERATION ON NON-VOLATILE STORAGE WITH COMPENSATION FOR COUPLING	US	Issued	11377972	17-Mar-06	7436733	14-Oct-08
SDK0761.002US	Nima Mokhlesi	READ OPERATION FOR NON-VOLATILE STORAGE WITH COMPENSATION FOR COUPLING	US	Issued	11384057	17-Mar-06	7499319	3-Mar-09
SDK0761.003US	Nima Mokhlesi	READ OPERATION FOR NON-VOLATILE STORAGE WITH COMPENSATION FOR COUPLING	US	Issued	12357364	21-Jan-09	7613068	3-Nov-09
SDK0761.004US	Nima Mokhlesi	READ OPERATION FOR NON-VOLATILE STORAGE WITH COMPENSATION FOR COUPLING	US	Issued	12357368	21-Jan-09	7778106	17-Aug-10
SDK0761.005US	Nima Mokhlesi	READ OPERATION FOR NON-VOLATILE STORAGE WITH COMPENSATION FOR COUPLING	US	Issued	12622966	20-Nov-09	7911838	22-Mar-11
SDK0761.006US	Nima Mokhlesi	READ OPERATION FOR NON-VOLATILE STORAGE WITH COMPENSATION FOR COUPLING	US	Pending	13028674	16-Feb-11		
SDK0762.000US	Che-Chung (Michael) Chang, Chin-Tien Hope Chiu, Cheeman Yu, Hem Takiar, Jack Chang Chien, Ning Liu	METHODS OF PROMOTING ADHESION BETWEEN TRANSFER MOLDED IC PACKAGES AND INJECTION MOLDED PLASTICS FOR CREATING OVER-MOLDED MEMORY CARDS	US	Issued	11373941	13-Mar-06	7615861	10-Nov-09
SDK0762.001US	Che-Chung (Michael) Chang, Chin-Tien Hope Chiu, Cheeman Yu, Hem Takiar, Jack Chang Chien, Ning Liu	METHODS OF PROMOTING ADHESION BETWEEN TRANSFER MOLDED IC PACKAGES AND INJECTION MOLDED PLASTICS FOR CREATING OVER-MOLDED MEMORY CARDS	US	Published	12614945	9-Nov-09		
SDK0762.002US	Che-Chung (Michael) Chang, Chin-Tien Hope Chiu, Cheeman Yu, Hem Takiar, Jack Chang Chien, Ning Liu	METHODS OF PROMOTING ADHESION BETWEEN TRANSFER MOLDED IC PACKAGES AND INJECTION MOLDED PLASTICS FOR CREATING OVER-MOLDED MEMORY CARDS	US	Published	12614960	9-Nov-09		
SDK0763.000US	Siu Lung Chan, Raul-Adrian Cernea	Method For Compensated Sensing In Non-Volatile Memory	US	Issued	11321681	28-Dec-05	7324393	29-Jan-08
SDK0763.001US	Raul-Adrian Cernea, Siu Lung Chan	Reference Sense Amplifier For Non-Volatile Memory	US	Issued	11321953	28-Dec-05	7327619	5-Feb-08
SDK0763.002US	Siu Lung Chan, Raul-Adrian Cernea	Method For Compensated Sensing In Non-Volatile Memory	US	Issued	12020449	25-Jan-08	7593277	22-Sep-09
SDK0763.003US	Raul-Adrian Cernea	NON-VOLATILE MEMORY WITH IMPROVED SENSING BY REDUCING SOURCE LINE CURRENT	US	Issued	12507752	22-Jul-09	7852678	14-Dec-10
SDK0763.004US	Raul-Adrian Cernea	NON-VOLATILE MEMORY WITH IMPROVED SENSING BY REDUCING SOURCE LINE CURRENT	US	Pending	12965761	10-Dec-10		
SDK0764.000US	Shih-Chung Lee, Toru Miwa	Program Time Adjustment As Function of Program Voltage for Improved Programming Speed in Programming Method	US	Issued	11392265	28-Mar-06	7327608	5-Feb-08
SDK0764.001US	Shih-Chung Lee, Toru Miwa	Program Time Adjustment As Function of Program Voltage for Improved Programming Speed in Memory System	US	Issued	11391811	28-Mar-06	7330373	12-Feb-08
SDK0764.002US	Shih-Chung Lee, Toru Miwa	Program Time Adjustment As Function of Program Voltage for Improved Programming Speed in Memory System	US	Issued	12019422	24-Jan-08	7675780	9-Mar-10
SDK0766.000US	Gerrit Jan Hemink	Method of Partial Page Fail Bit Detection in Flash Memory Devices	US	Issued	11428083	30-Jun-06	7304893	4-Dec-07
SDK0766.001US	Gerrit Jan Hemink	Partial Page Fail Bit Detection in Flash Memory Devices	US	Issued	11428111	30-Jun-06	7355892	8-Apr-08
SDK0767.000US	Masaaki Higashitani	NAND flash memory with boosting	US	Issued	11381865	5-May-06	7436709	14-Oct-08



FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0767.001US	Masaaki Higashitani	Boosting methods for NAND flash memory	US	Issued	11381874	5-May-06	7286408	23-Oct-07
SDK0768.000US	Chih Chin Liao, Han-Shiao Joyce Chen, Chin-Tien Hope Chiu, Ken Jian Ming Wang, Cheeman Yu, Hem Takkar	Printed Circuit Board with Coextensive Electrical Connectors and Contact Pad Areas	US	Issued	11449493	8-Jun-06	7746661	29-Jun-10
SDK0768.001US	Chih Chin Liao, Han-Shiao Joyce Chen, Chin-Tien Hope Chiu, Ken Jian Ming Wang, Cheeman Yu, Hem Takkar	Printed Circuit Board with Coextensive Electrical Connectors and Contact Pad Areas	US	Published	12819840	21-Jun-10		
SDK0769.000US	Chih Chin Liao, Han-Shiao Joyce Chen, Cheeman Yu, Hem Takkar, Chin-Tien Hope Chiu	Semiconductor Device with a Distributed Plating Pattern	US	Published	11435518	17-May-06		
SDK0772.000US	Yan Li	Method for Non-Volatile Memory with Background Data Latch Caching During Program Operations	US	Issued	11381995	5-May-06	7502260	10-Mar-09
SDK0772.001US	Yan Li	Non-volatile memory with background data latch caching during program operations	US	Issued	11382006	5-May-06	7505320	17-Mar-09
SDK0773.000US	Jason Lin, Yan Li	Method for non-volatile memory with background data latch caching during erase operations	US	Issued	11382001	5-May-06	7619922	17-Nov-09
SDK0773.001US	Jason Lin, Yan Li	Non-volatile memory with background data latch caching during erase operations	US	Issued	11381998	5-May-06	7609552	27-Oct-09
SDK0775.000US	Ken Oowada	Self-Boosting Method With Suppression of High Lateral Electric Fields	US	Issued	11394460	30-Mar-06	7428165	23-Sep-08
SDK0775.001US	Ken Oowada	Self-Boosting System With Suppression Of High Lateral Electric Fields	US	Issued	11394803	30-Mar-06	7511995	31-Mar-09
SDK0775.002US	Ken Oowada	Self-Boosting System With Suppression Of High Lateral Electric Fields	US	Issued	12372636	17-Feb-09	7864570	4-Jan-11
SDK0776.000US	Eran Shen, Reuven Elhamias	Secure storage digital kiosk distribution methods	US	Published	11532420	15-Sep-06		
SDK0776.001US	Eran Shen, Reuven Elhamias	Secure storage digital kiosk distribution methods	US	Published	11532431	15-Sep-06		
SDK0777.000US	Eran Shen	Media with Pluggable Codec	US	Published	11382184	8-May-06		
SDK0777.001US	Eran Shen	Media with Pluggable Codec Methods	US	Published	11382189	8-May-06		
SDK0781.000US	Toru Miwa	Circuitry and Device for Generating and Adjusting Selected Word Line Voltage	US	Issued	11409164	21-Apr-06	7269092	11-Sep-07
SDK0781.001US	Toru Miwa	Method for Generating and Adjusting Selected Word Line Voltage	US	Issued	11408545	21-Apr-06	7518930	14-Apr-09
SDK0783.000US	Kevin Conley	Convolutional Coding Methods for Nonvolatile Memory	US	Issued	11383401	15-May-06	7840875	23-Nov-10
SDK0785.000US	Fumitoshi Ito	Method for partitioned erase and erase verification to compensate for capacitive coupling effects in non-volatile memory	US	Issued	11549515	13-Oct-06	7495954	24-Feb-09
SDK0785.001US	Fumitoshi Ito	System for Partitioned Erase and Erase Verification in Non-Volatile Memory To Compensate For Capacitive Coupling	US	Issued	11549533	13-Oct-06	7499317	3-Mar-09
SDK0785.002US	Fumitoshi Ito	Partitioned Soft Programming in Non-Volatile Memory	US	Issued	11549553	13-Oct-06	7499338	3-Mar-09
SDK0785.003US	Fumitoshi Ito	Systems for Partitioned Soft Programming in Non-Volatile Memory	US	Issued	11549564	13-Oct-06	7535766	19-May-09

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0785.004US	Fumitoshi Ito	Methods for partitioned erase and erase verification in non-volatile memory to compensate for capacitive coupling effects	US	Issued	12358633	23-Jan-09	7768826	3-Aug-10
SDK0788.000US	Nima Mokhlesi	Method for Increasing Programming Speed for Non-Volatile Memory by Applying Counter-Transitioning Waveforms to Word Lines	US	Issued	11425024	19-Jun-06	7349261	25-Mar-08
SDK0788.001US	Nima Mokhlesi	System for Increasing Programming Speed for Non-Volatile Memory by Applying Counter-Transitioning Waveforms to Word Lines	US	Issued	11425045	19-Jun-06	7492633	17-Feb-09
SDK0788.002US	Nima Mokhlesi	Method for Increasing Programming Speed for Non-Volatile Memory by Applying Direct-Transitioning Waveforms to Word Lines	US	Issued	12018127	22-Jan-08	7468921	23-Dec-08
SDK0790.000US	Gerrit Jan Hemink	CONTROLLED BOOSTING IN NON-VOLATILE MEMORY SOFT PROGRAMMING	US	Issued	11560744	16-Nov-06	7535763	19-May-09
SDK0790.001US	Gerrit Jan Hemink	SYSTEMS FOR CONTROLLED BOOSTING IN NON-VOLATILE MEMORY SOFT PROGRAMMING	US	Issued	11560751	16-Nov-06	7697338	13-Apr-10
SDK0790.002US	Gerrit Jan Hemink	Controlled Boosting in Non-Volatile Memory Soft Programming	US	Issued	12757833	9-Apr-10	7911849	22-Mar-11
SDK0791.000US	Nima Mokhlesi, Yingda Dong	Data Pattern Sensitivity Compensation using Different Voltage	US	Issued	11421871	2-Jun-06	7450421	11-Nov-08
SDK0791.001US	Nima Mokhlesi, Yingda Dong	System for Performing Data Pattern Sensitivity Compensation Using Different Voltage	US	Issued	11421884	2-Jun-06	7310272	18-Dec-07
SDK0791.002US	Nima Mokhlesi, Yingda Dong	System for Performing Data Pattern Sensitivity Compensation Using Different Voltage	US	Issued	11933632	1-Nov-07	7561473	14-Jul-09
SDK0792.000US	Alan Welsh Sinclair, Barry Wright	Management of Memory Blocks that Directly Store Data Files	US	Pending	11382224	8-May-06		
SDK0792.001US	Alan Welsh Sinclair, Barry Wright	Memory System with Management of Memory Blocks that Directly Store Data Files	US	Issued	11382228	8-May-06	7581057	25-Aug-09
SDK0793.001US	Alan Welsh Sinclair, Barry Wright	Indexing of File Data in Reprogrammable Non-Volatile Memory Systems that Directly Store Data Files	US	Allowed	11459255	21-Jul-06		
SDK0793.002US	Alan Welsh Sinclair, Barry Wright	Reprogrammable Non-Volatile Memory Systems with Indexing of Directly Stored Data Files	US	Issued	11459246	21-Jul-06	7669003	23-Feb-10
SDK0800.000US	Teruhiko Kamei	Method and System for Programming Multi-State Non-Volatile Memory Devices	US	Issued	11440511	25-May-06	7518911	14-Apr-09
SDK0801.000US	Teruhiko Kamei	Method and System for Flash Memory Devices	US	Issued	11407816	20-Apr-06	7440322	21-Oct-08
SDK0807.001US	Carlos J. Gonzalez, Edwin J Cuellar, Robert C. Miller, Ron LaPedis	Credit card sized USB flash drive	US	Issued	11536886	29-Sep-06	7344072	18-Mar-08
SDK0807.002US	Carlos J. Gonzalez, Edwin J Cuellar, Joel Jacobs, Robert C Miller, Ron LaPedis	Credit card sized USB flash drive	US	Issued	11953737	10-Dec-07	7537169	26-May-09
SDK0811.000US	Alan Welsh Sinclair, Barry Wright	Reclaiming data Storage Capacity in Flash Memories	US	Issued	11382232	8-May-06	7450420	11-Nov-08
SDK0811.001US	Alan Welsh Sinclair, Barry Wright	Reclaiming data Storage Capacity in Flash Memories Systems	US	Issued	11382235	8-May-06	7558905	7-Jul-09
SDK0814.000US	Prajit Nandi	Method and system for independent control of voltage and its temperature co-efficient in non-volatile memory devices	US	Issued	11499067	4-Aug-06	7436724	14-Oct-08

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0815.000US	Fabrice Jogand-Coulomb, Oktay Rasizade, Haluk Kent, Tanik	Method for Allowing Multiple Users to Access Preview Content	US	Pending	11599994	14-Nov-06		
SDK0815.001US	Fabrice Jogand-Coulomb, Haluk Kent Tanik, Oktay Rasizade	Method for Allowing Content Protected by a First DRM System to be Accessed by a Second DRM System	US	Pending	11600005	14-Nov-06		
SDK0815.002US	Fabrice Jogand-Coulomb, Pascal Caillon, Benjamin Vigier	Method for Connecting to a Network Location Associated with Content	US	Pending	11600300	14-Nov-06		
SDK0815.006US	Fabrice Jogand-Coulomb, Haluk Kent Tanik, Oktay Rasizade	Methods for Linking Content with License	US	Published	11599655	14-Nov-06		
SDK0815.008US	Fabrice Jogand-Coulomb, Haluk Kent Tanik, Oktay Rasizade	Methods for Accessing Content Based on a Session Ticket	US	Published	11600263	14-Nov-06		
SDK0815.011US	Fabrice Jogand-Coulomb, Farshid Sabet-Sharghi, Bahman Oawami	Apparatuses for Binding Content to a Separate Memory Device	US	Published	11600245	14-Nov-06		
SDK0816.000US	Feng Pan	Hybrid Charge Pump Regulation	US	Issued	11497465	31-Jul-06	7554311	30-Jun-09
SDK0817.000US	Feng Pan	Unified Voltage Generation Method with Improved Power Efficiency	US	Issued	11618522	29-Dec-06	7440342	21-Oct-08
SDK0817.001US	Feng Pan	Unified Voltage Generation Apparatus with Improved Power Efficiency	US	Issued	11618539	29-Dec-06	7477092	13-Jan-09
SDK0819.000US	Yan Li	Method for non-volatile memory with background with data latch caching during read operations	US	Issued	11381994	5-May-06	7447078	4-Nov-08
SDK0819.001US	Yan Li	Non-volatile memory with background data latch caching during read operations	US	Issued	11381997	5-May-06	7480181	20-Jan-09
SDK0819.002US	Yan Li	Method for non-volatile memory with background with data latch caching during read operations	US	Issued	12263658	3-Nov-08	7733703	8-Jun-10
SDK0819.003US	Yan Li	Method for non-volatile memory with background with data latch caching during read operations	US	Published	12782503	18-May-10		
SDK0823.004US	Raul-Adrian Cernea	Non-Volatile Memory with Redundancy Data Buffered in Remote Buffer Circuits	US	Pending	13046472	11-Mar-11		
SDK0825.000US	Reuven Elhamias, Ron Barzilai, Vivek Mani, Simon Stoler, David Zehavi	Portable Module Interface with Timeout Prevention by Dummy Blocks	US	Issued	11566685	4-Dec-06	7917719	29-Mar-11
SDK0826.000US	Nima Mokhlesi	Methods of forming non-volatile memories with shaped floating gates	US	Issued	11465038	16-Aug-06	7494860	24-Feb-09
SDK0826.001US	Nima Mokhlesi	Non-volatile memories with shaped floating gates	US	Issued	11465025	16-Aug-06	7755132	13-Jul-10
SDK0827.000US	Nima Mokhlesi, Dengtao Zhao	Method for Operating Non-Volatile Memory Using Temperature Compensation of voltages of Unselected Word Lines and Select Gates	US	Issued	11424800	16-Jun-06	7391650	24-Jun-08
SDK0827.001US	Nima Mokhlesi, Dengtao Zhao	System for Operating Non-Volatile Memory using Temperature Compensation of Voltages of Unselected Word Lines and Select Gates	US	Issued	11424812	16-Jun-06	7342831	11-Mar-08
SDK0827.002US	Nima Mokhlesi, Dengtao Zhao	Temperature Compensation of voltages of Unselected Word Lines in Non-Volatile Memory based on Word Line Position	US	Issued	11958524	18-Dec-07	7460407	2-Dec-08
SDK0827.003US	Nima Mokhlesi, Dengtao Zhao	Temperature Compensation of Select Gates in Non-Volatile Memory	US	Issued	11958534	18-Dec-07	7463528	9-Dec-08
SDK0830.000US	Nima Mokhlesi, Masaaki Higashitani	Non-Volatile Memory with Dual Voltage Select Gate Structure	US	Issued	11550382	17-Oct-06	7586157	8-Sep-09

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0830.001US	Nima Mokhlesi, Masaaki Higashitani	Programming Non-Volatile Memory with Dual Voltage Select Gate Structure	US	Issued	11550383	17-Oct-06	7616490	10-Nov-09
SDK0830.002US	Nima Mokhlesi, Masaaki Higashitani	Fabricating Non-Volatile Memory with Dual Voltage Select Gate Structure	US	Issued	11550386	17-Oct-06	7691710	6-Apr-10
SDK0832.000US	Raul-Adrian Cernea	Method For Remote Redundancy For Non-Volatile Memory	US	Issued	11388579	24-Mar-06	7352635	1-Apr-08
SDK0832.001US	Raul-Adrian Cernea	Non-Volatile Memory with Redundancy Data Buffered in Remote Buffer Circuits	US	Issued	11389411	24-Mar-06	7324389	29-Jan-08
SDK0832.002US	Raul-Adrian Cernea	Non-Volatile Memory with Redundancy Data Buffered in Remote Buffer Circuits	US	Issued	12019564	24-Jan-08	7567466	28-Jul-09
SDK0832.003US	Raul-Adrian Cernea	Non-Volatile Memory with Redundancy Data Buffered in Remote Buffer Circuits	US	Issued	12499757	8-Jul-09	7907458	15-Mar-11
SDK0833.000US	Chin-Tien Hope Chiu, Hem Takiar, Cheeman Yu, Hui Liu, Jack Chang Chien, Java Zhu	Molded SIP Package with Reinforced Solder Columns	US	Published	11414526	28-Apr-06		
SDK0833.001US	Chin-Tien Hope Chiu, Hem Takiar, Hui Liu, Java Zhu, Jack Chang Chien, Cheeman Yu	Method of Reducing Mechanical Stress on a Semiconductor Die During Fabrication	US	Issued	11414780	28-Apr-06	7435624	14-Oct-08
SDK0835.000US	Prashanti Govindu, Feng Pan, Trung Pham, Chi-Ming Wang, Gyuwan Kwon, Man Mui	Implementation of Output Floating Scheme for HV Charge Pumps	US	Issued	11523875	19-Sep-06	7368979	6-May-08
SDK0836.001US	Gerrit Jan Hemink	Reducing the Impact of Program Disturb During Read	US	Issued	11413951	28-Apr-06	7515463	7-Apr-09
SDK0836.002US	Gerrit Jan Hemink	Apparatus for Reducing the Impact of Program Disturb	US	Issued	11413683	28-Apr-06	7499326	3-Mar-09
SDK0836.003US	Gerrit Jan Hemink	Apparatus for Reducing the Impact of Program Disturb During Read	US	Issued	11413671	28-Apr-06	7426137	16-Sep-08
SDK0836.004US	Gerrit Jan Hemink	Reducing the Impact of Program Disturb	US	Issued	11414758	28-Apr-06	7436713	14-Oct-08
SDK0836.005US	Gerrit Jan Hemink	Apparatus for Reducing the Impact of Program Disturb	US	Issued	12365648	4-Feb-09	7796433	14-Sep-10
SDK0836.006US	Gerrit Jan Hemink	Apparatus for Reducing the Impact of Program Disturb	US	Issued	12725446	16-Mar-10	7911846	22-Mar-11
SDK0836.007US	Gerrit Jan Hemink	Apparatus for Reducing the Impact of Program Disturb	US	Pending	13025123	10-Feb-11		
SDK0836.008US	Gerrit Jan Hemink	Apparatus for Reducing the Impact of Program Disturb	US	Pending	13025126	10-Feb-11		
SDK0838.000US	Nima Mokhlesi	Scaled Dielectric Enabled By Stack Side Wall Process	US	Issued	11426560	26-Jun-06	7799637	21-Sep-10
SDK0840.000US	Yan Li	Method for non-volatile memory with managed execution of cached data	US	Issued	11381996	5-May-06	7463521	9-Dec-08
SDK0840.001US	Yan Li	Non-volatile memory with managed execution of cached data	US	Issued	11382000	5-May-06	7486558	3-Feb-09
SDK0841.000US	Fabrice Jogand-Coulomb, Robert C Chang	Method for interfacing with a memory card to access a program instruction	US	Published	11509978	25-Aug-06		
SDK0842.000US	Alan Bennett, Neil Hutchinson, Sergey Anatolievich Gorobets	Method for non-volatile memory with Worst Case control data management	US	Published	11549035	12-Oct-06		
SDK0842.001US	Alan Bennett, Neil Hutchinson, Sergey Anatolievich Gorobets	Non-Volatile memory with worst-case control data management	US	Published	11549040	12-Oct-06		
SDK0843.000US	Jason Lin	Managing A Pool Of Update Memory Blocks Based On Each Block's Activity And Data Order	US	Issued	11532456	15-Sep-06	7779056	17-Aug-10

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0843.001US	Jason Lin	Non-Volatile Memory With Management Of A Pool Of Update Memory Blocks Based On Each Block's Activity And Data Order	US	Issued	11532467	15-Sep-06	7774392	10-Aug-10
SDK0844.000US	Shai Traister, Jason Lin	Methods for phased garbage collection	US	Issued	11499606	4-Aug-06	7444461	28-Oct-08
SDK0844.001US	Shai Traister, Jason Lin	Non-volatile memory storage systems for phased garbage collection	US	Issued	11499598	4-Aug-06	7451265	11-Nov-08
SDK0844.002US	Shai Traister, Jason Lin	Methods for phased garbage collection using phased garbage collection block or scratch pad block as a buffer	US	Issued	11541035	28-Sep-06	7444462	28-Oct-08
SDK0844.003US	Shai Traister, Jason Lin	Methods for phased garbage collection using phased garbage collection block or scratch pad block as a buffer	US	Issued	11541012	28-Sep-06	7441071	21-Oct-08
SDK0846.000US	Nima Mokhlesi	Alternating Read Mode	US	Issued	11618569	29-Dec-06	7495962	24-Feb-09
SDK0846.001US	Nima Mokhlesi	Apparatus for Alternating Read Mode	US	Issued	11618578	29-Dec-06	7440324	21-Oct-08
SDK0847.000US	Cheeman Yu, Chih Chin Liao, Hem Takiar	Stacked, Interconnected Semiconductor Packages	US	Issued	11427689	29-Jun-06	7550834	23-Jun-09
SDK0847.001US	Cheeman Yu, Chih Chin Liao, Hem Takiar	Method of Stacking and Interconnecting Semiconductor Packages Via Electrical Connectors Extending Between Adjoining Semiconductor Packages	US	Issued	11427695	29-Jun-06	7615409	10-Nov-09
SDK0847.002US	Cheeman Yu, Chih Chin Liao, Hem Takiar	Stacked, Interconnected Semiconductor Packages	US	Pending	12489298	22-Jun-09		
SDK0847.003US	Cheeman Yu, Chih Chin Liao, Hem Takiar	Method of Stacking and Interconnecting Semiconductor Packages Via Electrical Connectors Extending Between Adjoining Semiconductor Packages	US	Published	12614981	9-Nov-09		
SDK0847.004US	Cheeman Yu, Chih Chin Liao, Hem Takiar	Method of Stacking and Interconnecting Semiconductor Packages Via Electrical Connectors Extending Between Adjoining Semiconductor Packages	US	Published	12614998	9-Nov-09		
SDK0852.000US	Alan Welsh Sinclair, Barry Wright	Use of a Direct Data File System with a Continuous Logical Address Space Interface	US	Published	11616242	26-Dec-06		
SDK0852.001US	Alan Welsh Sinclair, Barry Wright	System Using a Direct Data File System with a Continuous Logical Address Space Interface	US	Issued	11616236	26-Dec-06	7739444	15-Jun-10
SDK0852.002US	Alan Welsh Sinclair, Barry Wright	Configuration of Host LBA Interface with Flash Memory	US	Published	11616231	26-Dec-06		
SDK0852.003US	Alan Welsh Sinclair, Barry Wright	Host System with Direct Data File Interface	US	Issued	11616228	26-Dec-06	7917686	29-Mar-11
SDK0852.004US	Alan Welsh Sinclair, Barry Wright	Managing a LBA Interface in a Direct Data File Memory System	US	Published	11616226	26-Dec-06		
SDK0852.005US	Alan Welsh Sinclair, Barry Wright	Host System that Manages a LBA Interface with Flash Memory	US	Published	11616218	26-Dec-06		
SDK0854.000US	Ron LaPedis, Arrigo Triulzi	Method for Code Execution	US	Issued	11618519	29-Dec-06	7890723	15-Feb-11
SDK0854.001US	Ron LaPedis, Arrigo Triulzi	System for Code Execution	US	Issued	11618526	29-Dec-06	7890724	15-Feb-11
SDK0857.001US	Alan Welsh Sinclair, Barry Wright	Methods of Managing Blocks in Non Volatile Memory	US	Issued	11459268	21-Jul-06	7558906	7-Jul-09
SDK0857.002US	Alan Welsh Sinclair, Barry Wright	Non Volatile Memory with Block Management	US	Issued	11459260	21-Jul-06	7552271	23-Jun-09
SDK0858.000US	Sergey Anatolevich Gorobets	Method for Phased Garbage Collection With State Indicators	US	Issued	11540778	29-Sep-06	7464216	9-Dec-08

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0858.001US	Sergey Anatolevich Gorobets	System for Phased Garbage Collection With State Indicators.	US	Issued	11541371	29-Sep-06	7444463	28-Oct-08
SDK0860.000US	Patricio Collantes, Robert C Miller, Steven T. Sprouse, Dhaval Parikh	Methods for ESD protection	US	Issued	11465049	16-Aug-06	7416419	26-Aug-08
SDK0860.001US	Patricio Collantes, Robert C Miller, Steven T. Sprouse, Dhaval Parikh	Connector with EDS protection	US	Issued	11465045	16-Aug-06	7581967	1-Sep-09
SDK0863.000US	Cheeman Yu, Hem Takiar, Chih Chin Liao	METHODS OF FORMING A SINGLE LAYER SUBSTRATE FOR HIGH CAPACITY MEMORY CARDS	US	Issued	11538220	3-Oct-06	7772107	10-Aug-10
SDK0865.000US	Fumitoshi Ito	Providing local boosting control implant for non-volatile memory	US	Allowed	11536389	28-Sep-06		
SDK0865.001US	Fumitoshi Ito	Non-volatile memory with local boosting control implant	US	Issued	11536416	28-Sep-06	7705387	27-Apr-10
SDK0867.000US	Dana Lee, Yingda Dong, Changyuan Chen, Jeffrey Lutze	Hybrid Programming Methods and systems for non-volatile memory storage elements	US	Published	11535452	26-Sep-06		
SDK0868.000US	Gerrit Jan Hemink, Shih-Chung Lee	Reducing program distrib in non-volatile storage	US	Published	11535628	27-Sep-06		
SDK0868.001US	Gerrit Jan Hemink, Shih-Chung Lee	Apparatus with reduced program distrib in non-volatile storage	US	Published	11535634	27-Sep-06		
SDK0869.000US	Gerrit Jan Hemink, Shih-Chung Lee	FASTER PROGRAMMING OF HIGHEST MULTI-LEVEL STATE FOR NON-VOLATILE MEMORY	US	Issued	11554223	30-Oct-06	7596031	29-Sep-09
SDK0871.000US	Neil Dunlop, Kevin Kealy	Method Combining Once-Writable And Rewriteable Information Storage To Support Data Processing	US	Issued	11529582	29-Sep-06	7730270	1-Jun-10
SDK0871.001US	Neil Dunlop, Kevin Kealy	Apparatus Combining Rewriteable/One Time One Time Programmable (OTP) Memory Product	US	Issued	11529555	29-Sep-06	7630225	8-Dec-09
SDK0872.000US	Fumitoshi Ito	Programming non-volatile memory with improved boosting	US	Issued	11516976	6-Sep-06	7440326	21-Oct-08
SDK0873.000US	Yishai Kagan	Method of High Performance Flash Memory Data Transfer	US	Issued	11424573	16-Jun-06	7525855	28-Apr-09
SDK0873.001US	Yishai Kagan	High-Performance Flash Memory Data Transfer	US	Issued	11424581	16-Jun-06	7366029	29-Apr-08
SDK0876.000US	Lee Merrill Gavens, Charles Michael Schroter, Shing Wong	Methods for Performing Resilient Firmware Upgrades to a Functioning Memory	US	Published	11594283	7-Nov-06		
SDK0876.001US	Lee Merrill Gavens, Charles Michael Schroter, Shing Wong	Memory Controllers for Performing Resilient Firmware Upgrades to a Functioning Memory	US	Published	11594583	7-Nov-06		
SDK0877.000US	Jason Lin	Method of High-Performance Flash Memory Data Transfer	US	Issued	11458422	19-Jul-06	7499369	3-Mar-09
SDK0877.001US	Jason Lin	High-Performance Flash Memory Data Transfer	US	Issued	11458431	19-Jul-06	7499339	3-Mar-09
SDK0880.000US	Yigal Brandman, Kevin Conley	Methods of soft-input soft-output decoding for non-volatile memory	US	Issued	11536286	28-Sep-06	7818653	19-Oct-10
SDK0880.001US	Yigal Brandman, Kevin Conley	Soft-input soft-output decoder for non-volatile memory	US	Issued	11536327	28-Sep-06	7904783	8-Mar-11
SDK0881.000US	Yigal Brandman, Kevin Conley	Method of adapting operation of non-volatile memory	US	Issued	11536347	28-Sep-06	7805663	28-Sep-10
SDK0882.000US	Yishai Kagan, Michael James McCarthy	Dual voltage flash memory methods	US	Issued	11537214	29-Sep-06	7656735	2-Feb-10
SDK0882.001US	Yishai Kagan, Michael James McCarthy	Dual voltage flash memory card	US	Issued	11537232	29-Sep-06	7675802	9-Mar-10

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0884.000US	Yan Li, Yupin Fong, Nirma Mokhlesi	Pseudo Random and Command Driven Bit Compensation for the Cycling Effects in Flash Memory	US	Issued	11530392	8-Sep-06	7734861	8-Jun-10
SDK0884.001US	Yan Li, Yupin Fong, Nirma Mokhlesi	Methods in a Pseudo Random and command driven bit compensation for the cycling effects in flash memory	US	Issued	11530399	8-Sep-06	7606966	20-Oct-09
SDK0885.000US	Yan Li, Loc Tu, Charles Moana Hook	Method for Non-Volatile Memory with Reduced Erase/Write Cycling During Trimming of Initial Programming Voltage	US	Issued	11531217	12-Sep-06	7606091	20-Oct-09
SDK0885.001US	Yan Li, Loc Tu, Charles Moana Hook	Non-Volatile Memory with Reduced Erase/Write Cycling During Trimming of Initial Programming Voltage	US	Issued	11531223	12-Sep-06	7606077	20-Oct-09
SDK0886.000US	Eran Shen	Method for Reducing Fraud	US	Pending	11613067	19-Dec-06		
SDK0886.001US	Eran Shen	System for Reducing Fraud	US	Pending	11613095	19-Dec-06		
SDK0887.000US	Susan Cannon, Kevin Lewis	Method for interfacing with a memory device in cryptographic operations	US	Issued	11511766	28-Aug-06	7743258	22-Jun-10
SDK0887.002US	Kevin Lewis, Susan Cannon	Method and computing device for interfacing with a memory device in operations	US	Pending	12760359	14-Apr-10		
SDK0888.000US	Nirma Mokhlesi	Margined Neighbor Reading for Non-Volatile Memory Read Operations Including Coupling Compensation	US	Issued	11618616	29-Dec-06	7518923	14-Apr-09
SDK0888.001US	Nirma Mokhlesi	Systems for Margined Neighbor Reading for Non-Volatile Memory Read Operations Including Coupling Compensation	US	Issued	11618624	29-Dec-06	7606070	20-Oct-09
SDK0889.000US	Prajit Nandi	Voltage Regulator In a Non-Volatile Memory Device	US	Issued	11549885	16-Oct-06	7372748	13-May-08
SDK0890.000US	Dhaval Parikh, Rajeev Nagabhirava	Methods of configuring a generic bluetooth controller of a flash drive to be compatible with multiple bluetooth peripheral devices	US	Issued	11536095	28-Sep-06	7801561	21-Sep-10
SDK0890.001US	Dhaval Parikh, Rajeev Nagabhirava	Flash drive that configures generic bluetooth controller of the drive to be compatible with multiple bluetooth peripheral devices	US	Issued	11536155	28-Sep-06	7774027	10-Aug-10
SDK0894.000US	Alan Jun Li, Prajit Nandi, Mehrdad Mofidi	METHOD FOR CONTROLLING VOLTAGE IN NON-VOLATILE MEMORY SYSTEMS	US	Issued	11618541	29-Dec-06	7447093	4-Nov-08
SDK0894.001US	Alan Jun Li, Prajit Nandi, Mehrdad Mofidi	SYSTEM FOR CONTROLLING VOLTAGE IN NON-VOLATILE MEMORY SYSTEMS	US	Issued	11618544	29-Dec-06	7403434	22-Jul-08
SDK0896.000US	Andrew Tomlin, Sergey Anatolievich Gorobets	Methods of managing file allocation table information	US	Issued	11537243	29-Sep-06	7752412	6-Jul-10
SDK0896.001US	Andrew Tomlin, Sergey Anatolievich Gorobets	System for managing file allocation table information	US	Issued	11537216	29-Sep-06	7681008	16-Mar-10
SDK0899.001US	Matthijs Hutten	Video Content Recording Apparatus with Card-Slot Specific Recording	US	Published	11540121	29-Sep-06		
SDK0901.000US	Nirma Mokhlesi, Raul-Adrian Cernea	Resistance Sensing and Compensation for Non-Volatile Storage	US	Issued	11617972	29-Dec-06	7590002	15-Sep-09
SDK0901.001US	Nirma Mokhlesi, Raul-Adrian Cernea	Non-Volatile Storage System with Resistance Sensing and Compensation	US	Issued	11617981	29-Dec-06	7616498	10-Nov-09
SDK0902.000US	Nirma Mokhlesi	Page By Page ECC Variation in a Memory Device	US	Issued	11618686	29-Dec-06	7870457	11-Jan-11
SDK0902.001US	Nirma Mokhlesi	Page By Page ECC Variation in a Memory Device	US	Issued	11618694	29-Dec-06	7877665	25-Jan-11

FILE #	INVENTORS	TITLE	COUNTRY	STATUS	SER #	FILING DATE	PAT #	ISSUE DATE
SDK0903.000US	Nima Mokhlesi	OPERATING NON-VOLATILE MEMORY WITH BOOST STRUCTURES	US	Issued	11558980	13-Nov-06	7508710	24-Mar-09
SDK0903.001US	Nima Mokhlesi	NON-VOLATILE MEMORY WITH BOOST STRUCTURES	US	Issued	11558984	13-Nov-06	7508703	24-Mar-09
SDK0903.002US	Nima Mokhlesi	FABRICATING NON-VOLATILE MEMORY WITH BOOST STRUCTURES	US	Issued	11558986	13-Nov-06	7696035	13-Apr-10
SDK0903.003US	Nima Mokhlesi	FABRICATING NON-VOLATILE MEMORY WITH BOOST STRUCTURES	US	Published	12714801	1-Mar-10		
SDK1017.000US	Loc Tu, Charles Moana Hook, Yan Li	Method for non-volatile memory with linear estimation of initial programming voltage	US	Issued	11531227	12-Sep-06	7453731	18-Nov-08
SDK1017.001US	Loc Tu, Charles Moana Hook, Yan Li	Non-volatile memory with linear estimation of initial programming voltage	US	Issued	11531230	12-Sep-06	7599223	6-Oct-09
SDK1017.002US	Loc Tu, Charles Moana Hook, Yan Li	Non-volatile memory with linear estimation of initial programming voltage	US	Published	12573405	5-Oct-09		
SOC0233.000US	Wesley G. Brewer, Michael Gifford, Yoram Cedar, Leonard L. Ott, Robert Wallace, Robert C. Miller, Kevin J. Mills	Cooperative interconnection and operation of a non-volatile memory card an an input-output card	US	Issued	9653062	1-Sep-00	7107378	12-Sep-06
SOC0233.002US	Wesley G. Brewer, Michael Gifford, Yoram Cedar, Leonard L. Ott, Robert Wallace, Robert C. Miller, Kevin J. Mills	Cooperative interconnection and operation of a non-volatile memory card an an input-output card	US	Issued	11461063	31-Jul-06	7680974	16-Mar-10
TSB0001.000US	Yoshihisa Iwata, Masaki Momodomi, Yasuo Itoh, Tomoharu Tanaka, Hideko Odaira	Nand Cell Type Programmable Read-Only Memory With Common Control Gate Driver Circuit	US	Issued	7532627	4-Jun-90	5043942	27-Aug-91
TSB0002.000US	Tomoharu Tanaka, Yoshiyuki Tanaka, Hiroshi Nakamura, Hideko Odaira	Electrically Erasable and Programmable Non-Volatile Semiconductor Memory With Automatic Write-Verify Controller	US	Issued	9893544	29-Jun-01	6477087	5-Nov-02