

PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT
NATURE OF CONVEYANCE:	Corrective Assignment to correct the Assignor Name previously recorded on Reel 026906 Frame 0531. Assignor(s) hereby confirms the Assignor Name of Bitwave Semiconductor, Incorporated..
CONVEYING PARTY DATA	
Name	Execution Date
Bitwave Semiconductor, Incorporated	06/14/2011
RECEIVING PARTY DATA	
Name:	TriDev Research LLC
Street Address:	160 Greentree Drive, Suite 101
City:	Dover
State/Country:	DELAWARE
Postal Code:	19904
PROPERTY NUMBERS Total: 7	
Property Type	Number
Patent Number:	7672645
Patent Number:	7482887
Patent Number:	7323945
Patent Number:	7580684
Patent Number:	7508898
Patent Number:	7764125
Application Number:	12348606
CORRESPONDENCE DATA	
Fax Number:	(216)696-8731
Phone:	216-696-8730
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<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
Correspondent Name:	Turocy & Watson, LLP
Address Line 1:	127 Public Square

OP \$280.00 7672645

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PATENT
REEL: 026930 FRAME: 0622

Address Line 2: 57th Floor, Key Tower
Address Line 4: Cleveland, OHIO 44114

ATTORNEY DOCKET NUMBER:	VBIT MATTERS (EX. B)
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NAME OF SUBMITTER:	Thomas Watson
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Total Attachments: 10
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CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence (along with any paper referred to as being attached or enclosed) is being faxed to **571-273-8300** on the date shown below to Mail Stop Assignment Services Division, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Date: September 19, 2011

/Robin Wardzala/
Robin Wardzala

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Patent Number:	7672645
Patent Number:	7482887
Patent Number:	7323945
Patent Number:	7580684
Patent Number:	7508898
Patent Number:	7764125
Application Number:	12348606

**Mail Stop Assignment Services Division
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450**

REQUEST TO CORRECT ASSIGNMENT

This Request is in response to the Assignment Recordation received in connection with the above-identified patent application. There is an error with respect to the following data, which was incorrectly entered. The Assignor Name was truncated to appear as **Bitwave Semiconductor**. However, the full Assignor Name should appear as **Bitwave Semiconductor, Incorporated**.

**PATENT
REEL: 026930 FRAME: 0624**

The required documents, the original Assignment Recordation and the executed Assignment papers are hereby enclosed.

Should there be any questions regarding this paper, the Examiner is invited to contact applicants' undersigned representative at the telephone number below.

A credit card payment form is filed concurrently herewith in connection with all fees due associated with this request. In the event any additional fees may be due and/or are not covered by the credit card, the Commissioner is authorized to charge such fees to Deposit Account No. 50-1063.

Respectfully submitted,

TUROC & WATSON, LLP

/Thomas Watson/

Thomas Watson

Reg. No. 43,243

TUROC & WATSON, LLP
127 PUBLIC SQUARE
57th Floor, Key Tower
Cleveland, Ohio 44114
Telephone (216) 696-8730
Facsimile (216) 696-8731

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Bitwave Semiconductor, Incorporated, a Delaware corporation, with an office at PO Box 334, Pepperell, MA 01463 ("**Assignor**"), does hereby sell, assign, transfer, and convey unto TriDev Research LLC, a Delaware limited liability company, with an address at 160 Greentree Drive, Suite 101; Dover, DE 19904 ("**Assignee**"), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the "**Patent Rights**");

(a) the provisional patent applications, patent applications and patents listed in the table below (the "**Patents**");

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
CA2655463	CA	06/15/2007	Continuous gain compensation and fast band selection in a multi-standard, multi-frequency synthesizer Jeff Venuti
CN200780028020.8	CN	06/15/2007	Continuous gain compensation and fast band selection in a multi-standard, multi-frequency synthesizer Jeff Venuti
7,672,645	US	06/15/2006	Programmable transmitter architecture for non-constant and constant envelope modulation Jeff Venuti
CA2655452	CA	06/15/2007	Programmable transmitter architecture for non-constant and constant envelope modulation Jeff Venuti
CN200780028888.8	CN	06/15/2007	Non-constant envelope modulation and constant envelope structure of the programmable transmitter

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<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
			Jeff Venuti
KR10-2009-7000757	KR	06/15/2007	Programmable transmitter architecture for non-constant and constant envelope modulation
			Jeff Venuti
7,482,887	US	02/10/2005	Multi-band tunable resonant circuit
			Geoffrey C. Dawe
7,323,945	US	02/10/2005	Programmable radio transceiver
			Geoffrey C. Dawe
7,580,684	US	02/10/2005	Programmable radio transceiver
			Geoffrey C. Dawe
7,508,898	US	08/11/2005	Programmable radio transceiver
			Geoffrey C. Dawe
AU2006279698	AU	08/11/2006	Programmable radio transceiver
			Geoffrey C. Dawe
CA2618837	CA	08/11/2006	Programmable radio transceiver
			Geoffrey C. Dawe
CN200680037710.5	CN	08/11/2006	Programmable transceiver
			Geoffrey C. Dawe
JP2006-553242	JP	02/10/2005	Programmable transceiver
			Geoffrey C. Dawe
KR10-2008-7005995	KR	08/11/2006	Programmable radio transceiver
			Geoffrey C. Dawe
TW094103589	TW	02/04/2005	Programmable radio

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<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
			transceiver Geoffrey C. Dawe
TW095129512	TW	08/11/2006	Programmable radio transceiver Geoffrey C. Dawe
7,764,125	US	05/23/2008	Reconfigurable tunable rf power amplifier Geoffrey C. Dawe
12/348,606	US	01/05/2009	Reconfigurable mixer with gain control Geoffrey C. Dawe
KR10-2009-7000758	KR	06/15/2007	Continuous gain compensation and fast band selection in a multi-standard, multi-frequency synthesizer Jeff Venuti
HK07103820.3	HK	04/12/2007	Programmable radio transceiver Geoffrey C. Dawe

(b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, (ii) for which any of the Patents directly or indirectly forms a basis for priority, and/or (iii) that were co-owned applications that incorporate by reference, or are incorporated by reference into, the Patents;

(c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);

(d) all foreign patents, patent applications, and counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;

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(e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents below and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;

(f) inventions, invention disclosures, and discoveries described in any of the Patents and/or any item in the foregoing categories (b) through (e) that (i) are included in any claim in the Patents and/or any item in the foregoing categories (b) through (e), (ii) are subject matter capable of being reduced to a patent claim in a reissue or reexamination proceeding brought on any of the Patents and/or any item in the foregoing categories (b) through (e), and/or (iii) could have been included as a claim in any of the Patents and/or any item in the foregoing categories (b) through (e);

(g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (f), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;

(h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

- (1) damages,
- (2) injunctive relief, and
- (3) any other remedies of any kind

for past, current, and future infringement; and

(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h).

Assignor represents, warrants and covenants that:

(1) Assignor has the full power and authority, and has obtained all third party consents, approvals and/or other authorizations required to enter into this Agreement and to carry out its obligations hereunder, including the assignment of the Patent Rights to Assignee; and

(2) Assignor owns, and by this document assigns to Assignee, all right, title, and interest to the Patent Rights, including, without limitation, all right, title, and interest to sue for infringement of the Patent Rights. Assignor has obtained and properly recorded previously executed assignments for the Patent Rights as necessary to fully perfect its rights and title therein in accordance with governing law and regulations in each respective jurisdiction. The Patent Rights are free and clear of all liens, claims, mortgages, security interests or other

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encumbrances, and restrictions. There are no actions, suits, investigations, claims or proceedings threatened, pending or in progress relating in any way to the Patent Rights. There are no existing contracts, agreements, options, commitments, proposals, bids, offers, or rights with, to, or in any person to acquire any of the Patent Rights.

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

Assignor will, at the reasonable request of Assignee and without demanding any further consideration therefore, do all things necessary, proper, or advisable, including without limitation, the execution, acknowledgment, and recordation of specific assignments, oaths, declarations, and other documents on a country-by-country basis, to assist Assignee in obtaining, perfecting, sustaining, and/or enforcing the Patent Rights.

The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Pepperell
MA on 14 June 2011.

ASSIGNOR:

Bitwave Semiconductor, Incorporated

By: Russell J Cyr
Name: Russell J Cyr
Title: VP & CMO
(Signature MUST be attested)

ATTESTATION OF SIGNATURE PURSUANT TO 28 U.S.C. § 1746

The undersigned witnessed the signature of Russell J Cyr to the above Assignment of Patent Rights on behalf of Bitwave Semiconductor, Incorporated and makes the following statements:

1. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.

2. Russell J Cyr is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me on 14 June 2011 2011 to execute the above Assignment of Patent Rights on behalf of Bitwave Semiconductor, Incorporated.

3. Russell J Cyr subscribed to the above Assignment of Patent Rights on behalf of Bitwave Semiconductor, Incorporated.

I declare under penalty of perjury under the laws of the United States of America that the statements made in the three (3) numbered paragraphs immediately above are true and correct.

EXECUTED on 14 June 2011 (date)

Renee M. Cyr

Print Name: Renee M. Cyr