

**PATENT ASSIGNMENT**

Electronic Version v1.1  
 Stylesheet Version v1.1

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
Unisantis Electronics Japan Ltd.	09/13/2011
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	Unisantis Electronics Singapore Pte Ltd.
<b>Street Address:</b>	111 North Bridge Road
<b>Internal Address:</b>	#16-04
<b>City:</b>	Peninsula Plaza
<b>State/Country:</b>	SINGAPORE
<b>Postal Code:</b>	179098
<b>PROPERTY NUMBERS Total: 3</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	12284327
<b>Application Number:</b>	12319782
<b>Application Number:</b>	12319770
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(312)321-4299
<b>Phone:</b>	3123214200
<b>Email:</b>	hbernal@usebrinks.com
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Correspondent Name:</b>	Tadashi Horie/Hisae Bernal
<b>Address Line 1:</b>	Brinks Hofer Gilson & Lione
<b>Address Line 2:</b>	P.O. Box 10395
<b>Address Line 4:</b>	Chicago, ILLINOIS 60610
<b>ATTORNEY DOCKET NUMBER:</b>	13773/2 (UNISANTISTOHOKU)
<b>NAME OF SUBMITTER:</b>	Tadashi Horie

CH \$120.00 12284327

**Total Attachments: 4**

source=13773AssignmentAsFiled\_Unisantis\_TohokuUni#page1.tif

source=13773AssignmentAsFiled\_Unisantis\_TohokuUni#page2.tif

source=13773AssignmentAsFiled\_Unisantis\_TohokuUni#page3.tif

source=13773AssignmentAsFiled\_Unisantis\_TohokuUni#page4.tif

*Assignment for pending applications and patents  
(co-applicant)*

**PATENT ASSIGNMENT AGREEMENT**

This PATENT ASSIGNMENT AGREEMENT (this "Assignment"), effective as of the date last written below, is entered into by and between Unisantis Electronics Japan Ltd., a corporation organized and existing under the laws of Japan, having a place of business at 22-11 Shinkawa 1-Chome, Chuo-ku, Tokyo 104-0033 Japan, ("Assignor"), and Unisantis Electronics Singapore Pte Ltd., a corporation organized and existing under the laws of Singapore, having a place of business at 111 North Bridge Road, #16-04, Peninsula Plaza, Singapore (179098), ("Assignee") (each, a "party," and collectively, the "parties").

WHEREAS, Schedule A attached hereto identifies the patents and patent applications jointly owned by Assignor and Tohoku University (the "Patent"); and

WHEREAS, Assignor has agreed to assign to Assignee, and Assignee desires to receive from Assignor, all of Assignor's right, title and undivided interest in and to the Patents.

NOW, THEREFORE, in consideration of the premises and the mutual agreements and covenants hereinafter set forth, the parties hereto agree as follows:

1. Assignment (a) Assignor hereby irrevocably assigns, transfers, conveys and delivers to Assignee, free and clear of any Encumbrances (other than Permitted Encumbrances), and Assignee hereby receives from Assignor, all of Assignor's right, title and undivided interest in and to the Patents for the United States and for all foreign countries, including, without limitation, the inventions and improvements described and claimed therein, all reissuances, revisions, divisionals, continuations, extensions, continuations-in-part and counterparts thereof and all corresponding rights that are or may be secured under the laws of the United States or any foreign country, now or hereafter arising or in effect, for

Assignee's own use and enjoyment, and for the use and enjoyment of Assignee's successors, assigns and other legal representatives, as fully and entirely as the same would have been held and enjoyed by Assignor if this Assignment had not been made, together with Assignor's all rights to collect royalties, products and proceeds in connection with any of the foregoing and Assignor's all rights to sue and recover damages or other relief for all past, present or future infringement, misappropriation or other violation of any of the foregoing, and all rights corresponding thereto throughout the world.

(b) Assignor hereby requests the Commissioner of Patents and Trademarks in the United States Patent and Trademark Office, and the corresponding entities or agencies in any applicable foreign countries or multinational authorities, to record Assignee as the assignee and co-owner of the Patents and to deliver to Assignee, and to Assignee's attorneys, agents, successors or assigns, all official documents and communications as may be warranted by this Assignment.

(c) The parties agree that this Assignment does not affect or alter the right, title and undivided interest of Tohoku University in and to the Patents.

2. Further Action. Assignor and Assignee shall each take, and shall cause their respective Affiliates to take, any and all additional actions as may be necessary or appropriate to effect the transactions contemplated hereby, including, but not limited to, execution of individual assignment documentation for filing with the authorities of each individual country. The responsibility to file assignments with the national patent offices of each country shall be on the Assignee and the Assignee shall bear the cost of filing such assignments.

3. Counterparts. This Assignment may be executed and delivered (including by facsimile or other means of electronic transmission, such as by electronic mail in "pdf" form) in one or more counterparts, and by the different parties hereto in separate counterparts, each of which when executed shall be deemed to be an original, but all of which taken together shall constitute one and the same agreement.

4. Governing Law. This Assignment shall be governed by, and construed in accordance with, the Laws of Japan.

IN WITNESS THEREOF, the parties hereto have caused this Assignment to be executed as of the date last written below by their respective officers thereunto duly authorized.

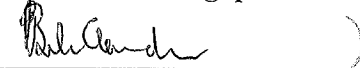
**Unisantis Electronics Japan Ltd.**

By   
Name Kinichi FUKUDA

Title Chief Director

Date September 13, 2011

**Unisantis Electronics Singapore Pte Ltd.**

By   
Name Balachandran MANYADATH

Title Director

Date September 13, 2011

## Schedule A

BHGL No.	Title	Filing Date	Application No.
13773/4	CMOS INVERTER COUPLING CIRCUIT COMPRISING VERTICAL TRANSISTORS	09/19/08	12/284,327
13773/6	NONVOLATILE SEMICONDUCTOR MEMORY AND METHOD OF DRIVING THE SAME	01/12/09	12/319,782
13773/7	NONVOLATILE SEMICONDUCTOR MEMORY AND METHOD OF DRIVING THE SAME	01/12/09	12/319,770