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Atty Ref/Docket No.: 303.

Patent and Trademark Office

To the Director of the U.S. Patent and Trademark Office: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Numonyx B.V.

Additional name(s) of conveying party(ies) attached?

[] Yes [X] No

3. Nature of conveyance:

[X] Assignment [] Merger

[] Security Agreement [] Change of Name

[] Other

Execution Date: September 30, 2011

2. Name and address of receiving party(ies):

Name: Micron Technology, Inc.

Street Address: 8000 So. Federal Way

City: Boise State: ID Zip: 83716-9632Country: United States of America

Additional name(s) & address(es) attached? [] Yes [X] No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is:

A. Patent Application No.(s)

B. Patent No.(s)

See attached Appendix A

Additional numbers attached? [] Yes [X] No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Mark V. Muller

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P.O. Box 2938

Minneapolis, MN 55402-0938

6. Total number of applications and patents involved: 607. Total fee (37 CFR 3.41): \$ 2400.00

[] Enclosed

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9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Mark V. Muller / Reg. No. 37,509

Name of Person Signing

Mark V. Muller

Signature

OCTOBER 10, 2011

Date

Total number of pages including cover sheet: 20

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PATENT
REEL: 027046 FRAME: 0040

CH \$2400.00 190743 12347738

Appendix A

<u>Appl. Serial No.:</u>	<u>Filing Date</u>	<u>Docket No.</u>	<u>Status</u>
12/347,738	Dec 31, 2008	303.B89US1	Pending
12/347,900	Dec 31, 2008	303.B90US1	Issued
12/992,062	Mar 2, 2011	303.B92US1	Pending
10/109,811	Mar 28, 2002	303.B98US1	Issued
10/942,147	Sep 16, 2004	303.B98US2	Issued
10/660,228	Sep 10, 2003	303.B99US1	Issued
10/382,680	Mar 5, 2003	303.C00US1	Issued
10/405,098	Mar 31, 2003	303.C01US1	Issued
11/173,760	Jun 30, 2005	303.C02US1	Pending
11/024,956	Dec 29, 2004	303.C03US1	Pending
12/347,721	Dec 31, 2008	303.C08US1	Pending
12/345,102	Dec 29, 2008	303.C10US1	Pending
12/463,329	May 8, 2009	303.C12US1	Pending
12/343,398	Dec 23, 2008	303.C13US1	Pending
12/542,528	Aug 17, 2009	303.C14US1	Pending
12/416,008	Mar 31, 2009	303.C22US1	Pending
12/496,503	Jul 1, 2009	303.C25US1	Pending
12/477,017	Jun 2, 2009	303.C26US1	Pending
12/415,991	Mar 31, 2009	303.C27US1	Pending
12/466,366	May 14, 2009	303.C31US1	Pending
12/617,661	Nov 12, 2009	303.C34US1	Pending
12/557,856	Sep 11, 2009	303.C36US1	Pending
12/651,097	Dec 31, 2009	303.C37US1	Pending
12/557,776	Sep 11, 2009	303.C38US1	Pending
12/577,631	Oct 12, 2009	303.C43US1	Pending
12/347,935	Dec 31, 2008	303.C45US1	Pending
12/472,153	May 26, 2009	303.C46US1	Pending
12/481,496	Jun 9, 2009	303.C47US1	Pending
12/345,572	Dec 29, 2008	303.C48US1	Abandoned
13/132,311	Sep 21, 2011	303.C52US1	Pending
12/341,959	Dec 22, 2008	303.C56US1	Pending
12/434,600	May 1, 2009	303.C65US1	Issued
12/491,180	Jun 24, 2009	303.C68US1	Pending
12/465,506	May 13, 2009	303.C70US1	Pending
12/416,022	Mar 31, 2009	303.C83US1	Pending
12/464,011	May 11, 2009	303.C84US1	Pending

<u>Appl. Serial No.:</u>	<u>Filing Date</u>	<u>Docket No.</u>	<u>Status</u>
12/345,462	Dec 29, 2008	303.C87US1	Issued
12/416,000	Mar 31, 2009	303.C88US1	Pending
12/345,411	Dec 29, 2008	303.C91US1	Issued
12/345,398	Dec 29, 2008	303.C92US1	Issued
12/628,153	Nov 30, 2009	303.C96US1	Pending
12/628,166	Nov 30, 2009	303.C97US1	Pending
12/464,036	May 11, 2009	303.D02US1	Pending
12/539,771	Aug 12, 2009	303.D03US1	Pending
12/463,337	May 8, 2009	303.D04US1	Pending
12/423,226	Apr 14, 2009	303.D05US1	Allowed
12/548,375	Aug 26, 2009	303.D06US1	Pending
12/415,973	Mar 31, 2009	303.D07US1	Pending
12/532,828	Apr 26, 2011	303.D11US1	Pending
12/628,073	Nov 30, 2009	303.D17US1	Pending
12/577,602	Oct 12, 2009	303.D18US1	Allowed
12/390,339	Feb 20, 2009	303.D19US1	Pending
12/347,920	Dec 31, 2008	303.D21US1	Pending
12/345,306	Dec 29, 2008	303.D22US1	Pending
12/387,655	May 5, 2009	303.D23US1	Abandoned
12/628,035	Nov 30, 2009	303.D24US1	Pending
12/628,152	Nov 30, 2009	303.D25US1	Pending
12/582,643	Oct 20, 2009	303.D26US1	Pending
12/483,198	Jun 11, 2009	303.D27US1	Pending
12/557,723	Sep 11, 2009	303.D28US1	Pending

PATENT ASSIGNMENT AGREEMENT

WHEREAS, Numonyx B.V., a company organized under the laws of Netherlands, having a place of business at A-ONE Biz Center, Z.A. Vers la Piece, Rte. De l'Etraz, 1180 Rolle, Switzerland (hereinafter referred to as "Assignor") has rights in and to the following:

1. U.S. Letters Patents Listed on Exhibit A,
2. U.S. Patent Applications listed on Exhibit B,
3. Foreign patents and patent applications listed on Exhibit C.

(said patents and patent applications listed in Exhibits A, B, and C being hereinafter referred to as "SAID PATENT RIGHTS"); and

WHEREAS, Micron Technology, Inc. is a corporation having a principal place of business at 8000 S. Federal Way, Boise, Idaho 83707, United States (hereinafter referred to as "Assignee") is desirous of obtaining rights to SAID PATENT RIGHTS;

NOW, THEREFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, Assignor does hereby sell, convey, transfer and assign to Assignee, its successors, assigns and legal representatives, subject to prior encumbrances, all right, title and interest in and to SAID PATENT RIGHTS, together with any and all continuations, continuation-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with the right to sue for and be entitled to any damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof.

IN WITNESS WHEREOF, Assignor has caused this Assignment to be executed by its duly authorized officer effective as of the 30 day of September, 2011.

Numonyx B.V.
By: Thomas L. Jarvis
Printed Name: THOMAS L. JARVIS JR
Title: DIRECTOR

2.S. Micron Technology, Inc.
By: Russell Slifer
Printed Name: Russell Slifer
Title: Chief Patent Counsel

Numonyx originated

Exhibit B - US Applications

App No.	App Title	App Status	App Type	App Date	Serial No.	Country
NMXN-0001.00/US	ENHANCED THROUGHPUT FOR SERIAL FLASH MEMORY	Pending	Utility - ORG	12/31/2008	12/347,510	US
NMXN-0005.00/US	MEMORY WITH GUARD VALUE DEPENDENT ERROR CORRECTION	Pending	Utility - ORG	12/16/2008	12/336,476	US
NMXN-0007.00/US	MEMORY WITH SUB-BLOCKS	Pending	Utility - ORG	6/24/2009	12/491,160	US
NMXN-0008.00/US	SYSTEM, APPARATUS, AND READING METHOD FOR NAND MEMORIES	Pending	Utility - ORG	11/30/2009	12/628,073	US
NMXN-0011.00/US	INTEGRATED CIRCUIT EDGE AND METHOD TO FABRICATE THE SAME	Allowed	Utility - ORG	10/12/2009	12/577,602	US
NMXN-0017.00/US	AVOIDING DEGRADATION OF CHALCOGENIDE MATERIAL DURING DEFINITION OF MULTILAYER STACK STRUCTURE	Pending	Utility - NSPCT	9/21/2011	13/132,311	US
NMXN-0020.00/US	SECONDARY MEMORY ELEMENT FOR NON-VOLATILE MEMORY	Pending	Utility - ORG	12/30/2008	12/346,015	US
NMXN-0022.00/US	SHALLOW TRENCH ISOLATION FOR A MEMORY	Pending	Utility - ORG	12/22/2008	12/341,002	US
NMXN-0023.00/US	METHOD OF FABRICATING A CHARGE TRAP NAND FLASH MEMORY DEVICE	Pending	Utility - ORG	12/30/2008	12/346,363	US
NMXN-0029.00/US	LOW TEMPERATURE BOARD LEVEL ASSEMBLY USING ANISOTROPICALLY CONDUCTIVE MATERIALS	Abandoned	Utility - ORG	11/14/2008	12/271,077	US
NMXN-0031.00/US	PROTECTIVE THIN FILM COATING IN CHIP PACKAGING	Abandoned	Utility - ORG	12/29/2008	12/345,572	US
NMXN-0032.00/US	PASSWORD PROTECTED BUILT-IN TEST MODE FOR MEMORIES	Pending	Utility - ORG	12/2/2008	12/326,165	US
NMXN-0034.00/US	A SEMICONDUCTOR PACKAGE SUBSTRATE WITH METAL BUMPS	Abandoned	Utility - ORG	12/31/2008	12/347,800	US
NMXN-0036.00/US	INTEGRATING DIVERSE TRANSISTORS ON THE SAME WAFER	Pending	Utility - ORG	12/22/2008	12/341,014	US
NMXN-0038.00/US	MULTI-DIE BUILDING BLOCK FOR STACKED-DIE PACKAGE	Pending	Utility - ORG	12/31/2008	12/347,738	US
NMXN-0040.00/US	INTEGRATION OF POLY RESISTOR AND CAPACITOR IN NAND CHARGE TRAP PROCESS FLOW	Pending	Utility - NSPCT	12/30/2008	13/132,312	US
NMXN-0041.00/US	CONTROLLING THE CIRCUITRY AND MEMORY ARRAY RELATIVE HEIGHT IN A PHASE CHANGE MEMORY FEOL PROCESS FLOW	Pending	Utility - ORG	12/29/2008	12/345,102	US
NMXN-0043.00/US	REVERSING A POTENTIAL POLARITY FOR READING PHASE-CHANGE CELLS TO SHORTEN A RECOVERY DELAY AFTER PROGRAMMING	Pending	Utility - NSPCT	3/2/2011	12/992,062	US
NMXN-0045.00/US	INCREASING THE SPATIAL RESOLUTION OF DOSIMETRY SENSORS	Pending	Utility - ORG	12/8/2008	12/329,740	US
NMXN-0047.00/US	FORMING ISOLATION REGIONS FOR INTEGRATED CIRCUITS	Pending	Utility - ORG	12/23/2008	12/342,312	US
NMXN-0049.00/US	MULTILEVEL ENCODING WITH ERROR CORRECTION	Pending	Utility - ORG	12/11/2008	12/333,100	US
NMXN-0059.00/US	Package on Package Assembly using Electrically Conductive Adhesive Material	Pending	Utility - ORG	12/14/2008	12/334,517	US
NMXN-0060.00/US	ERASE COMPLETION RECOGNITION	Allowed	Utility - ORG	12/29/2008	12/345,511	US
NMXN-0060.01/US	ERASE COMPLETION RECOGNITION	Pending	Utility - CON	8/11/2011	13/208,313	US
NMXN-0061.00/US	ERROR CORRECTION CODE FOR UNIDIRECTIONAL MEMORY	Pending	Utility - NSPCT	11/20/2009	12/623,310	US
NMXN-0063.00/US	NON-VOLATILE MEMORY WITH EXTENDED OPERATING TEMPERATURE RANGE	Pending	Utility - NSPCT	12/30/2008	12/532,828	US
NMXN-0066.00/US	DATA CONTENT ROTATION FOR MEMORY WEAR LEVELING	Pending	Utility - ORG	12/31/2008	12/347,920	US
NMXN-0073.00/US	ALWAYS OPEN WRITE-ONLY REGISTER BASED MEMORY MAPPED OVERLAY INTERFACE FOR A NONVOLATILE MEMORY	Pending	Utility - ORG	12/31/2008	12/347,962	US
NMXN-0076.00/US	WORD-LINE DRIVER INCLUDING PULL-UP RESISTOR AND PULL-DOWN TRANSISTOR	Pending	Utility - ORG	11/19/2009	12/621,933	US
NMXN-0077.00/US	DOUBLE PATTERNING METHOD FOR CREATING A REGULAR ARRAY OF PILLARS WITH DUAL SHALLOW TRENCH	Pending	Utility - NSPCT	6/2/2011	13/132,602	US

PATENT

REEL: 027046 FRAME: 0045

Exhibit B - US Applications

Patent No.	Matter Type	Matter Status	Title	Filed	Serial No.	Country
NMXN-0078.00/US	Utility - ORG	Pending	PHASE CHANGE MEMORY DEVICE WITH REDUCED PROGRAMMING DISTURBANCE	11/30/2009	12/628,153	US
NMXN-0079.00/US	Utility - ORG	Pending	RELIABLE SET OPERATION FOR PHASE-CHANGE MEMORY CELL	11/20/2009	12/623,299	US
NMXN-0081.00/US	Utility - ORG	Pending	RECOVERY FOR NON-VOLATILE MEMORY AFTER POWER LOSS	12/31/2008	12/347,935	US
NMXN-0083.00/US	Utility - ORG	Pending	METHOD OF HIGH ASPECT RATIO PLUG FILL	12/31/2008	12/347,721	US
NMXN-0084.01/US	Utility - CON	Pending	DYNAMIC POLARIZATION FOR REDUCING STRESS INDUCED LEAKAGE CURRENT	4/18/2011	13/089,259	US
NMXN-0086.00/US	Utility - NSPCT	Pending	VIRTUALIZED ECC NAND	4/6/2011	13/122,909	US
NMXN-0087.00/US	Utility - ORG	Pending	MEMORY INCLUDING A SELECTOR SWITCH ON A VARIABLE RESISTANCE MEMORY CELL	11/30/2010	12/957,286	US
NMXN-0088.01/US	Utility - DIV	Allowed	FABRICATING BIPOLEAR JUNCTION SELECT TRANSISTORS FOR SEMICONDUCTOR MEMORIES	10/27/2010	12/912,829	US
NMXN-0090.00/US	Utility - NSPCT	Pending	METHOD FOR FABRICATING A PHASE-CHANGE MEMORY CELL	6/2/2011	13/132,603	US
NMXN-0091.00/US	Utility - ORG	Pending	PHASE CHANGE MEMORY CELL WITH SELF-ALIGNED VERTICAL HEATER	6/9/2009	12/481,496	US
NMXN-0092.00/US	Utility - ORG	Pending	CONTROL SIGNAL OUTPUT PIN TO INDICATE MEMORY INTERFACE CONTROL FLOW	3/27/2009	12/412,829	US
NMXN-0095.00/US	Utility - NSPCT	Pending	EXECUTE IN-PLACE MODE CONFIGURATION FOR SERIAL NON-VOLATILE MEMORY	6/30/2011	10/434,800	US
NMXN-0096.00/US	Utility - NSPCT	Pending	NON-VOLATILE CONFIGURATION FOR SERIAL FLASH MEMORY	6/30/2011	13/143,075	US
NMXN-0102.01/US	Utility - DIV	Pending	REDUCED SIGNAL INTERFACE MEMORY DEVICE, SYSTEM, AND METHOD	4/29/2011	13/097,568	US
NMXN-0103.00/US	Utility - NSPCT	Pending	ENHANCED ADDRESSABILITY FOR SERIAL NON-VOLATILE MEMORY	6/30/2011	13/143,072	US
NMXN-0104.00/US	Utility - ORG	Pending	SELF-SELECTING PCM DEVICE NOT REQUIRING A DEDICATED SELECTOR TRANSISTOR	12/31/2009	12/651,097	US
NMXN-0105.00/US	Utility - ORG	Pending	EXTENDED ADDRESS MODE FOR SERIAL FLASH MEMORY	12/31/2008	12/347,623	US
NMXN-0106.00/US	Utility - NSPCT	Pending	TEMPERATURE ALERT AND LOW RATE REFRESH FOR A NON-VOLATILE MEMORY	6/27/2011	13/142,418	US
NMXN-0108.00/US	Utility - ORG	Pending	DYNAMIC RANGE UNLOCK-LOCK SECURITY MECHANISMS FOR PCM AND OTHER NVM TECHNOLOGIES	11/30/2009	12/628,166	US
NMXN-0109.00/US	Utility - ORG	Pending	METHOD AND APPARATUS FOR CONFIGURING WRITE PERFORMANCE FOR ELECTRICALLY WRITABLE MEMORY DEVICES	12/17/2008	12/337,573	US
NMXN-0112.00/US	Utility - ORG	Pending	METHOD AND APPARATUS TO PROFILE RAM MEMORY OBJECTS FOR DISPLACEMENT WITH NONVOLATILE MEMORY	12/29/2008	12/345,306	US
NMXN-0114.00/US	Utility - ORG	Pending	MOLDING COMPOUND INCLUDING CARBON NANO-TUBE DISPERSION	12/23/2008	12/343,398	US
NMXN-0115.00/US	Utility - ORG	Pending	Nonvolatile/Volatile Memory Write System and Method	12/24/2008	12/343,485	US
NMXN-0116.00/US	Utility - NSPCT	Pending	MEMORY DEVICE AND METHOD OF FABRICATING THEREOF	6/30/2011	13/143,061	US
NMXN-0118.00/US	Utility - NSPCT	Pending	WEAR LEAVING FOR ERASABLE MEMORIES	6/28/2011	13/142,487	US
NMXN-0119.00/US	Utility - ORG	Pending	HIGH RATE SELECTIVE POLYMER GROWTH ON A SUBSTRATE	3/31/2009	12/416,094	US
NMXN-0123.00/US	Utility - ORG	Pending	METHOD AND APPARATUS FOR ACTIVE RANGE MAPPING FOR A NONVOLATILE MEMORY DEVICE	12/31/2008	12/347,983	US

Exhibit B - US Applications

MTI Docket No.	Matter Type	Matter Status	The Matter	Filed Date	Serial No.	Country
NMXN-0124.00/US	Utility - ORG	Allowed	PROVIDING A READY-BUSY SIGNAL FROM A NON-VOLATILE MEMORY DEVICE TO A MEMORY CONTROLLER	12/16/2008	12/335,587	US
NMXN-0125.00/US	Utility - ORG	Pending	NON-VOLATILE MEMORY DEVICE CAPABLE OF INITIATING TRANSACTIONS	12/10/2008	12/331,772	US
NMXN-0127.00/US	Utility - ORG	Pending	PARTITIONING ON A MANAGED MEMORY SYSTEM	1/12/2009	12/352,476	US
NMXN-0131.00/US	Utility - ORG	Pending	POWER-LOSS DEVICES HAVING NON-VOLATILE MEMORY	12/22/2008	12/341,959	US
NMXN-0133.00/US	Utility - ORG	Abandoned	LIMITING FLASH MEMORY OVER PROGRAMMING DURING PROGRAMMING	7/21/2009	12/506,934	US
NMXN-0134.00/US	Utility - ORG	Pending	METHOD AND DEVICES FOR CONTROLLING POWER LOSS	5/26/2009	12/472,153	US
NMXN-0137.00/US	Utility - ORG	Pending	PCM MEMORIES FOR STORAGE BUS INTERFACES	5/14/2009	12/466,366	US
NMXN-0138.00/US	Utility - ORG	Abandoned	Filling Gaps in Integrated Circuit Fabrication	3/26/2009	12/411,450	US
NMXN-0139.00/US	Utility - ORG	Pending	PHASE CHANGE MEMORY CELL WITH SELF-ALIGNED VERTICAL HEATER AND LOW RESISTIVITY INTERFACE	7/1/2009	12/496,503	US
NMXN-0140.00/US	Utility - ORG	Pending	CONFIGURABLE PARTITIONS FOR NON-VOLATILE MEMORY	5/28/2009	12/473,629	US
NMXN-0142.00/US	Utility - ORG	Pending	PARTITIONING PROCESS TO IMPROVE MEMORY CELL RETENTION	10/1/2009	12/572,182	US
NMXN-0143.00/US	Utility - ORG	Pending	NON-VOLATILE MEMORY	7/30/2009	12/512,907	US
NMXN-0147.00/US	Utility - ORG	Pending	LIMITING FLASH MEMORY OVER PROGRAMMING	6/23/2009	12/490,002	US
NMXN-0151.00/US	Utility - ORG	Pending	PASSWORD ACCESSIBLE MICROELECTRONIC MEMORY	3/26/2009	12/411,453	US
NMXN-0157.00/US	Utility - ORG	Pending	ERROR CORRECTING CODES FOR INCREASED STORAGE CAPACITY IN MULTILEVEL MEMORY DEVICES	6/10/2009	12/482,400	US
NMXN-0158.00/US	Utility - ORG	Abandoned	CHARGE PUMP CIRCUIT AND METHOD	5/5/2009	12/387,655	US
NMXN-0159.00/US	Utility - ORG	Pending	NON-VOLATILE MEMORY WITH BI-DIRECTIONAL ERROR CORRECTION PROTECTION	5/18/2009	12/467,965	US
NMXN-0160.00/US	Utility - ORG	Pending	NON-VOLATILE MEMORY WITH EXTENDED ERROR CORRECTION PROTECTION	4/23/2009	12/427,706	US
NMXN-0161.00/US	Utility - ORG	Allowed	METHOD FOR WRITING TO AND ERASING A NON-VOLATILE MEMORY	4/14/2009	12/423,226	US
NMXN-0162.00/US	Utility - ORG	Pending	METHOD AND APPARATUS FOR IMPROVING MEMORY TRANSLATION TABLE PERFORMANCE FOR ELECTRICALLY WRITABLE MEMORY DEVICES	5/5/2009	12/435,885	US
NMXN-0163.00/US	Utility - ORG	Pending	METHOD AND APPARATUS FOR ELIMINATING FILE SYSTEM MOUNT TIME OR INITIALIZATION TIME IN ELECTRICALLY WRITABLE MEMORY DEVICES	5/5/2009	12/435,804	US
NMXN-0165.00/US	Utility - ORG	Pending	METHOD AND APPARATUS FOR PREVENTING FOREGROUND ERASE OPERATIONS IN ELECTRICALLY WRITABLE MEMORY DEVICES	5/5/2009	12/435,986	US
NMXN-0166.00/US	Utility - ORG	Pending	ENABLING A SECURE BOOT FROM NON-VOLATILE MEMORY	3/26/2009	12/411,784	US
NMXN-0167.00/US	Utility - ORG	Pending	METHOD FOR OPERATING A PORTION OF AN EXECUTABLE PROGRAM IN AN EXECUTABLE NON-VOLATILE MEMORY	6/2/2009	12/477,017	US
NMXN-0172.00/US	Utility - ORG	Pending	FULL CHIP WEAR LEVELING IN MEMORY DEVICE	8/26/2009	12/548,375	US
NMXN-0177.00/US	Utility - ORG	Pending	STACKED PACKAGE MEMORY MODULE	6/30/2010	12/828,217	US
NMXN-0185.00/US	Utility - ORG	Pending	ENHANCED MULTILEVEL MEMORY	5/19/2010	12/783,483	US
NMXN-0187.00/US	Utility - ORG	Pending	CHARGE TRAP MEMORY HAVING LIMITED CHARGE DIFFUSION	4/20/2010	12/764,063	US
NMXN-0192.00/US	Utility - ORG	Pending	AUTHENTICATED OPERATIONS AND EVENT COUNTERS	11/25/2009	12/625,716	US
NMXN-0193.00/US	Utility - ORG	Pending	NON-VOLATILE SRAM CELL THAT INCORPORATES PHASE-CHANGE MEMORY INTO A CMOS PROCESS	10/12/2009	12/577,631	US

Exhibit B - US Applications

WIPO No.	Patent Type	Matter Status	Title	File Date	Serial No.	Country
NMXN-0256.00/US	Utility - ORG	Pending	WRITE PERFORMANCE OF PHASE CHANGE MEMORY USING SET-PULSE SHAPING	11/23/2009	12/623,926	US
NMXN-0257.00/US	Utility - ORG	Pending	SELF-ADAPTIVE SOLID STATE DRIVE CONTROLLER	12/30/2009	12/649,954	US
NMXN-0258.00/US	Utility - ORG	Pending	SOLID STATE DRIVE CONTROLLER	12/30/2009	12/650,035	US
NMXN-0259.00/US	Utility - ORG	Pending	PACKAGE INCLUDING AT LEAST ONE TOPOLOGICAL FEATURE ON AN ENCAPSULANT MATERIAL TO RESIST OUT-OF-PLANE DEFORMATION	11/30/2009	12/628,042	US
NMXN-0261.00/US	Utility - ORG	Pending	SERIAL PARALLEL INTERFACE FOR DATA WORD ARCHITECTURE	12/28/2009	12/647,879	US
NMXN-0262.00/US	Utility - ORG	Pending	PCM AND NAND SYSTEM WITH VERY SHORT LATENCY FOR XIP SSD	10/14/2010	12/904,807	US
NMXN-0271.00/US	Utility - ORG	Pending	WRITE OPERATION FOR PHASE CHANGE MEMORY	7/29/2010	12/846,775	US
NMXN-0276.00/US	Utility - ORG	Pending	VIA FORMATION FOR CROSS-POINT MEMORY	12/29/2009	12/648,979	US
NMXN-0280.00/US	Utility - ORG	Pending	READ DISTRIBUTION MANAGEMENT FOR PHASE CHANGE MEMORY	10/15/2010	12/905,754	US
NMXN-0284.00/US	Utility - ORG	Pending	PROGRAMMING AN ARRAY OF RESISTANCE RANDOM ACCESS MEMORY CELLS USING UNIPOLAR PULSES	1/14/2011	13/007,544	US
NMXN-0285.00/US	Utility - ORG	Pending	FLASH MEMORY HAVING MULTI-LEVEL ARCHITECTURE	4/20/2010	12/764,060	US
NMXN-0286.00/US	Utility - ORG	Pending	SECURE MEMORY DEVICE ERASE	4/27/2010	12/768,345	US
NMXN-0287.00/US	Utility - ORG	Pending	CODE PATCHING FOR NON-VOLATILE MEMORY	11/30/2010	12/956,660	US
NMXN-0289.00/US	Utility - ORG	Pending	FILLING CAVITIES IN SEMICONDUCTOR STRUCTURES	11/18/2010	12/948,897	US
NMXN-0290.00/US	Utility - ORG	Pending	MANAGED HYBRID MEMORY WITH ADAPTIVE POWER SUPPLY	7/30/2010	12/847,807	US
NMXN-0294.00/US	Utility - ORG	Pending	CAPACITY TO HIGHLY IMPROVE APPLICATION THROUGHPUT IN TIMESHIFTING APPLICATIONS ON MEMORY DEVICES	10/15/2010	12/905,238	US
NMXN-0295.00/US	Utility - ORG	Pending	FORMING DIFFERENT METALIZATIONS IN DIFFERENT AREAS OF A SEMICONDUCTOR DIE	5/19/2010	12/782,805	US
NMXN-0297.00/US	Utility - ORG	Pending	SIGNAL LINE TO INDICATE PROGRAM-FAIL IN MEMORY	4/29/2010	12/770,599	US
NMXN-0298.00/US	Utility - ORG	Pending	PRESERVING DATA INTEGRITY IN A MEMORY SYSTEM	11/30/2010	12/957,364	US
NMXN-0304.00/US	Utility - ORG	Pending	VERIFY OR READ PULSE FOR PHASE CHANGE MEMORY AND SWITCH	11/30/2010	12/956,853	US
NMXN-0305.00/US	Utility - ORG	Pending	EXTENDED INTERFACE COMMANDS	12/30/2010	12/982,847	US
NMXN-0306.00/US	Utility - ORG	Pending	RELIABLE WRITE FOR NON-VOLATILE MEMORY	11/30/2010	12/956,742	US
NMXN-0311.00/US	Utility - ORG	Pending	THERMAL TREATMENT OF FLASH MEMORIES	5/5/2011	13/101,322	US
NMXN-0312.00/US	Utility - ORG	Pending	MULTI-LAYER FLASH MEMORY	4/20/2010	12/764,054	US
NMXN-0313.00/US	Utility - ORG	Pending	ERROR DETECTION OR CORRECTION OF A PORTION OF A CODEWORD IN A MEMORY DEVICE	9/30/2010	12/894,920	US
NMXN-0315.00/US	Utility - ORG	Pending	RESISTIVE RANDOM ACCESS MEMORY	7/15/2010	12/836,661	US
NMXN-0316.00/US	Utility - ORG	Pending	FORMING ELECTRODES FOR CHALCOGENIDE CONTAINING DEVICES	5/11/2010	12/777,419	US
NMXN-0317.00/US	Utility - ORG	Pending	WORDLINE DRIVER FOR MEMORY	5/13/2010	12/779,752	US
NMXN-0319.00/US	Utility - ORG	Pending	STAGGERED PROGRAMMING FOR RESISTIVE MEMORIES	5/13/2010	12/779,728	US
NMXN-0326.00/US	Utility - ORG	Pending	MEMORY APPARATUS AND SYSTEM WITH SHARED WORDLINE DECODER	4/12/2011	13/085,454	US
NMXN-0329.00/US	Utility - ORG	Pending	PARTITIONED BITLINE FOR MEMORY	4/30/2010	12/772,147	US
NMXN-0332.00/US	Utility - ORG	Pending	FORMING RESISTIVE RANDOM ACCESS MEMORIES TOGETHER WITH FUSE ARRAYS	8/4/2010	12/849,864	US

PATENT

REEL: 027046 FRAME: 0049

Exhibit B - US Applications

App No.	App Title	App Status	App Type	App Date	App No.	Country
NMXN-0335.00/US	CHALCOGENIDE CONTAINING SEMICONDUCTORS WITH CHALCOGENIDE GRADIENT	Pending	Utility - ORG	11/11/2010	12/944,119	US
NMXN-0339.00/US	REDUCING CHALCOGENIDE EROSION DURING DRY ETCHING	Pending	Utility - ORG	5/6/2010	12/774,772	US
NMXN-0342.00/US	CHARGE TRAP NON-VOLATILE MEMORY	Pending	Utility - ORG	11/11/2010	12/944,125	US
NMXN-0344.00/US	ORGANIZATION AND LAYOUT OF CLAMPING ELEMENTS IN BIT/DIODE SELECTED ARRAY CREATED WITH SADP TECHNIQUES	Pending	Utility - ORG	12/28/2010	12/979,461	US
NMXN-0346.00/US	METHOD AND APPARATUS TO PERFORM CONCURRENT READ AND WRITE MEMORY OPERATIONS	Pending	Utility - ORG	11/16/2010	12/947,771	US
NMXN-0347.00/US	Forming High Aspect Ratio Isolation Structures	Pending	Utility - ORG	4/11/2011	13/083,836	US
NMXN-0349.00/US	FORMING HEATERS FOR PHASE CHANGE MEMORIES	Pending	Utility - ORG	11/11/2010	12/944,134	US
NMXN-0350.00/US	SELF-CHECK CALIBRATION OF PROGRAM OR ERASE AND VERIFY PROCESS USING MEMORY CELL DISTRIBUTION	Pending	Utility - ORG	3/16/2011	13/049,717	US
NMXN-0351.00/US	CONCURRENT READ AND WRITE MEMORY OPERATIONS IN A SERIAL INTERFACE MEMORY	Pending	Utility - ORG	11/16/2010	12/947,781	US
NMXN-0356.00/US	READING MEMORY CELL HISTORY DURING PROGRAM OPERATION FOR ADAPTIVE PROGRAMMING	Pending	Utility - ORG	3/3/2011	13/040,200	US
NMXN-0357.00/US	POST DEPOSITION ADJUSTMENT OF CHALCOGENIDE COMPOSITION IN CHALCOGENIDE CONTAINING SEMICONDUCTORS	Pending	Utility - ORG	11/11/2010	12/942,132	US
NMXN-0358.00/US	CACHE COHERENCE PROTOCOL FOR PERSISTENT MEMORIES	Pending	Utility - ORG	6/9/2010	12/797,522	US
NMXN-0363.00/US	PERSISTENT MEMORY FOR PROCESSOR MAIN MEMORY	Pending	Utility - ORG	6/9/2010	12/797,512	US
NMXN-0364.00/US	SUB-BLOCK ACCESSIBLE NONVOLATILE MEMORY CACHE	Pending	Utility - ORG	11/22/2010	12/951,604	US
NMXN-0366.00/US	METADATA STORAGE ASSOCIATED WITH FLASH TRANSLATION LAYER	Pending	Utility - ORG	11/4/2010	12/939,953	US
NMXN-0369.00/US	PHASE CHANGE MEMORY STATE DETERMINATION USING THRESHOLD EDGE DETECTION	Pending	Utility - ORG	9/23/2010	12/889,372	US
NMXN-0372.00/US	Continuous Page Read mode in parallel interface non-volatile flash memory (IDF TITLE)	Pending	Utility - ORG	8/18/2011	13/202,326	US
NMXN-0378.00/US	SECURITY PROTECTION FOR MEMORY CONTENT OF PROCESSOR MAIN MEMORY	Pending	Utility - ORG	9/30/2010	12/895,574	US
NMXN-0379.00/US	VALIDATING PERSISTENT MEMORY CONTENT FOR PROCESSOR MAIN MEMORY	Pending	Utility - ORG	9/30/2010	12/895,627	US
NMXN-0381.00/US	OPERATION COMMAND WITH PARAMETER	Pending	Utility - ORG	11/18/2010	12/949,728	US
NMXN-0382.00/US	MULTI-CHANNEL MEMORY WITH EMBEDDED CHANNEL SELECTION	Pending	Utility - ORG	11/16/2010	12/947,785	US
NMXN-0383.00/US	SUPPLY VOLTAGE OR GROUND CONNECTIONS INCLUDING BOND PAD INTERCONNECTS FOR INTEGRATED CIRCUIT DEVICE	Pending	Utility - ORG	3/31/2010	12/751,695	US
NMXN-0402.00/US	Method and Apparatus for Efficient Memory Placement	Pending	Utility - CIP	4/30/2009	12/433,763	US
NMXN-0405.00/US	HIERARCHICAL MEMORY ARCHITECTURE TO CONNECT MASS STORAGE DEVICES	Pending	Utility - ORG	3/31/2009	12/415,991	US
NMXN-0406.00/US	HIERARCHICAL MEMORY ARCHITECTURE USING A CONCENTRATOR DEVICE	Pending	Utility - ORG	3/31/2009	12/416,000	US
NMXN-0407.00/US	HIERARCHICAL MEMORY ARCHITECTURE WITH AN INTERFACE TO DIFFERING MEMORY FORMATS	Pending	Utility - ORG	3/31/2009	12/416,008	US

Exhibit B - US Applications

Patent No.	Matter Type	Matter Status	Title	File Date	Serial No.	Country
NMXN-0408.00/US	Utility - ORG	Pending	HIERARCHICAL MEMORY ARCHITECTURE WITH A PHASE-CHANGE MEMORY (PCM) CONTENT ADDRESSABLE MEMORY (CAM)	3/31/2009	12/416,022	US
NMXN-0409.00/US	Utility - ORG	Pending	MEMORY DEVICE FOR A HIERARCHICAL MEMORY ARCHITECTURE	6/9/2009	12/483,198	US
NMXN-0410.00/US	Utility - ORG	Pending	AUTONOMOUS MEMORY ARCHITECTURE	9/11/2009	12/557,776	US
NMXN-0411.00/US	Utility - ORG	Pending	AUTONOMOUS SUBSYSTEM ARCHITECTURE	9/11/2009	12/557,856	US
NMXN-0412.00/US	Utility - ORG	Pending	SIGNAL SHIFTING TO ALLOW INDEPENDENT CONTROL OF IDENTICAL STACKED MEMORY MODULES	5/8/2009	12/463,329	US
NMXN-0413.00/US	Utility - ORG	Pending	DEDICATED INTERFACE TO FACTORY PROGRAM PHASE-CHANGE MEMORIES	5/11/2009	12/464,036	US
NMXN-0414.00/US	Utility - ORG	Pending	WIRELESS INTERFACE TO PROGRAM PHASE-CHANGE MEMORIES	5/13/2009	12/465,506	US
NMXN-0415.00/US	Utility - ORG	Pending	HIGH-SPEED WIRELESS SERIAL COMMUNICATION LINK FOR A STACKED DEVICE CONFIGURATION USING NEAR FIELD COUPLING	8/17/2009	12/542,528	US
NMXN-0416.00/US	Utility - ORG	Pending	MEMORY DEVICES HAVING DATA FLOW PIPELINING	3/31/2009	12/415,973	US
NMXN-0417.00/US	Utility - ORG	Pending	Persistent Content in Nonvolatile Memory	12/15/2009	12/638,950	US
NMXN-0418.00/US	Utility - ORG	Pending	SUB-OS VIRTUAL MEMORY MANAGEMENT LAYER	12/31/2009	12/650,952	US
NMXN-0419.00/US	Utility - ORG	Pending	NON-VOLATILE THERMAL BUDGET SENSING PHASE CHANGE MEMORY CELL	12/30/2009	12/655,377	US
NMXN-0420.00/US	Utility - ORG	Pending	PHASE CHANGE MEMORY IN A DUAL INLINE MEMORY MODULE	7/16/2009	12/504,029	US
NMXN-0421.00/US	Utility - ORG	Pending	USING OUT-OF-BAND SIGNALING TO COMMUNICATE WITH DAISY CHAINED NONVOLATILE MEMORIES	5/8/2009	12/463,337	US
NMXN-0422.00/US	Utility - ORG	Pending	DAISY CHAINING NONVOLATILE MEMORIES	8/12/2009	12/539,771	US
NMXN-0423.00/US	Utility - ORG	Pending	WIRELESS COMMUNICATION LINK USING NEAR FIELD COUPLING	12/11/2009	12/635,961	US
NMXN-0424.00/US	Utility - ORG	Pending	NONVOLATILE MEMORY INTERNAL SIGNATURE GENERATION	12/15/2009	12/638,953	US
NMXN-0425.00/US	Utility - ORG	Pending	NONVOLATILE STORAGE WITH DISPARATE MEMORY TYPES	1/5/2010	12/652,666	US
NMXN-0427.00/US	Utility - ORG	Pending	FLASH TRANSLATION LAYER USING PHASE CHANGE MEMORY	12/15/2009	12/638,956	US
NMXN-0428.00/US	Utility - ORG	Pending	PHASE-CHANGE MEMORY TEMPERATURE SENSITIVE DETECTOR	5/11/2009	12/464,011	US
NMXN-0429.00/US	Utility - ORG	Pending	DUAL MODE CLOCK AND DATA SCHEME FOR MEMORY PROGRAMMING	9/11/2009	12/557,723	US
NMXN-0430.00/US	Utility - ORG	Pending	SUPPLY VOLTAGE OR GROUND CONNECTIONS FOR INTEGRATED CIRCUIT DEVICE	12/31/2009	12/651,122	US
NMXN-0431.00/US	Utility - ORG	Pending	KEYHOLE-FREE SLOPED HEATER FOR PHASE CHANGE MEMORY	11/30/2009	12/627,443	US
NMXN-0432.00/US	Utility - ORG	Pending	INDEXED REGISTER ACCESS FOR MEMORY DEVICE	10/29/2010	12/916,421	US
NMXN-0433.00/US	Utility - ORG	Pending	Transactional Memory (IDF TITLE)	1/27/2011	13/015,396	US
NMXN-0434.00/US	Utility - ORG	Pending	ERROR CONTROL IN MEMORY SYSTEMS	2/28/2011	13/037,298	US
NMXN-0436.00/US	Utility - ORG	Pending	SET PULSE FOR PHASE CHANGE MEMORY PROGRAMMING	6/8/2011	13/156,293	US
NMXN-0437.00/US	Utility - ORG	Pending	BARRIER LAYER FOR INTEGRATED CIRCUIT CONTACTS	4/11/2011	13/083,868	US
NMXN-0438.00/US	Utility - ORG	Pending	Optimized Read-While-Write for XIP Devices (IDF TITLE)	8/31/2011	13/223,171	US
NMXN-0441.00/US	Utility - ORG	Pending	Snap-back sensing (IDF TITLE)	8/18/2011	13/213,018	US
NMXN-0442.00/US	Utility - ORG	Pending	MEMORY DEVICE HAVING THREE DIMENSIONAL GATE STRUCTURE	8/26/2010	12/869,569	US

PATENT

REEL: 027046 FRAME: 0051

Exhibit C - Foreign Applications and Patents

MT-ORIGIN	Matter Type	Matter Status	Title	Filing Date	Serial No.	Country
NIMXN-0031.00/CN	Utility - ORG	Pending	PROTECTIVE THIN FILM COATING IN CHIP PACKAGING	11/13/2009	200910222340.8	CN
NIMXN-0034.00/CN	Utility - ORG	Abandoned	A SEMICONDUCTOR PACKAGE SUBSTRATE WITH METAL BUMPS	11/13/2009	200910222341.2	CN
NIMXN-0036.00/CN	Utility - ORG	Pending	INTEGRATING DIVERSE TRANSISTORS ON THE SAME WAFER	12/18/2009	200910262463.4	CN
NIMXN-0038.00/CN	Utility - ORG	Pending	MULTI-DIE BUILDING BLOCK FOR STACKED-DIE PACKAGE	11/13/2009	200910222337.6	CN
NIMXN-0041.00/CN	Utility - ORG	Pending	CONTROLLING THE CIRCUITRY AND MEMORY ARRAY RELATIVE HEIGHT IN A PHASE CHANGE MEMORY FEOL PROCESS FLOW	11/11/2009	200910221640.4	CN
NIMXN-0043.00/CN	Utility - NSPCT	Pending	REVERSING A POTENTIAL POLARITY FOR READING PHASE-CHANGE CELLS TO SHORTEN A RECOVERY DELAY AFTER PROGRAMMING	7/29/2008	200880129821.8	CN
NIMXN-0063.00/CN	Utility - NSPCT	Pending	NON-VOLATILE MEMORY WITH EXTENDED OPERATING TEMPERATURE RANGE	8/28/2009	200880006540.3	CN
NIMXN-0081.00/CN	Utility - ORG	Pending	RECOVERY FOR NON-VOLATILE MEMORY AFTER POWER LOSS	11/13/2009	200910222338	CN
NIMXN-0083.00/CN	Utility - ORG	Pending	METHOD OF HIGH ASPECT RATIO PLUG FILL	11/13/2009	200910222342.7	CN
NIMXN-0086.00/CN	Utility - NSPCT	Pending	VIRTUALIZED ECC NAND	10/9/2008	200880131514.3	CN
NIMXN-0103.00/CN	Utility - NSPCT	Pending	ENHANCED ADDRESSABILITY FOR SERIAL NON-VOLATILE MEMORY	12/30/2008	200880132558.8	CN
NIMXN-0104.00/CN	Utility - ORG	Pending	SELF-SELECTING PCM DEVICE NOT REQUIRING A DEDICATED SELECTOR TRANSISTOR	11/16/2010	201010548105.2	CN
NIMXN-0106.00/CN	Utility - NSPCT	Pending	TEMPERATURE ALERT AND LOW RATE REFRESH FOR A NON-VOLATILE MEMORY	12/30/2008	20088013259.1	CN
NIMXN-0112.00/CN	Utility - ORG	Pending	METHOD AND APPARATUS TO PROFILE RAM MEMORY OBJECTS FOR DISPLACEMENT WITH NONVOLATILE MEMORY	11/12/2009	200910212366.4	CN
NIMXN-0124.00/CN	Utility - ORG	Pending	PROVIDING A READY-BUSY SIGNAL FROM A NON-VOLATILE MEMORY DEVICE TO A MEMORY CONTROLLER	12/15/2009	200910260421.7	CN
NIMXN-0125.00/CN	Utility - ORG	Pending	NON-VOLATILE MEMORY DEVICE CAPABLE OF INITIATING TRANSACTIONS	12/11/2009	200910246794.9	CN
NIMXN-0134.00/CN	Utility - ORG	Pending	METHOD AND DEVICES FOR CONTROLLING POWER LOSS	3/26/2010	201010186885	CN
NIMXN-0137.00/CN	Utility - ORG	Pending	PCM MEMORIES FOR STORAGE BUS INTERFACES	5/12/2010	201010172633.2	CN
NIMXN-0139.00/CN	Utility - ORG	Pending	PHASE CHANGE MEMORY CELL WITH SELF-ALIGNED VERTICAL HEATER AND LOW RESISTIVITY INTERFACE	6/29/2010	201010215301.8	CN
NIMXN-0157.00/CN	Utility - ORG	Pending	ERROR CORRECTING CODES FOR INCREASED STORAGE CAPACITY IN MULTILEVEL MEMORY DEVICES	6/8/2010	201010198478.1	CN
NIMXN-0158.00/CN	Utility - ORG	Abandoned	CHARGE PUMP CIRCUIT AND METHOD	5/5/2010	201010165724.3	CN
NIMXN-0161.00/CN	Utility - ORG	Pending	METHOD FOR WRITING TO AND ERASING A NON-VOLATILE MEMORY	4/14/2010	201010148434.8	CN
NIMXN-0167.00/CN	Utility - ORG	Pending	METHOD FOR OPERATING A PORTION OF AN EXECUTABLE PROGRAM IN AN EXECUTABLE NON-VOLATILE MEMORY	5/31/2010	201010188526.9	CN
NIMXN-0185.00/CN	Utility - ORG	Pending	ENHANCED MULTILEVEL MEMORY	5/18/2011	201110136221.8	CN
NIMXN-0187.00/CN	Utility - ORG	Pending	CHARGE TRAP MEMORY HAVING LIMITED CHARGE DIFFUSION	4/20/2011	201110103430.2	CN
NIMXN-0193.00/CN	Utility - ORG	Pending	NON-VOLATILE SRAM CELL THAT INCORPORATES PHASE-CHANGE MEMORY INTO A CMOS PROCESS	10/12/2010	201010508771.3	CN
NIMXN-0198.00/CN	Utility - ORG	Pending	MEMORY BUFFER HAVING ACCESSIBLE INFORMATION AFTER A PROGRAM-FAIL	5/12/2011	201110126802.3	CN
NIMXN-0215.00/CN	Utility - ORG	Pending	MEMORY DEVICE WITH CUSTOMIZABLE ECC CORRECTION AND CLOSE TO ANALOG SENSING	11/12/2010	201010546314.3	CN
NIMXN-0233.00/CN	Utility - ORG	Pending	PACKAGE INCLUDING AN UNDERFILL MATERIAL IN A PORTION OF AN AREA BETWEEN THE PACKAGE AND A SUBSTRATE OR ANOTHER PACKAGE	11/26/2010	201010587429.7	CN

Exhibit C - Foreign Applications and Patents

Patent No.	Patent Type	Patent Status	Title	File Date	Serial No.	Country
NMXN-0246.00/CN	Utility - ORG	Pending	BIT ERROR THRESHOLD AND REMAPPING A MEMORY DEVICE	6/24/2010	201010214152.3	CN
NMXN-0248.00/CN	Utility - ORG	Pending	NON-VOLATILE MEMORY TO STORE MEMORY REMAP INFORMATION	6/29/2010	201010219745.9	CN
NMXN-0250.00/CN	Utility - ORG	Pending	BIT ERROR THRESHOLD AND CONTENT ADDRESSABLE MEMORY TO ADDRESS A REMAPPED MEMORY DEVICE	6/28/2010	201010218341.8	CN
NMXN-0255.00/CN	Utility - ORG	Pending	STORED MULTI-BIT CHARACTERIZED BY MULTIPLE-DIMENSIONAL MEMORY STATES	11/29/2010	201010568301.6	CN
NMXN-0271.00/CN	Utility - ORG	Pending	WRITE OPERATION FOR PHASE CHANGE MEMORY	7/27/2011	201110219273.1	CN
NMXN-0285.00/CN	Utility - ORG	Pending	FLASH MEMORY HAVING MULTI-LEVEL ARCHITECTURE	4/20/2011	201110103423.2	CN
NMXN-0290.00/CN	Utility - ORG	Pending	MANAGED HYBRID MEMORY WITH ADAPTIVE POWER SUPPLY	7/28/2011	201110219308.1	CN
NMXN-0297.00/CN	Utility - ORG	Pending	SIGNAL LINE TO INDICATE PROGRAM-FAIL IN MEMORY	4/28/2011	201110113612.8	CN
NMXN-0316.00/CN	Utility - ORG	Pending	FORMING ELECTRODES FOR CHALCOGENIDE CONTAINING DEVICES	5/11/2011		CN
NMXN-0317.00/CN	Utility - ORG	Pending	WORDLINE DRIVER FOR MEMORY	5/12/2011	201110126767.5	CN
NMXN-0319.00/CN	Utility - ORG	Pending	STAGGERED PROGRAMMING FOR RESISTIVE MEMORIES	5/12/2011	201110126770.7	CN
NMXN-0332.00/CN	Utility - ORG	Pending	FORMING RESISTIVE RANDOM ACCESS MEMORIES TOGETHER WITH FUSE ARRAYS	8/3/2011	201110225660.6	CN
NMXN-0358.00/CN	Utility - ORG	Pending	CACHE COHERENCE PROTOCOL FOR PERSISTENT MEMORIES	6/9/2011	201110159791.9	CN
NMXN-0363.00/CN	Utility - ORG	Pending	PERSISTENT MEMORY FOR PROCESSOR MAIN MEMORY	6/9/2011	201110159792.3	CN
NMXN-0403.00/CN	Utility - ORG	Pending	METHOD FOR LOW POWER ACCESSING A PHASE CHANGE MEMORY DEVICE	11/11/2009	200910221645.7	CN
NMXN-0404.00/CN	Utility - ORG	Pending	METHOD FOR LOW-STRESS MULTILEVEL READING OF PHASE CHANGE MEMORY CELLS AND MULTILEVEL PHASE CHANGE MEMORY	11/13/2009	200910207182.9	CN
NMXN-0405.00/CN	Utility - ORG	Pending	HIERARCHICAL MEMORY ARCHITECTURE TO CONNECT MASS STORAGE DEVICES	3/31/2010	201010187336.5	CN
NMXN-0409.00/CN	Utility - ORG	Pending	MEMORY DEVICE FOR A HIERARCHICAL MEMORY ARCHITECTURE	8/27/2009	200910171261.9	CN
NMXN-0410.00/CN	Utility - ORG	Pending	AUTONOMOUS MEMORY ARCHITECTURE	7/9/2010	201010225267.2	CN
NMXN-0411.00/CN	Utility - ORG	Pending	AUTONOMOUS SUBSYSTEM ARCHITECTURE	7/8/2010	201010226463.1	CN
NMXN-0413.00/CN	Utility - ORG	Pending	DEDICATED INTERFACE TO FACTORY PROGRAM PHASE-CHANGE MEMORIES	5/11/2010	201010195158	CN
NMXN-0414.00/CN	Utility - ORG	Pending	WIRELESS INTERFACE TO PROGRAM PHASE-CHANGE MEMORIES	5/13/2010	201010195183.9	CN
NMXN-0415.00/CN	Utility - ORG	Pending	HIGH-SPEED WIRELESS SERIAL COMMUNICATION LINK FOR A STACKED DEVICE CONFIGURATION USING NEAR FIELD COUPLING	7/8/2010	201010226532.9	CN
NMXN-0420.00/CN	Utility - ORG	Pending	PHASE CHANGE MEMORY IN A DUAL INLINE MEMORY MODULE	7/5/2010	201010222395.1	CN
NMXN-0431.00/CN	Utility - ORG	Pending	KEYHOLE-FREE SLOPED HEATER FOR PHASE CHANGE MEMORY	11/29/2010	201010568302.0	CN
NMXN-0432.00/CN	Utility - ORG	Pending	RPMB OTP REGISTER PROGRAM AND READ	5/3/2011	201110115893.0	CN
NMXN-0031.00/DE	Utility - ORG	Abandoned	PROTECTIVE THIN FILM COATING IN CHIP PACKAGING	10/30/2009	102009051342.6	DE
NMXN-0034.00/DE	Utility - ORG	Abandoned	A SEMICONDUCTOR PACKAGE SUBSTRATE WITH METAL BUMPS	10/27/2009	10 2009 050 743.4	DE
NMXN-0036.00/DE	Utility - ORG	Pending	INTEGRATING DIVERSE TRANSISTORS ON THE SAME WAFER	11/22/2009	10 2009 059 970.3-33	DE
NMXN-0038.00/DE	Utility - ORG	Pending	MULTI-DIE BUILDING BLOCK FOR STACKED-DIE PACKAGE	10/27/2009	10 2009 050 744.2	DE
NMXN-0041.00/DE	Utility - ORG	Pending	CONTROLLING THE CIRCULARITY AND MEMORY ARRAY RELATIVE HEIGHT IN A PHASE CHANGE MEMORY FEOL PROCESS FLOW	10/30/2009	10/2009 051 341.8	DE
NMXN-0043.00/DE	Utility - NSPCT	Pending	REVERSING A POTENTIAL POLARITY FOR READING PHASE-CHANGE CELLS TO SHORTEN A RECOVERY DELAY AFTER PROGRAMMING	7/29/2008	112010000 015.8	DE
NMXN-0063.00/DE	Utility - NSPCT	Pending	NON-VOLATILE MEMORY WITH EXTENDED OPERATING TEMPERATURE RANGE	12/30/2008	112008000 0968.6	DE
NMXN-0081.00/DE	Utility - ORG	Pending	RECOVERY FOR NON-VOLATILE MEMORY AFTER POWER LOSS	11/4/2009	10 2009 051 862.2	DE

Exhibit C - Foreign Applications and Patents

Patent No.	App No.	App Type	App Status	App Title	File Date	Serial No.	Country
NMXN-0083.00/DE		Utility - ORG	Pending	METHOD OF HIGH ASPECT RATIO PLUG FILL	11/27/2009	102009052393.6	DE
NMXN-0086.00/DE		Utility - NSPCT	Pending	VIRTUALIZED ECC NAND	10/9/2008	112008004033.8	DE
NMXN-0104.00/DE		Utility - ORG	Pending	SELF-SELECTING PCM DEVICE NOT REQUIRING A DEDICATED SELECTOR TRANSISTOR	11/25/2010	10 2010 052 598.2	DE
NMXN-0112.00/DE		Utility - ORG	Pending	METHOD AND APPARATUS TO PROFILE RAM MEMORY OBJECTS FOR DISPLACEMENT WITH NONVOLATILE MEMORY	10/30/2009	102009051339.6	DE
NMXN-0124.00/DE		Utility - ORG	Pending	PROVIDING A READY-BUSY SIGNAL FROM A NON-VOLATILE MEMORY DEVICE TO A MEMORY CONTROLLER	12/15/2009	102009058348.3	DE
NMXN-0125.00/DE		Utility - ORG	Pending	NON-VOLATILE MEMORY DEVICE CAPABLE OF INITIATING TRANSACTIONS	12/9/2009	102009057547.2	DE
NMXN-0134.00/DE		Utility - ORG	Pending	METHOD AND DEVICES FOR CONTROLLING POWER LOSS	5/5/2010	10 2010 019 487.5-55	DE
NMXN-0137.00/DE		Utility - ORG	Pending	PCM MEMORIES FOR STORAGE BUS INTERFACES	4/29/2010	10 2010 018 765.8-53	DE
NMXN-0139.00/DE		Utility - ORG	Pending	PHASE CHANGE MEMORY CELL WITH SELF-ALIGNED VERTICAL HEATER AND LOW RESISTIVITY INTERFACE	6/16/2010	10 2010 023 957.7	DE
NMXN-0157.00/DE		Utility - ORG	Pending	ERROR CORRECTING CODES FOR INCREASED STORAGE CAPACITY IN MULTILEVEL MEMORY DEVICES	5/26/2010	102010021516.3	DE
NMXN-0158.00/DE		Utility - ORG	Abandoned	CHARGE PUMP CIRCUIT AND METHOD	4/29/2010	10 2010 018 763.1	DE
NMXN-0161.00/DE		Utility - ORG	Pending	METHOD FOR WRITING TO AND ERASING A NON-VOLATILE MEMORY	4/13/2010	10 2010 014 781.8	DE
NMXN-0167.00/DE		Utility - ORG	Pending	METHOD FOR OPERATING A PORTION OF AN EXECUTABLE PROGRAM IN AN EXECUTABLE NON-VOLATILE MEMORY	5/5/2010	10 2010 019 486.7-53	DE
NMXN-0185.00/DE		Utility - ORG	Pending	ENHANCED MULTILEVEL MEMORY	5/17/2011	102011075966.2	DE
NMXN-0187.00/DE		Utility - ORG	Pending	CHARGE TRAP MEMORY HAVING LIMITED CHARGE DIFFUSION	4/20/2011	102011007747.2	DE
NMXN-0193.00/DE		Utility - ORG	Pending	NON-VOLATILE SRAM CELL THAT INCORPORATES PHASE-CHANGE MEMORY INTO A CMOS PROCESS	10/8/2010	10 2010 047 933.0	DE
NMXN-0215.00/DE		Utility - ORG	Pending	MEMORY DEVICE WITH CUSTOMIZABLE ECC CORRECTION AND CLOSE TO ANALOG SENSING	11/10/2010	10 2010 050 957.4	DE
NMXN-0223.00/DE		Utility - ORG	Pending	MODIFIED READ OPERATION FOR NON-VOLATILE MEMORY	8/23/2011	102011081438.8	DE
NMXN-0233.00/DE		Utility - ORG	Pending	PACKAGE INCLUDING AN UNDERFILL MATERIAL IN A PORTION OF AN AREA BETWEEN THE PACKAGE AND A SUBSTRATE OR ANOTHER PACKAGE	11/18/2010	102010051765.8	DE
NMXN-0246.00/DE		Utility - ORG	Pending	BIT ERROR THRESHOLD AND REMAPPING A MEMORY DEVICE	6/30/2010	102010030748.3	DE
NMXN-0248.00/DE		Utility - ORG	Pending	NON-VOLATILE MEMORY TO STORE MEMORY REMAP INFORMATION	6/30/2010	102010030745.9	DE
NMXN-0250.00/DE		Utility - ORG	Pending	BIT ERROR THRESHOLD AND CONTENT ADDRESSABLE MEMORY TO ADDRESS A REMAPPED MEMORY DEVICE	6/30/2010	102010030750.5	DE
NMXN-0255.00/DE		Utility - ORG	Pending	STORED MULTI-BIT CHARACTERIZED BY MULTIPLE-DIMENSIONAL MEMORY STATES	11/25/2010	102010060804.1	DE
NMXN-0285.00/DE		Utility - ORG	Pending	FLASH MEMORY HAVING MULTI-LEVEL ARCHITECTURE	4/20/2011	102011007746.4	DE
NMXN-0290.00/DE		Utility - ORG	Pending	MANAGED HYBRID MEMORY WITH ADAPTIVE POWER SUPPLY	7/27/2011	102011079910.9	DE
NMXN-0297.00/DE		Utility - ORG	Pending	SIGNAL LINE TO INDICATE PROGRAM-FAIL IN MEMORY	4/27/2011	102011017634.9	DE
NMXN-0316.00/DE		Utility - ORG	Pending	FORMING ELECTRODES FOR CHALCOGENIDE CONTAINING DEVICES	5/11/2011	10 2011 101 192.0	DE
NMXN-0317.00/DE		Utility - ORG	Pending	WORDLINE DRIVER FOR MEMORY	5/13/2011	102011075811.9	DE
NMXN-0319.00/DE		Utility - ORG	Pending	STAGGERED PROGRAMMING FOR RESISTIVE MEMORIES	5/13/2011	102011075966.2	DE
NMXN-0358.00/DE		Utility - ORG	Pending	CACHE COHERENCE PROTOCOL FOR PERSISTENT MEMORIES	6/1/2011	102011076895.5	DE
NMXN-0363.00/DE		Utility - ORG	Pending	PERSISTENT MEMORY FOR PROCESSOR MAIN MEMORY	6/1/2011	10 2011 076 894.7	DE

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Patent No.	Matte Type	Matter Status	Title	Filed Date	Serial No.	Country
NMXN-0369.00/DE	Utility - ORG	Pending	PHASE CHANGE MEMORY STATE DETERMINATION USING THRESHOLD EDGE DETECTION	9/6/2011	102011082182.1	DE
NMXN-0378.00/DE	Utility - ORG	Pending	SECURITY PROTECTION FOR MEMORY CONTENT OF PROCESSOR MAIN MEMORY	9/6/2011	102011082184.8	DE
NMXN-0403.00/DE	Utility - ORG	Pending	METHOD FOR LOW POWER ACCESSING A PHASE CHANGE MEMORY DEVICE	10/27/2009	10 2009 050 745.0	DE
NMXN-0404.00/DE	Utility - ORG	Pending	METHOD FOR LOW-STRESS MULTILEVEL READING OF PHASE CHANGE MEMORY CELLS AND MULTILEVEL PHASE CHANGE MEMORY	10/27/2009	102009050746.9	DE
NMXN-0405.00/DE	Utility - ORG	Pending	HIERARCHICAL MEMORY ARCHITECTURE TO CONNECT MASS STORAGE DEVICES	3/30/2010	10 2010 013 388.4	DE
NMXN-0409.00/DE	Utility - ORG	Pending	MEMORY DEVICE FOR A HIERARCHICAL MEMORY ARCHITECTURE	8/19/2009	10 2009 037 984.3	DE
NMXN-0410.00/DE	Utility - ORG	Pending	AUTONOMOUS MEMORY ARCHITECTURE	9/7/2010	10 2010 044 531.2-53	DE
NMXN-0411.00/DE	Utility - ORG	Pending	AUTONOMOUS SUBSYSTEM ARCHITECTURE	9/7/2010	10 2010 044 529.0	DE
NMXN-0413.00/DE	Utility - ORG	Pending	DEDICATED INTERFACE TO FACTORY PROGRAM PHASE-CHANGE MEMORIES	4/29/2010	10 2010 018 762.3-55	DE
NMXN-0414.00/DE	Utility - ORG	Pending	WIRELESS INTERFACE TO PROGRAM PHASE-CHANGE MEMORIES	4/29/2010	10 2010 018 766.6	DE
NMXN-0415.00/DE	Utility - ORG	Pending	HIGH-SPEED WIRELESS SERIAL COMMUNICATION LINK FOR A STACKED DEVICE CONFIGURATION USING NEAR FIELD COUPLING	8/4/2010	10 2010 033 342.5	DE
NMXN-0420.00/DE	Utility - ORG	Pending	PHASE CHANGE MEMORY IN A DUAL INLINE MEMORY MODULE	6/30/2010	102010030742.4	DE
NMXN-0431.00/DE	Utility - ORG	Pending	KEYHOLE-FREE SLOPED HEATER FOR PHASE CHANGE MEMORY	11/30/2010	102010060893.9	DE
NMXN-0432.00/DE	Utility - ORG	Pending	RPMB OTP REGISTER PROGRAM AND READ	4/29/2011	102011075023.1	DE
NMXN-0103.00/EP	Utility - NSPCT	Pending	ENHANCED ADDRESSABILITY FOR SERIAL NON-VOLATILE MEMORY	12/30/2008	08879280.9	EP
NMXN-0043.00/IN	Utility - NSPCT	Abandoned	REVERSING A POTENTIAL POLARITY FOR READING PHASE-CHANGE CELLS TO SHORTEN A RECOVERY DELAY AFTER PROGRAMMING	7/29/2008	7924/CHE/2010	IN
NMXN-0063.00/IN	Utility - NSPCT	Abandoned	OPERATING TEMPERATURE RANGE EXTENSION FOR PCM AND OTHER NVM TECHNOLOGIES	12/30/2008	4493/CHE/2009	IN
NMXN-0139.00/IN	Utility - ORG	Abandoned	PHASE CHANGE MEMORY CELL WITH SELF-ALIGNED VERTICAL HEATER AND LOW RESISTIVITY INTERFACE	6/9/2010	1590/CHE/2010	IN
NMXN-0157.00/IN	Utility - ORG	Abandoned	ERROR CORRECTING CODES FOR INCREASED STORAGE CAPACITY IN MULTILEVEL MEMORY DEVICES	5/7/2010	1081/DEL/2010	IN
NMXN-0158.00/IN	Utility - ORG	Abandoned	CHARGE PUMP CIRCUIT AND METHOD	4/9/2010	998/CHE/2010	IN
NMXN-0161.00/IN	Utility - ORG	Abandoned	METHOD FOR WRITING TO AND ERASING A NON-VOLATILE MEMORY	4/9/2010	997/CHE/2010	IN
NMXN-0167.00/IN	Utility - ORG	Abandoned	METHOD FOR OPERATING A PORTION OF AN EXECUTABLE PROGRAM IN AN EXECUTABLE NON-VOLATILE MEMORY	4/23/2010	1144/CHE/2010	IN
NMXN-0403.00/IN	Utility - ORG	Abandoned	METHOD FOR LOW POWER ACCESSING A PHASE CHANGE MEMORY DEVICE	10/27/2009	2599/CHE/2009	IN
NMXN-0404.00/IN	Utility - ORG	Abandoned	METHOD FOR LOW-STRESS MULTILEVEL READING OF PHASE CHANGE MEMORY CELLS AND MULTILEVEL PHASE CHANGE MEMORY	10/27/2009	2600/CHE/2009	IN
NMXN-0432.00/IT	Utility - ORG	Pending	RPMB OTP REGISTER PROGRAM AND READ	4/30/2010	M1201DA000761	IT
NMXN-0031.00/JP	Utility - ORG	Pending	PROTECTIVE THIN FILM COATING IN CHIP PACKAGING	10/30/2009	2009-265615	JP
NMXN-0034.00/JP	Utility - ORG	Abandoned	A SEMICONDUCTOR PACKAGE SUBSTRATE WITH METAL BUMPS	10/29/2009	2009-264355	JP
NMXN-0036.00/JP	Utility - ORG	Pending	INTEGRATING DIVERSE TRANSISTORS ON THE SAME WAFER	12/21/2009	2009-289311	JP
NMXN-0038.00/JP	Utility - ORG	Pending	MULTI-DIE BUILDING BLOCK FOR STACKED-DIE PACKAGE	10/29/2009	2009-264356	JP
NMXN-0041.00/JP	Utility - ORG	Pending	CONTROLLING THE CIRCUITRY AND MEMORY ARRAY RELATIVE HEIGHT IN A PHASE CHANGE MEMORY FEOL PROCESS FLOW	11/10/2009	2009-273924	JP

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App No.	App Type	Matter Status	Title	Filed Date	Serial No.	Country
NMXN-0043.00/JP	Utility - NSPCT	Pending	REVERSING A POTENTIAL POLARITY FOR READING PHASE-CHANGE CELLS TO SHORTEN A RECOVERY DELAY AFTER PROGRAMMING	7/29/2008	2011-520605	JP
NMXN-0063.00/JP	Utility - NSPCT	Pending	OPERATING TEMPERATURE RANGE EXTENSION FOR PCM AND OTHER NVM TECHNOLOGIES	12/30/2008	PCT/JP2008/000816	JP
NMXN-0081.00/JP	Utility - ORG	Pending	RECOVERY FOR NON-VOLATILE MEMORY AFTER POWER LOSS	11/5/2009	2009-269278	JP
NMXN-0083.00/JP	Utility - ORG	Pending	METHOD OF HIGH ASPECT RATIO PLUG FILL	11/5/2009	2009-269279	JP
NMXN-0085.00/JP	Utility - NSPCT	Pending	VIRTUALIZED ECC NAND	10/9/2008	2011-530580	JP
NMXN-0104.00/JP	Utility - ORG	Pending	SELF-SELECTING PCM DEVICE NOT REQUIRING A DEDICATED SELECTOR TRANSISTOR	11/18/2010	2010-272982	JP
NMXN-0112.00/JP	Utility - ORG	Pending	METHOD AND APPARATUS TO PROFILE RAM MEMORY OBJECTS FOR DISPLACEMENT WITH NONVOLATILE MEMORY	11/10/2009	2009-273923	JP
NMXN-0124.00/JP	Utility - ORG	Pending	PROVIDING A READY-BUSY SIGNAL FROM A NON-VOLATILE MEMORY DEVICE TO A MEMORY CONTROLLER	12/14/2009	2009-282802	JP
NMXN-0125.00/JP	Utility - ORG	Pending	NON-VOLATILE MEMORY DEVICE CAPABLE OF INITIATING TRANSACTIONS	12/10/2009	2009-280323	JP
NMXN-0134.00/JP	Utility - ORG	Pending	METHOD AND DEVICES FOR CONTROLLING POWER LOSS	5/26/2010	2010-134480	JP
NMXN-0137.00/JP	Utility - ORG	Pending	PCM MEMORIES FOR STORAGE BUS INTERFACES	5/13/2010	2010-125187	JP
NMXN-0139.00/JP	Utility - ORG	Pending	PHASE CHANGE MEMORY CELL WITH SELF-ALIGNED VERTICAL HEATER AND LOW RESISTIVITY INTERFACE	6/15/2010	2010-151445	JP
NMXN-0157.00/JP	Utility - ORG	Pending	ERROR CORRECTING CODES FOR INCREASED STORAGE CAPACITY IN MULTILEVEL MEMORY DEVICES	5/18/2010	2010-114819	JP
NMXN-0158.00/JP	Utility - ORG	Pending	CHARGE PUMP CIRCUIT AND METHOD	4/30/2010	2010-117768	JP
NMXN-0161.00/JP	Utility - ORG	Pending	METHOD FOR WRITING TO AND ERASING A NON-VOLATILE MEMORY	4/14/2010	2010-106739	JP
NMXN-0167.00/JP	Utility - ORG	Pending	METHOD FOR OPERATING A PORTION OF AN EXECUTABLE PROGRAM IN AN EXECUTABLE NON-VOLATILE MEMORY	6/2/2010	2010-139896	JP
NMXN-0185.00/JP	Utility - ORG	Pending	ENHANCED MULTILEVEL MEMORY	5/11/2011	2011-106289	JP
NMXN-0187.00/JP	Utility - ORG	Pending	CHARGE TRAP MEMORY HAVING LIMITED CHARGE DIFFUSION	4/15/2011	2011-090818	JP
NMXN-0193.00/JP	Utility - ORG	Pending	NON-VOLATILE SRAM CELL THAT INCORPORATES PHASE-CHANGE MEMORY INTO A CMOS PROCESS	10/6/2010	2010-239032	JP
NMXN-0198.00/JP	Utility - ORG	Pending	MEMORY BUFFER HAVING ACCESSIBLE INFORMATION AFTER A PROGRAM-FAIL	4/22/2011	2011-096432	JP
NMXN-0215.00/JP	Utility - ORG	Pending	MEMORY DEVICE WITH CUSTOMIZABLE ECC CORRECTION AND CLOSE TO ANALOG SENSING	11/12/2010	2010-268790	JP
NMXN-0223.00/JP	Utility - ORG	Pending	MODIFIED READ OPERATION FOR NON-VOLATILE MEMORY	8/22/2011	2011-180212	JP
NMXN-0233.00/JP	Utility - ORG	Pending	PACKAGE INCLUDING AN UNDERFILL MATERIAL IN A PORTION OF AN AREA BETWEEN THE PACKAGE AND A SUBSTRATE OR ANOTHER PACKAGE	11/29/2010	2010-265831	JP
NMXN-0246.00/JP	Utility - ORG	Pending	BIT ERROR THRESHOLD AND REMAPPING A MEMORY DEVICE	6/29/2010	2010-148360	JP
NMXN-0248.00/JP	Utility - ORG	Pending	NON-VOLATILE MEMORY TO STORE MEMORY REMAP INFORMATION	6/29/2010	2010-148353	JP
NMXN-0250.00/JP	Utility - ORG	Pending	BIT ERROR THRESHOLD AND CONTENT ADDRESSABLE MEMORY TO ADDRESS A REMAPPED MEMORY DEVICE	6/29/2010	2010-148359	JP
NMXN-0255.00/JP	Utility - ORG	Pending	STORED MULTI-BIT CHARACTERIZED BY MULTIPLE-DIMENSIONAL MEMORY STATES	11/29/2010	2010-265808	JP
NMXN-0271.00/JP	Utility - ORG	Pending	WRITE OPERATION FOR PHASE CHANGE MEMORY	7/6/2011	2011-150208	JP

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App No.	Matter Type	Matter Status	Title	File Date	Serial No.	Country
NMXN-0280.00/JP	Utility - ORG	Pending	READ DISTRIBUTION MANAGEMENT FOR PHASE CHANGE MEMORY	9/16/2011	2011-203372	JP
NMXN-0285.00/JP	Utility - ORG	Pending	FLASH MEMORY HAVING MULTI-LEVEL ARCHITECTURE	4/15/2011	2011-090905	JP
NMXN-0290.00/JP	Utility - ORG	Pending	MANAGED HYBRID MEMORY WITH ADAPTIVE POWER SUPPLY	7/6/2011	2011-150127	JP
NMXN-0297.00/JP	Utility - ORG	Pending	SIGNAL LINE TO INDICATE PROGRAM-FAIL IN MEMORY	4/19/2011	2011-093507	JP
NMXN-0316.00/JP	Utility - ORG	Pending	FORMING ELECTRODES FOR CHALCOGENIDE CONTAINING DEVICES	5/6/2011	2011-103402	JP
NMXN-0317.00/JP	Utility - ORG	Pending	WORDLINE DRIVER FOR MEMORY	7/28/2011	2011-101340	JP
NMXN-0319.00/JP	Utility - ORG	Pending	STAGGERED PROGRAMMING FOR RESISTIVE MEMORIES	4/25/2011	2011-097295	JP
NMXN-0332.00/JP	Utility - ORG	Pending	FORMING RESISTIVE RANDOM ACCESS MEMORIES TOGETHER WITH FUSE ARRAYS	7/26/2011	2011-163317	JP
NMXN-0358.00/JP	Utility - ORG	Pending	CACHE COHERENCE PROTOCOL FOR PERSISTENT MEMORIES	5/23/2011	2011-114340	JP
NMXN-0363.00/JP	Utility - ORG	Pending	PERSISTENT MEMORY FOR PROCESSOR MAIN MEMORY	5/17/2011	2011-110233	JP
NMXN-0378.00/JP	Utility - ORG	Pending	SECURITY PROTECTION FOR MEMORY CONTENT OF PROCESSOR MAIN MEMORY	8/10/2011	2011-175264	JP
NMXN-0403.00/JP	Utility - ORG	Pending	METHOD FOR LOW POWER ACCESSING A PHASE CHANGE MEMORY DEVICE	10/29/2009	2009-264353	JP
NMXN-0404.00/JP	Utility - ORG	Pending	METHOD FOR LOW-STRESS MULTILEVEL READING OF PHASE CHANGE MEMORY CELLS AND MULTILEVEL PHASE CHANGE MEMORY	10/29/2009	2009-264354	JP
NMXN-0405.00/JP	Utility - ORG	Pending	HIERARCHICAL MEMORY ARCHITECTURE TO CONNECT MASS STORAGE DEVICES	3/31/2010	2010-095441	JP
NMXN-0409.00/JP	Utility - ORG	Pending	MEMORY DEVICE FOR A HIERARCHICAL MEMORY ARCHITECTURE	8/28/2009	2009-217985	JP
NMXN-0410.00/JP	Utility - ORG	Pending	AUTONOMOUS MEMORY ARCHITECTURE	8/2/2010	2010-183450	JP
NMXN-0411.00/JP	Utility - ORG	Pending	AUTONOMOUS SUBSYSTEM ARCHITECTURE	8/2/2010	2010-183449	JP
NMXN-0413.00/JP	Utility - ORG	Pending	DEDICATED INTERFACE TO FACTORY PROGRAM PHASE-CHANGE MEMORIES	5/10/2010	2010-120973	JP
NMXN-0415.00/JP	Utility - ORG	Pending	HIGH-SPEED WIRELESS SERIAL COMMUNICATION LINK FOR A STACKED DEVICE CONFIGURATION USING NEAR FIELD COUPLING	8/17/2010	2010-197109	JP
NMXN-0420.00/JP	Utility - ORG	Pending	PHASE CHANGE MEMORY IN A DUAL INLINE MEMORY MODULE	7/2/2010	2010151680	JP
NMXN-0431.00/JP	Utility - ORG	Pending	KEYHOLE-FREE SLOPED HEATER FOR PHASE CHANGE MEMORY	11/29/2010	2010-265811	JP
NMXN-0432.00/JP	Utility - ORG	Pending	RPMB OTP REGISTER PROGRAM AND READ	5/2/2011	2011-102875	JP
NMXN-0031.00/KR	Utility - ORG	Pending	PROTECTIVE THIN FILM COATING IN CHIP PACKAGING	11/13/2009	10-2009-0109685	KR
NMXN-0034.00/KR	Utility - ORG	Abandoned	A SEMICONDUCTOR PACKAGE SUBSTRATE WITH METAL BUMPS	11/13/2009	10-2009-0109678	KR
NMXN-0038.00/KR	Utility - ORG	Pending	MULTI-DIE BUILDING BLOCK FOR STACKED-DIE PACKAGE	11/11/2009	10-2009-0108548	KR
NMXN-0041.00/KR	Utility - ORG	Pending	CONTROLLING THE CIRCUITRY AND MEMORY ARRAY RELATIVE HEIGHT IN A PHASE CHANGE MEMORY FEOL PROCESS FLOW	11/13/2009	10-2009-0109651	KR
NMXN-0043.00/KR	Utility - NSPCT	Pending	REVERSING A POTENTIAL POLARITY FOR READING PHASE-CHANGE CELLS TO SHORTEN A RECOVERY DELAY AFTER PROGRAMMING	7/29/2008	10-2010-7029002	KR
NMXN-0063.00/KR	Utility - NSPCT	Pending	NON-VOLATILE MEMORY WITH EXTENDED OPERATING TEMPERATURE RANGE	12/30/2008	2009-7017997	KR
NMXN-0081.00/KR	Utility - ORG	Pending	RECOVERY FOR NON-VOLATILE MEMORY AFTER POWER LOSS	11/13/2009	10-2009-0109644	KR
NMXN-0103.00/KR	Utility - NSPCT	Pending	ENHANCED ADDRESSABILITY FOR SERIAL NON-VOLATILE MEMORY	12/30/2008	10-2011-7017938	KR
NMXN-0114.00/KR	Utility - ORG	Pending	MOLDING COMPOUND INCLUDING CARBON NANO-TUBE DISPERSION	11/13/2009	10-2009-0109826	KR
NMXN-0124.00/KR	Utility - ORG	Pending	PROVIDING A READY-BUSY SIGNAL FROM A NON-VOLATILE MEMORY DEVICE TO A MEMORY CONTROLLER	12/16/2009	10-2009-125649	KR
NMXN-0125.00/KR	Utility - ORG	Pending	NON-VOLATILE MEMORY DEVICE CAPABLE OF INITIATING TRANSACTIONS	12/9/2009	10-2009-121718	KR
NMXN-0134.00/KR	Utility - ORG	Pending	METHOD AND DEVICES FOR CONTROLLING POWER LOSS	5/25/2010	10-2010-48722	KR

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Patent No.	Mat. Type	Mat. Status	Title	File Date	Serial No.	Country
NMXN-0137.00/KR	Utility - ORG	Pending	PCM MEMORIES FOR STORAGE BUS INTERFACES	5/13/2010	10-2010-0044978	KR
NMXN-0139.00/KR	Utility - ORG	Pending	PHASE CHANGE MEMORY CELL WITH SELF-ALIGNED VERTICAL HEATER AND LOW RESISTIVITY INTERFACE	6/30/2010	10-2010-0062745	KR
NMXN-0157.00/KR	Utility - ORG	Pending	ERROR CORRECTING CODES FOR INCREASED STORAGE CAPACITY IN MULTILEVEL MEMORY DEVICES	6/9/2010	10-2010-54300	KR
NMXN-0158.00/KR	Utility - ORG	Pending	CHARGE PUMP CIRCUIT AND METHOD	4/30/2010	10-2010-40476	KR
NMXN-0161.00/KR	Utility - ORG	Pending	METHOD FOR WRITING TO AND ERASING A NON-VOLATILE MEMORY	4/14/2010	10-2010-34401	KR
NMXN-0167.00/KR	Utility - ORG	Pending	METHOD FOR OPERATING A PORTION OF AN EXECUTABLE PROGRAM IN AN EXECUTABLE NON-VOLATILE MEMORY	5/7/2010	10-2010-43041	KR
NMXN-0185.00/KR	Utility - ORG	Pending	ENHANCED MULTILEVEL MEMORY	5/18/2011	10-2011-47060	KR
NMXN-0187.00/KR	Utility - ORG	Pending	CHARGE TRAP MEMORY HAVING LIMITED CHARGE DIFFUSION	4/20/2011	10-2011-36831	KR
NMXN-0193.00/KR	Utility - ORG	Pending	NON-VOLATILE SRAM CELL THAT INCORPORATES PHASE-CHANGE MEMORY INTO A CMOS PROCESS	10/7/2010	2010-0097820	KR
NMXN-0198.00/KR	Utility - ORG	Pending	MEMORY BUFFER HAVING ACCESSIBLE INFORMATION AFTER A PROGRAM-FAIL	5/12/2011	10-2011-44653	KR
NMXN-0215.00/KR	Utility - ORG	Pending	MEMORY DEVICE WITH CUSTOMIZABLE ECC CORRECTION AND CLOSE TO ANALOG SENSING	11/10/2010	10-2010-0111531	KR
NMXN-0223.00/KR	Utility - ORG	Pending	MODIFIED READ OPERATION FOR NON-VOLATILE MEMORY	8/29/2011	10-2011-86729	KR
NMXN-0233.00/KR	Utility - ORG	Pending	PACKAGE INCLUDING AN UNDERFILL MATERIAL IN A PORTION OF AN AREA BETWEEN THE PACKAGE AND A SUBSTRATE OR ANOTHER PACKAGE	11/29/2010	10-2010-0119274	KR
NMXN-0246.00/KR	Utility - ORG	Pending	BIT ERROR THRESHOLD AND REMAPPING A MEMORY DEVICE	5/28/2010	2010-50100	KR
NMXN-0248.00/KR	Utility - ORG	Pending	NON-VOLATILE MEMORY TO STORE MEMORY REMAP INFORMATION	5/28/2010	2010-50111	KR
NMXN-0250.00/KR	Utility - ORG	Pending	BIT ERROR THRESHOLD AND CONTENT ADDRESSABLE MEMORY TO ADDRESS A REMAPPED MEMORY DEVICE	5/28/2010	2010-50121	KR
NMXN-0255.00/KR	Utility - ORG	Pending	STORED MULTI-BIT CHARACTERIZED BY MULTIPLE-DIMENSIONAL MEMORY STATES	11/30/2010	2010-120281	KR
NMXN-0271.00/KR	Utility - ORG	Pending	WRITE OPERATION FOR PHASE CHANGE MEMORY	7/28/2011	10-2011-75420	KR
NMXN-0285.00/KR	Utility - ORG	Pending	FLASH MEMORY HAVING MULTI-LEVEL ARCHITECTURE	4/19/2011	10-2011-36208	KR
NMXN-0290.00/KR	Utility - ORG	Pending	MANAGED HYBRID MEMORY WITH ADAPTIVE POWER SUPPLY	7/27/2011	10-2011-74545	KR
NMXN-0297.00/KR	Utility - ORG	Pending	SIGNAL LINE TO INDICATE PROGRAM-FAIL IN MEMORY	4/28/2011	10-2011-40067	KR
NMXN-0316.00/KR	Utility - ORG	Pending	FORMING ELECTRODES FOR CHALCOGENIDE CONTAINING DEVICES	5/11/2011	10-2011-44224	KR
NMXN-0317.00/KR	Utility - ORG	Pending	WORDLINE DRIVER FOR MEMORY	5/12/2011	10-2011-44648	KR
NMXN-0319.00/KR	Utility - ORG	Pending	STAGGERED PROGRAMMING FOR RESISTIVE MEMORIES	5/12/2011	10-2011-44656	KR
NMXN-0332.00/KR	Utility - ORG	Pending	FORMING RESISTIVE RANDOM ACCESS MEMORIES TOGETHER WITH FUSE ARRAYS	8/3/2011	10-2011-77489	KR
NMXN-0358.00/KR	Utility - ORG	Pending	CACHE COHERENCE PROTOCOL FOR PERSISTENT MEMORIES	6/9/2011	10-2011-55706	KR
NMXN-0363.00/KR	Utility - ORG	Pending	PERSISTENT MEMORY FOR PROCESSOR MAIN MEMORY	6/8/2011	10-2011-54932	KR
NMXN-0369.00/KR	Utility - ORG	Pending	PHASE CHANGE MEMORY STATE DETERMINATION USING THRESHOLD EDGE DETECTION	9/22/2011	10-2011-95861	KR
NMXN-0403.00/KR	Utility - ORG	Pending	METHOD FOR LOW POWER ACCESSING A PHASE CHANGE MEMORY DEVICE	11/13/2009	10-2009-0109616	KR
NMXN-0404.00/KR	Utility - ORG	Pending	METHOD FOR LOW-STRESS MULTILEVEL READING OF PHASE CHANGE MEMORY CELLS AND MULTILEVEL PHASE CHANGE MEMORY	11/12/2009	10-2009-0109043	KR

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Patent No.	Matter Type	Matter Status	Title	Filed Date	Serial No.	Country
NMXN-0405.00/KR	Utility - ORG	Pending	HIERARCHICAL MEMORY ARCHITECTURE TO CONNECT MASS STORAGE DEVICES	3/31/2010	10-2010-29307	KR
NMXN-0409.00/KR	Utility - ORG	Pending	MEMORY DEVICE FOR A HIERARCHICAL MEMORY ARCHITECTURE	8/27/2009	10-2009-0079938	KR
NMXN-0410.00/KR	Utility - ORG	Pending	AUTONOMOUS MEMORY ARCHITECTURE	7/12/2010	2010-0066779	KR
NMXN-0411.00/KR	Utility - ORG	Pending	AUTONOMOUS SUBSYSTEM ARCHITECTURE	7/16/2010	2010-0068824	KR
NMXN-0413.00/KR	Utility - ORG	Pending	DEDICATED INTERFACE TO FACTORY PROGRAM PHASE-CHANGE MEMORIES	5/7/2010	10-2010-0043045	KR
NMXN-0414.00/KR	Utility - ORG	Pending	WIRELESS INTERFACE TO PROGRAM PHASE-CHANGE MEMORIES	5/10/2010	10-2010-43387	KR
NMXN-0415.00/KR	Utility - ORG	Pending	HIGH-SPEED WIRELESS SERIAL COMMUNICATION LINK FOR A STACKED DEVICE CONFIGURATION USING NEAR FIELD COUPLING	7/12/2010	2010-0066776	KR
NMXN-0420.00/KR	Utility - ORG	Pending	PHASE CHANGE MEMORY IN A DUAL INLINE MEMORY MODULE	6/1/2010	201051646	KR
NMXN-0431.00/KR	Utility - ORG	Pending	KEYHOLE-FREE SLOPED HEATER FOR PHASE CHANGE MEMORY	11/30/2010	2010-120425	KR
NMXN-0432.00/KR	Utility - ORG	Pending	RPMB OTP REGISTER PROGRAM AND READ	5/2/2011	10-2011-41698	KR
NMXN-0114.00/MY	Utility - ORG	Abandoned	MOLDING COMPOUND INCLUDING CARBON NANO-TUBE DISPERSION	11/3/2009	PI 20094635	MY
NMXN-0081.00/SG	Utility - ORG	Pending	RECOVERY FOR NON-VOLATILE MEMORY AFTER POWER LOSS	11/9/2009	200907451-9	SG
NMXN-0104.00/SG	Utility - ORG	Pending	SELF-SELECTING PCM DEVICE NOT REQUIRING A DEDICATED SELECTOR TRANSISTOR	10/13/2010	201007497-9	SG
NMXN-0124.00/SG	Utility - ORG	Pending	PROVIDING A READY-BUSY SIGNAL FROM A NON-VOLATILE MEMORY DEVICE TO A MEMORY CONTROLLER	11/17/2009	200907643-1	SG
NMXN-0125.00/SG	Utility - ORG	Pending	NON-VOLATILE MEMORY DEVICE CAPABLE OF INITIATING TRANSACTIONS	11/17/2009	200907642-3	SG
NMXN-0134.00/SG	Utility - ORG	Pending	METHOD AND DEVICES FOR CONTROLLING POWER LOSS	4/28/2010	201002982-5	SG
NMXN-0137.00/SG	Utility - ORG	Pending	PCM MEMORIES FOR STORAGE BUS INTERFACES	4/22/2010	201002863-7	SG
NMXN-0157.00/SG	Utility - ORG	Pending	ERROR CORRECTING CODES FOR INCREASED STORAGE CAPACITY IN MULTILEVEL MEMORY DEVICES	4/27/2010	201002957-7	SG
NMXN-0161.00/SG	Utility - ORG	Pending	METHOD FOR WRITING TO AND ERASING A NON-VOLATILE MEMORY	4/14/2010	201002586-4	SG
NMXN-0332.00/SG	Utility - ORG	Pending	FORMING RESISTIVE RANDOM ACCESS MEMORIES TOGETHER WITH FUSE ARRAYS	7/18/2011	201105174-5	SG
NMXN-0413.00/SG	Utility - ORG	Pending	DEDICATED INTERFACE TO FACTORY PROGRAM PHASE-CHANGE MEMORIES	4/21/2010	201002796-9	SG
NMXN-0036.00/TW	Utility - ORG	Pending	INTEGRATING DIVERSE TRANSISTORS ON THE SAME WAFER	11/13/2009	98138611	TW
NMXN-0041.00/TW	Utility - ORG	Pending	CONTROLLING THE CIRCUITRY AND MEMORY ARRAY RELATIVE HEIGHT IN A PHASE CHANGE MEMORY FEOL PROCESS FLOW	11/26/2009	98140396	TW
NMXN-0043.00/TW	Utility - ORG	Pending	REVERSING A POTENTIAL POLARITY FOR READING PHASE-CHANGE CELLS TO SHORTEN A RECOVERY DELAY AFTER PROGRAMMING	10/9/2008	097139046	TW
NMXN-0063.00/TW	Utility - ORG	Pending	NON-VOLATILE MEMORY WITH EXTENDED OPERATING TEMPERATURE RANGE	8/11/2009	98126991	TW
NMXN-0083.00/TW	Utility - ORG	Pending	METHOD OF HIGH ASPECT RATIO PLUG FILL	11/26/2009	98140387	TW
NMXN-0086.00/TW	Utility - ORG	Pending	VIRTUALIZED ECC NAND	10/9/2008	97139047	TW
NMXN-0101.00/TW	Utility - ORG	Pending	CONTROLLING CLOCK INPUT BUFFERS	12/29/2010	99146570	TW
NMXN-0106.00/TW	Utility - ORG	Pending	TEMPERATURE ALERT AND LOW RATE REFRESH FOR A NON-VOLATILE MEMORY	11/26/2009	98140392	TW
NMXN-0112.00/TW	Utility - ORG	Pending	METHOD AND APPARATUS TO PROFILE RAM MEMORY OBJECTS FOR DISPLACEMENT WITH NONVOLATILE MEMORY	11/26/2009	98140389	TW
NMXN-0114.00/TW	Utility - ORG	Pending	MOLDING COMPOUND INCLUDING CARBON NANO-TUBE DISPERSION	11/26/2009	98140373	TW

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Pat. No.	Doc. No.	Mat. Type	Mat. Status	Title	Filed Date	Serial No.	Country
NMXN-0139.00/TW		Utility - ORG	Pending	PHASE CHANGE MEMORY CELL WITH SELF-ALIGNED VERTICAL HEATER AND LOW RESISTIVITY INTERFACE	6/28/2010	99121092	TW
NMXN-0158.00/TW		Utility - ORG	Pending	CHARGE PUMP CIRCUIT AND METHOD	5/4/2010	99114250	TW
NMXN-0167.00/TW		Utility - ORG	Pending	METHOD FOR OPERATING A PORTION OF AN EXECUTABLE PROGRAM IN AN EXECUTABLE NON-VOLATILE MEMORY	5/13/2010	99115349	TW
NMXN-0185.00/TW		Utility - ORG	Pending	ENHANCED MULTILEVEL MEMORY	5/19/2011	100117641	TW
NMXN-0187.00/TW		Utility - ORG	Pending	CHARGE TRAP MEMORY HAVING LIMITED CHARGE DIFFUSION	4/20/2011	100113771	TW
NMXN-0188.00/TW		Utility - ORG	Pending	SUSPENSION OF MEMORY OPERATIONS FOR REDUCED WRITE LATENCY IN MEMORY ARRAYS	6/7/2010	99118447	TW
NMXN-0193.00/TW		Utility - ORG	Pending	NON-VOLATILE SRAM CELL THAT INCORPORATES PHASE-CHANGE MEMORY INTO A CMOS PROCESS	10/11/2010	99134600	TW
NMXN-0204.00/TW		Utility - ORG	Pending	METHODS FOR A PHASE-CHANGE MEMORY ARRAY	11/23/2010	99141065	TW
NMXN-0215.00/TW		Utility - ORG	Pending	MEMORY DEVICE WITH CUSTOMIZABLE ECC CORRECTION AND CLOSE TO ANALOG SENSING	11/1/2010	99137534	TW
NMXN-0227.00/TW		Utility - ORG	Pending	REFRESH ARCHITECTURE AND ALGORITHM FOR NON-VOLATILE MEMORIES	11/23/2010	99140451	TW
NMXN-0246.00/TW		Utility - ORG	Pending	BIT ERROR THRESHOLD AND REMAPPING A MEMORY DEVICE	6/15/2010	99119458	TW
NMXN-0248.00/TW		Utility - ORG	Pending	NON-VOLATILE MEMORY TO STORE MEMORY REMAP INFORMATION	6/15/2010	99119459	TW
NMXN-0250.00/TW		Utility - ORG	Pending	BIT ERROR THRESHOLD AND CONTENT ADDRESSABLE MEMORY TO ADDRESS A REMAPPED MEMORY DEVICE	6/15/2010	99119456	TW
NMXN-0271.00/TW		Utility - ORG	Pending	WRITE OPERATION FOR PHASE CHANGE MEMORY	7/6/2011	100123927	TW
NMXN-0285.00/TW		Utility - ORG	Pending	FLASH MEMORY HAVING MULTI-LEVEL ARCHITECTURE	4/20/2011	100113769	TW
NMXN-0290.00/TW		Utility - ORG	Pending	MANAGED HYBRID MEMORY WITH ADAPTIVE POWER SUPPLY	7/6/2011	100123922	TW
NMXN-0297.00/TW		Utility - ORG	Pending	SIGNAL LINE TO INDICATE PROGRAM-FAIL IN MEMORY	4/29/2011	100115212	TW
NMXN-0316.00/TW		Utility - ORG	Pending	FORMING ELECTRODES FOR CHALCOGENIDE CONTAINING DEVICES	5/11/2011	100116569	TW
NMXN-0317.00/TW		Utility - ORG	Pending	WORDLINE DRIVER FOR MEMORY	5/13/2011	100116941	TW
NMXN-0319.00/TW		Utility - ORG	Pending	STAGGERED PROGRAMMING FOR RESISTIVE MEMORIES	5/13/2011	100116911	TW
NMXN-0358.00/TW		Utility - ORG	Pending	CACHE COHERENCE PROTOCOL FOR PERSISTENT MEMORIES	6/9/2011	100120245	TW
NMXN-0363.00/TW		Utility - ORG	Pending	PERSISTENT MEMORY FOR PROCESSOR MAIN MEMORY	6/9/2011	100120246	TW
NMXN-0369.00/TW		Utility - ORG	Pending	PHASE CHANGE MEMORY STATE DETERMINATION USING THRESHOLD EDGE DETECTION	8/1/2011	100127237	TW
NMXN-0378.00/TW		Utility - ORG	Pending	SECURITY PROTECTION FOR MEMORY CONTENT OF PROCESSOR MAIN MEMORY	8/1/2011	100127253	TW
NMXN-0403.00/TW		Utility - ORG	Pending	METHOD FOR LOW POWER ACCESSING A PHASE CHANGE MEMORY DEVICE	11/26/2009	98140395	TW
NMXN-0404.00/TW		Utility - ORG	Pending	METHOD FOR LOW-STRESS MULTILEVEL READING OF PHASE CHANGE MEMORY CELLS AND MULTILEVEL PHASE CHANGE MEMORY	11/26/2009	98140352	TW
NMXN-0420.00/TW		Utility - ORG	Pending	PHASE CHANGE MEMORY IN A DUAL INLINE MEMORY MODULE	6/30/2010	99121412	TW
NMXN-0432.00/TW		Utility - ORG	Pending	RPMB OTP REGISTER PROGRAM AND READ	8/16/2011	100115189	TW
NMXN-0017.00/PC		Utility - ORG	Completed	AVOIDING DEGRADATION OF CHALCOGENIDE MATERIAL DURING DEFINITION OF MULTILAYER STACK STRUCTURE	12/31/2008	PCT/AT2008/000827	WO
NMXN-0040.00/PC		Utility - ORG	Completed	INTEGRATION OF POLY RESISTOR AND CAPACITOR IN NAND CHARGE TRAP PROCESS FLOW	12/30/2008	PCT/AT2008/000812	WO

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Int. No.	Matter Type	Matter Status	Title	File Date	Serial No.	Country
NMXN-0043.00/PC	Utility - ORG	Expired	REVERSING A POTENTIAL POLARITY FOR READING PHASE-CHANGE CELLS TO SHORTEN A RECOVERY DELAY AFTER PROGRAMMING	7/29/2008	PCT/IB08/001975	WO
NMXN-0061.00/PC	Utility - ORG	Pending	ERROR CORRECTION CODE FOR UNIDIRECTIONAL MEMORY	12/31/2008	PCT/IT08/000824	WO
NMXN-0063.00/PC	Utility - ORG	Completed	OPERATING TEMPERATURE RANGE EXTENSION FOR PCM AND OTHER NVM TECHNOLOGIES	12/30/2008	PCT/IT08/000816	WO
NMXN-0076.00/PC	Utility - ORG	Pending	WORD-LINE DRIVER INCLUDING PULL-UP RESISTOR AND PULL-DOWN TRANSISTOR	12/31/2008	PCT/IT08/000822	WO
NMXN-0077.00/PC	Utility - ORG	Completed	DOUBLE PATTERNING METHOD FOR CREATING A REGULAR ARRAY OF PILLARS WITH DUAL SHALLOW TRENCH	12/30/2008	PCT/IT08/000813	WO
NMXN-0079.00/PC	Utility - ORG	Pending	RELIABLE SET OPERATION FOR PHASE-CHANGE MEMORY CELL	12/31/2008	PCT/IT08/000823	WO
NMXN-0086.00/PC	Utility - ORG	Completed	VIRTUALIZED ECC NAND	10/9/2008	PCT/IB2008/002658	WO
NMXN-0087.00/PC	Utility - ORG	Pending	MEMORY INCLUDING A SELECTOR SWITCH ON A VARIABLE RESISTANCE MEMORY CELL	11/30/2009	PCT/IT2009/000537	WO
NMXN-0090.00/PC	Utility - ORG	Completed	METHOD FOR FABRICATING A PHASE-CHANGE MEMORY CELL	12/30/2008	PCT/IT2008/000815	WO
NMXN-0095.00/PC	Utility - ORG	Pending	EXECUTE IN-PLACE MODE CONFIGURATION FOR SERIAL NON-VOLATILE MEMORY	12/30/2008	PCT/IB08/055582	WO
NMXN-0096.00/PC	Utility - ORG	Pending	NON-VOLATILE CONFIGURATION FOR SERIAL FLASH MEMORY	12/30/2008	PCT/IB2008/055583	WO
NMXN-0101.00/PC	Utility - ORG	Pending	CONTROLLING CLOCK INPUT BUFFERS	12/30/2009	PCT/IT2009/000592	WO
NMXN-0103.00/PC	Utility - ORG	Pending	ENHANCED ADDRESSABILITY FOR SERIAL NON-VOLATILE MEMORY	12/30/2008	PCT/IB2008/055584	WO
NMXN-0106.00/PC	Utility - ORG	Completed	TEMPERATURE ALERT AND LOW RATE REFRESH FOR A NON-VOLATILE MEMORY	12/30/2008	PCT/IT2008/000814	WO
NMXN-0116.00/PC	Utility - ORG	Pending	MEMORY DEVICE AND METHOD OF FABRICATING THEREOF	12/30/2008	PCT/IB2008/055586	WO
NMXN-0118.00/PC	Utility - ORG	Pending	WEAR LEAVING FOR ERASABLE MEMORIES	12/30/2008	PCT/IT2008/000817	WO
NMXN-0184.00/PC	Utility - ORG	Completed	APPARATUS AND METHOD FOR READING A PHASE-CHANGE MEMORY CELL	12/10/2009	PCT/IT2009/000557	WO
NMXN-0188.00/PC	Utility - ORG	Completed	SUSPENSION OF MEMORY OPERATIONS FOR REDUCED READ LATENCY IN MEMORY ARRAYS	6/10/2009	PCT/IT2009/000253	WO
NMXN-0204.00/PC	Utility - ORG	Completed	METHODS FOR A PHASE-CHANGE MEMORY ARRAY	12/31/2009	PCT/IT2009/000603	WO
NMXN-0216.00/PC	Utility - ORG	Pending	DESCENDING SET VERIFY ALGORITHM FOR PHASE CHANGE MEMORIES	12/29/2009	PCT/IT2009/000584	WO
NMXN-0227.00/PC	Utility - ORG	Completed	REFRESH ARCHITECTURE AND ALGORITHM FOR NON-VOLATILE MEMORIES	12/2/2009	PCT/IT2009/000541	WO
NMXN-0234.00/PC	Utility - ORG	Pending	MEMORY DEVICE WITH ADDRESS AND COMMAND SELECTABLE CAPABILITIES	12/29/2009	PCT/IT2009/000582	WO
NMXN-0240.00/PC	Utility - ORG	Pending	MIXED MODE PROGRAMMING FOR PHASE CHANGE MEMORY DEVICES	12/29/2009	PCT/IT2009/000583	WO
NMXN-0242.00/PC	Utility - ORG	Pending	OPTIMIZED PROGRAMMING ALGORITHM OF THE RESET STATE FOR READOUT MARGIN ENHANCEMENT	12/18/2009	PCT/IT2009/000572	WO
NMXN-0372.00/PC	Utility - ORG	Pending	Continuous Page Read mode in parallel interface non-volatile flash memory	12/24/2010	PCT/CN2010/00153	WO
NMXN-0426.00/PC	Utility - ORG	Pending	TIMING VIOLATION HANDLING IN A SYNCHRONOUS INTERFACE MEMORY	12/29/2009	PCT/IT2009/000585	WO

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