

**PATENT ASSIGNMENT**

Electronic Version v1.1  
 Stylesheet Version v1.1

<b>SUBMISSION TYPE:</b>	CORRECTIVE ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	Corrective Assignment to correct the Assignee previously recorded on Reel 023971 Frame 0171. Assignor(s) hereby confirms the Assignment was incorrectly assigned to Glitter Technology LLP (A non-U.S. Assignee).
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
Hiroshi IWASAKI	01/25/2010
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	EMPIRE TECHNOLOGY DEVELOPMENT LLC
<b>Street Address:</b>	2711 Centerville Road, Suite 400
<b>City:</b>	Wilmington
<b>State/Country:</b>	DELAWARE
<b>Postal Code:</b>	19808
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	12665320
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(608)258-4258
<b>Phone:</b>	608-257-5035
<b>Email:</b>	ptomailmadison@foley.com
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Correspondent Name:</b>	Nicholas M. Lagerwall
<b>Address Line 1:</b>	Foley & Lardner LLP
<b>Address Line 2:</b>	150 E. Gilman St.
<b>Address Line 4:</b>	Madison, WISCONSIN 53703
<b>ATTORNEY DOCKET NUMBER:</b>	091619-0401
<b>NAME OF SUBMITTER:</b>	Nicholas M. Lagerwall

OP \$40.00 12665320

Total Attachments: 8  
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**PATENT**  
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**02/22/2010**  
**501099012**

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
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<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
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**CONVEYING PARTY DATA**

Name	Execution Date
Hiroshi IWASAKI	01/25/2010

**RECEIVING PARTY DATA**

<b>Name:</b>	Empire Technology Development LLC
<b>Street Address:</b>	2711 Centerville Road, Suite 400
<b>City:</b>	Wilmington
<b>State/Country:</b>	DELAWARE
<b>Postal Code:</b>	19808

<b>Name:</b>	Glitter Technology LLP
<b>Street Address:</b>	152 Beach Road, #37-06, Gateway East
<b>City:</b>	Singapore
<b>State/Country:</b>	SINGAPORE
<b>Postal Code:</b>	189721

**PROPERTY NUMBERS Total: 1**

Property Type	Number
<b>Application Number:</b>	12665320

**CORRESPONDENCE DATA**

Fax Number: (608)258-4258  
*Correspondence will be sent via US Mail when the fax attempt is unsuccessful.*

Phone: 608-258-4466  
 Email: PTOMailMadison@foley.com  
 Correspondent Name: Nicholas M. Lagerwall  
 Address Line 1: Foley & Lardner LLP  
 Address Line 2: P.O. Box 1497  
 Address Line 4: Madison, WISCONSIN 53701-1497

<b>ATTORNEY DOCKET NUMBER:</b>	091619-0401
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OP \$40.00 12665320

TO: NICHOLAS M. LAGERWALL COMPANY: FOLEY &amp; LARDNER LLP

NAME OF SUBMITTER:

Nicholas M. Lagerwall

**Total Attachments: 3**

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**ASSIGNMENT OF PATENT RIGHTS**

For valuable consideration, the receipt and sufficiency of which is hereby acknowledged,

Hiroshi IWASAKI, with an address at 8-14, Higashi-Kouri-Shinmachi, Hirakata, Osaka, 573-0077  
JAPAN Empire Technology Development LLC ,

("Assignor"),

effective as of October, 6, 2008 hereby sells, assigns, transfers, and conveys unto

(A) Empire Technology Development LLC, a Delaware limited liability company, with an address at 2711 Centerville Road, Suite 400, Wilmington, DE 19808 ("U.S. Assignee"), all United States rights, title, and interests that exist today and may exist in the future in and to any and all of the following items (1) through (8) below (collectively, the "U.S. Rights"), and

(B) Glitter Technology LLP, a Singapore limited liability partnership, with an address at 152 Beach Road, #37-06, Gateway East, Singapore 189721 ("Non-U.S. Assignee"), all non-United States rights, title, and interests that exist today and may exist in the future in and to any and all of the following items (1) through (8) below (collectively, the "Non-U.S. Rights"):

1. the inventions disclosed in the solution report(s)/invention disclosure(s) titled Semiconductor Device(3) and all inventions claimed and/or described in the Application (collectively, the "Invention");
2. the patent applications listed in the table below (the "Application");

Patent Application No.	Country	Filing Date	Title
PCT/JP2008/072248	WIPO	12/8/2008	SEMICONDUCTOR STORAGE DEVICE AND METHOD OF MANUFACTURING SAME
12/665,320	U.S.	12/17/2009	SEMICONDUCTOR STORAGE DEVICE AND METHOD OF MANUFACTURING SAME

3. all rights with respect to the Invention, including all UNITED STATES patents or other governmental grants or issuances that may be granted with respect to the Invention or from any direct or indirect divisionals, continuations, continuations-in-part, or other patent applications claiming priority rights from the Application ("Potential Patents");

4. all reissues, reexaminations, extensions, or registrations of the Potential Patents;

5. all non-United States patents, patent applications, and counterparts relating to any or all of the Invention, the Application, and the Potential Patents, including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances ("Foreign Rights"), and including the right to file foreign applications directly in the name of Assignee, its successors and assigns;

6. all rights to claim priority rights deriving from the Application;

7. all causes of action and remedies related to any or all of the Application, the Invention, the Potential Patents, and the Foreign Rights (including, without limitation, the right to sue for past, present, or future infringement, misappropriation or violation of rights related to any of the foregoing and the right to collect royalties and other payments under or on account of any of the foregoing); and

8. any and all other rights and interests arising out of, in connection with, or in relation to the Application, the Invention, Potential Patents, and the Foreign Rights.


As used in this Assignment, "Assignee" means, collectively, the U.S. Assignee and the Non-U.S. Assignee. Assignor will not sign any document or do any act conflicting with this Assignment, and, without further compensation, will sign all documents and do such additional acts as Assignee, its successors, legal representatives, and assigns deem necessary or desirable to perfect enjoyment of the Rights, conduct proceedings regarding the Rights (including any litigation or interference proceedings), or perfect or defend title to the Rights. Assignor requests the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models, or other governmental grants or issuances that may be granted upon any of the Rights in the name of the Assignee, as the assignee to the entire interest therein.

Assignor hereby authorizes and requests legal representative Foley & Lardner LLP, with an address at 150 E. Gilman St. Madison, WI 53703 U.S.A., to insert on this Assignment the filing date and Patent Application Numbers in the table above when known.

The terms and conditions of this Assignment will inure to the benefit of Assignee, its successors, legal representatives, and assigns and will be binding upon Assignor, their successors, legal representatives and assigns.

[Signature Page(s) to Follow]

Inventor / Organization Stamp:

By:   
Print name: Hiroshi IWASAKI  
Title: \_\_\_\_\_  
Organisation : \_\_\_\_\_

**ATTESTATION**

Each of the undersigned witnessed the signature of Hiroshi IWASAKI to this document and makes the following statements:

1. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.
2. Hiroshi IWASAKI is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me on January, 25, 2010, to execute this document.
3. Hiroshi IWASAKI subscribed to this document.

I declare under penalty of perjury under the laws of the United States of America, and under penalty of the laws of any other jurisdiction before which this document may be presented, that the foregoing is true and correct.

By:   
Print name: Yoshinori YAMAGUCHI

By:   
Print name: Masato SAITO