PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	Supplemental Patent Security Agreement

CONVEYING PARTY DATA

Name	Execution Date
Microsemi Corporation	10/26/2011
Microsemi Corp. - Analog Mixed Signal Group	10/26/2011
Microsemi Corp. - Massachusetts	10/26/2011
Actel Corporation	10/26/2011

RECEIVING PARTY DATA

Name:	Morgan Stanley & Co. LLC
Street Address:	1585 BROADWAY
City:	New York
State/Country:	NEW YORK
Postal Code:	10036

PROPERTY NUMBERS Total: 142

Property Type	Number
Patent Number:	5231474
Patent Number:	5256583
Patent Number:	5262336
Patent Number:	7852019
Patent Number:	7906868
Patent Number:	8008864
Patent Number:	8008867
Patent Number:	8022635
Patent Number:	7977888
Patent Number:	7990072
Patent Number:	7977928
Patent Number:	7573329
Patent Number:	7924092

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Patent Number:	6979959
Patent Number:	7279852
Patent Number:	7411360
Patent Number:	6870330
Patent Number:	6965219
Patent Number:	6836103
Patent Number:	7952298
Patent Number:	7919952
Patent Number:	7932683
Patent Number:	7868603
Patent Number:	7906943
Patent Number:	D634229
Patent Number:	D618269
Patent Number:	7873182
Patent Number:	7601958
Patent Number:	D571244
Patent Number:	7781717
Patent Number:	7858938
Patent Number:	7956491
Patent Number:	7911255
Patent Number:	6636025
Patent Number:	6233165
Patent Number:	7782115
Patent Number:	6759688
Patent Number:	6294766
Patent Number:	7851881
Patent Number:	7956455
Patent Number:	7915981
Patent Number:	7965046
Patent Number:	7939928
Patent Number:	7449841
Patent Number:	8003446
Patent Number:	8018042
Patent Number:	6160420
Patent Number:	5781033

	5698992
Patent Number:	5606267
Patent Number:	5600265
Patent Number:	5570041
Patent Number:	5510730
Patent Number:	5223792
Patent Number:	5208530
Patent Number:	5198705
Patent Number:	5194759
Patent Number:	5191241
Patent Number:	5187393
Patent Number:	7830173
Patent Number:	7838944
Patent Number:	7839681
Patent Number:	7859302
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Patent Number:	7885122
Patent Number:	7884636
Patent Number:	7886130
Patent Number:	7886261
Patent Number:	7898018
Patent Number:	7911226
Patent Number:	7910436
Patent Number:	7906805
Patent Number:	7915918
Patent Number:	7915665
Patent Number:	7919977
Patent Number:	7919979
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Patent Number:	7924052
Patent Number:	7924053
Patent Number:	7929345
Patent Number:	7932744
Patent Number:	7932745
Patent Number:	7937601

	7937647
Patent Number:	7941685
Patent Number:	7944238
Patent Number:	7956404
Patent Number:	7977970
Patent Number:	7872497
Patent Number:	6285212
Patent Number:	6272655
Patent Number:	6252273
Patent Number:	6564273
Patent Number:	6072720
Patent Number:	6125059
Application Number:	12815441
Application Number:	12815496
Application Number:	12861919
Application Number:	12895875
Application Number:	12960504
Application Number:	12960505
Application Number:	13017128
Application Number:	12838254
Application Number:	13084229
Application Number:	12658328
Application Number:	12658576
Application Number:	12338780
Application Number:	12338807
Application Number:	12867807
Application Number:	11835886
Application Number:	12043246
Application Number:	12110520
Application Number:	12118960
Application Number:	12131539
Application Number:	12136000
Application Number:	12142982
Application Number:	12575604
Application Number:	12893171

	12846240
Application Number:	13019722
Application Number:	29385710
Application Number:	29379049
Application Number:	29385707
Application Number:	12912539
Application Number:	12938974
Application Number:	12941401
Application Number:	12870699
Application Number:	12030907
Application Number:	12383468
Application Number:	12404979
Application Number:	12416363
Application Number:	12472539
Application Number:	12536596
Application Number:	12545437
Application Number:	12719204
Application Number:	13012645
Application Number:	12824301
Application Number:	12850120
Application Number:	12903387
Application Number:	12181533
Application Number:	12336990
Application Number:	11079323

CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	391000/1394
NAME OF SUBMITTER:	John Deming

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SUPPLEMENTAL PATENT SECURITY AGREEMENT

This SUPPLEMENTAL PATENT SECURITY AGREEMENT, dated as of October 26, 2011 ("Supplemental Patent Security Agreement"), made by each of the signatories hereto (together with any other entity that may become a party hereto as provided herein, the "Grantors"), is in favor of MORGAN STANLEY & CO. LLC, as collateral agent (in such capacity, the "Collateral Agent") for the Secured Parties (in such capacity, the "Assignee").

W I T N E S S E T H:

WHEREAS, the Grantors are party to a Guarantee and Collateral Agreement dated as of November 2, 2010 (the "Guarantee and Collateral Agreement") in favor of the Assignee and MORGAN STANLEY SENIOR FUNDING, INC., as administrative agent (in such capacity, the "Administrative Agent") pursuant to which the Grantors are required to execute and deliver this Supplemental Patent Security Agreement (capitalized terms used but not otherwise defined herein shall have the meanings given to them in the Guarantee and Collateral Agreement);

WHEREAS, pursuant to the terms of the Guarantee and Collateral Agreement, each Grantor has created in favor of the Collateral Agent a security interest in, and the Collateral Agent has become a secured creditor with respect to, the Patent Collateral (as defined below).

NOW, THEREFORE, in consideration of the premises and to induce the Agents and the Lenders to enter into the Credit Agreement and to induce Lenders to make their respective extensions of credit to the Borrower thereunder and to induce the Qualified Counterparties to enter into the Specified Hedge Agreements and the Specified Cash Management Agreements and provide financial accommodation, each Grantor hereby grants to the Collateral Agent, for the benefit of the Secured Parties, a security interest in all of the following property now owned or at any time hereafter acquired by such Grantor or in which such Grantor now has or at any time in the future may acquire any right, title or interest (collectively, the "Patent Collateral"), as collateral security for the complete payment and performance when due (whether at the stated maturity, by acceleration or otherwise) of all Secured Obligations:

(a) all Patents of such Grantor, including, without limitation, the registered and applied-for Patents of such Grantor listed on Schedule 1 attached hereto; and

(b) to the extent not covered by clause (a), all Proceeds of any of the foregoing; provided, that (i) this Supplemental Patent Security Agreement shall not constitute a grant of a security interest in any property to the extent that and for as long as such grant of a security interest would be prohibited by the terms of the Guarantee and Collateral Agreement; and (ii) the security interest granted hereby (x) shall attach at all times to all proceeds of such property, (y) shall attach to such property immediately and automatically (without need for any further grant or act) at such time as the condition described in clause (i) ceases to exist and (z) to the extent severable shall in any event attach to all rights in respect of such property that are not subject to the applicable condition described in clause (i).

The security interest granted pursuant to this Supplemental Patent Security Agreement is granted in conjunction with security interest granted to the Assignee pursuant to the Guarantee and Collateral Agreement and Grantors hereby acknowledge and affirm that the rights and remedies of the Assignee with respect to the security interest in the Patents made and granted hereby are more fully set forth in the Guarantee and Collateral Agreement. In the event that any provision of this Supplemental Patent Security Agreement is deemed to conflict with the Guarantee and Collateral Agreement, the provisions of the Guarantee and Collateral Agreement shall govern.

Each Grantor hereby authorizes and requests that the Commissioner of Patents and Trademarks record this Supplemental Patent Security Agreement.

THIS SUPPLEMENTAL PATENT SECURITY AGREEMENT AND THE RIGHTS AND OBLIGATIONS OF THE PARTIES UNDER THIS PATENT SECURITY AGREEMENT SHALL BE GOVERNED BY, AND CONSTRUED AND INTERPRETED IN ACCORDANCE WITH, THE LAW OF THE STATE OF NEW YORK.

This Supplemental Patent Security Agreement may be executed by one or more of the parties to this Supplemental Patent Security Agreement on any number of separate counterparts, and all of said counterparts taken together shall be deemed to constitute one and the same instrument. Delivery of an executed signature page of this Supplemental Patent Security Agreement by facsimile transmission or electronic transmission (in PDF format) shall be effective as delivery of a manually executed counterpart hereof. A set of the copies of this Supplemental Patent Security Agreement signed by all the parties shall be lodged with the Borrower, the Administrative Agent and the Collateral Agent.

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IN WITNESS WHEREOF, each Grantor has caused this SUPPLEMENTAL PATENT SECURITY AGREEMENT to be executed and delivered by its duly authorized officer as of the date first above written.

MICROSEMI CORPORATION

By: _____
Name: John W. Hohener
Title: Executive Vice President, Chief Financial Officer, Secretary and Treasurer

MICROSEMI CORP. – ANALOG MIXED SIGNAL GROUP

By: _____
Name: John W. Hohener
Title: Vice President, Chief Financial Officer, Secretary and Treasurer

MICROSEMI CORP. – MASSACHUSETTS

By: _____
Name: John A. Caruso
Title: President, Chief Executive Officer, Chief Financial Officer and Secretary

ACTEL CORPORATION

By: _____
Name: John W. Hohener
Title: Chief Financial Officer and Secretary

IN WITNESS WHEREOF, each Grantor has caused this SUPPLEMENTAL PATENT SECURITY AGREEMENT to be executed and delivered by its duly authorized officer as of the date first above written.

MICROSEMI CORPORATION

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Name: John W. Hohener
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Name: John W. Hohener
Title: Vice President, Chief Financial Officer, Secretary and Treasurer

MICROSEMI CORP. – MASSACHUSETTS

By: _____
Name: John A. Caruso
Title: President, Chief Executive Officer, Chief Financial Officer and Secretary

ACTEL CORPORATION

By: _____
Name: John W. Hohener
Title: Chief Financial Officer and Secretary

[Signature page for Supplemental Patent Security Agreement]

Accepted and Agreed:
MORGAN STANLEY & CO. LLC, as Assignee

By: _____
Name: Andrew W. Earls
Title: Authorized Signatory

[Signature page for Supplemental Patent Security Agreement]

Schedule 1

PATENTS

Patent Registrations and Applications

Registered Patents:

	Registered Owner(s)	Patent Description	Registration Number	Registration Date
1.	Microsemi Corporation	SEMICONDUCTOR DEVICE WITH DOPED ELECTRICAL BREAKDOWN CONTROL REGION	5231474	07/27/93
2.	Microsemi Corporation	MASK SURROGATE SEMICONDUCTOR PROCESS WITH POLYSILICON GATE PROTECTION	5256583	10/26/93
3.	Microsemi Corporation	IGBT PROCESS TO PRODUCE PLATINUM LIFETIME CONTROL	5262336	11/16/93
4.	Microsemi Corporation	Using a triangular waveform to synchronize the operation of an electronic circuit	7852019	12/14/10
5.	Microsemi Corporation	Fine tuned multiple output converter	7906868	03/15/11
6.	Microsemi Corporation	Single LED string lighting	8008864	08/30/11
7.	Microsemi Corporation	Arrangement suitable for driving floating CCFL based backlight	8008867	08/30/11
8.	Microsemi Corporation	CCFL controller with multi-function terminal	8022635	09/20/11
9.	Microsemi Corporation	Direct coupled balancer driver for floating lamp structure	7977888	07/12/11
10.	Microsemi Corporation	Balancing arrangement with reduced amount of balancing transformers	7990072	08/02/11
11.	Microsemi Corporation	Method and apparatus for modifying right half-plane zero in a cascaded DC-DC buck-boost converter	7977928	07/12/11
12.	Microsemi Corporation	System and method for IM3 reduction and cancellation in amplifiers	7573329	08/11/09
13.	Microsemi Corporation	Inter-stage matching network to enhance common mode stability	7924092	04/12/11
14.	Microsemi Corporation	Apparatus and method for striking a fluorescent lamp	6979959	12/27/25
15.	Microsemi Corporation	Apparatus and method for striking a fluorescent lamp	7279852	10/09/07
16.	Microsemi Corporation	Apparatus and method for striking a fluorescent lamp	7411360	08/12/08
17.	Microsemi Corporation	Shorted lamp detection in backlight system	6870330	03/22/05
18.	Microsemi Corporation	Method and apparatus for auto-interleaving synchronization in a multiphase switching power converter	6965219	11/15/05
19.	Microsemi Corporation	Method and apparatus for dithering auto-synchronization of a multiphase switching power converter	6836103	12/28/04
20.	Microsemi Corporation	Split phase inverters for CCFL backlight system	7952298	05/31/11
21.	Microsemi Corporation	Automatic gain control technique for current monitoring in current-mode switching regulators	7919952	04/05/11
22.	Microsemi Corporation	Balancing transformers for multi-lamp operation	7932683	04/26/11
23.	Microsemi Corporation	Method and apparatus to compensate for supply voltage variations in a PWM-based voltage regulator	7868603	01/11/11
24.	Microsemi Corporation	Boost converter with adaptive coil peak current	7906943	03/15/11
25.	Microsemi Corporation	Security system housing	D634229	03/15/11
26.	Microsemi Corporation	Pedestal mount for security camera	D618269	06/22/11
27.	Microsemi Corporation	Multiple camera imaging method and system for detecting concealed objects	7873182	01/18/11

	Registered Owner(s)	Patent Description	Registration Number	Registration Date
28.	Microsemi Corporation	Broadband energy illuminator	7601958	10/13/09
29.	Microsemi Corporation	Security system housing	D571244	06/17/08
30.	Microsemi Corporation	System and method for manipulating real-time video playback time-synchronized with millimeter wave imagery	7781717	08/24/10
31.	Microsemi Corporation	System for deployment of a millimeter wave concealed object detection system using an outdoor passively illuminated structure	7858938	12/28/10
32.	ASIC Advantage, Inc.[1]	Integrated multi-transformer	7956491	06/07/11
33.	ASIC Advantage, Inc.[2]	Voltage level shifter for arbitrary input signals	7911255	03/22/11
34.	ASIC Advantage, Inc.[3]	Controller for switch mode power supple	6636025	10/21/03
35.	ASIC Advantage, Inc.[4]	Power converter having a low voltage regulator powered from a high voltage source	6233165	05/15/01
36.	ASIC Advantage, Inc.[5]	Voltage level shifter	7782115	08/24/10
37.	Microsemi Microwave Products[6]	Monolithic surface mount optoelectronic device and method for fabricating the device	6759688	07/06/04
38.	Microsemi Corporation	Battery cell bypass with pre-loaded compression action	6294766	09/25/01
39.	Microsemi Corporation	Schottky barrier code (SBD) and its off-shoot merged PN/Schottky diode or junction barrier Schottky (JBS diode	7851881	12/14/10
40.	Microsemi Corporation	RF power transistor package	7956455	06/07/11
41.	Microsemi Corporation	Coaxial-to-microstrip transitions	7915981	03/29/11
42.	Microsemi Corporation	Full-bridge compatible driver timing schedule for direct drive backlight system	7965046	06/21/11
43.	Microsemi Corporation	Method and apparatus for stacked die package with insulated wire bonds	7939928	02/10/11
44.	Microsemi Corporation	Charge limited high voltage switch circuits	7449841	11/11/08
45.	Microsemi Corporation[7]	Flexible diode package and method of manufacturing	8003446	08/23/11
46.	Microsemi Corporation[8]	Integrated circuit with flexible planar leads	8018042	09/13/11
47.	Actel Corporation	Programmable interconnect architecture	6160420	12/12/00
48.	Actel Corporation	Logic module with configurable combinational and sequential blocks	5781033	07/14/98
49.	Actel Corporation	Programmable logic module and architecture for field programmable gate array device	5698992	12/16/97
50.	Actel Corporation	Programmable logic module and architecture for field programmable gate array device	5606267	02/25/97
51.	Actel Corporation	Programmable interconnect architecture	5600265	02/04/97
52.	Actel Corporation	Programmable logic module and architecture for field programmable gate array device	5570041	10/29/96
53.	Actel Corporation	Reconfigurable programmable interconnect architecture	5510730	04/23/96

[1] This patent was acquired by Microsemi Corp. - Analog Mixed Signal Group.
[2] This patent was acquired by Microsemi Corp. - Analog Mixed Signal Group.
[3] This patent was acquired by Microsemi Corp. - Analog Mixed Signal Group.
[4] This patent was acquired by Microsemi Corp. - Analog Mixed Signal Group.
[5] This patent was acquired by Microsemi Corp. - Analog Mixed Signal Group.
[6] This patent was acquired by Microsemi Corp. - Massachusetts, is of immaterial value, and may be abandoned.
[7] This patent is of immaterial value and may be abandoned.
[8] This patent is of immaterial value and may be abandoned.

	Registered Owner(s)	Patent Description	Registration Number	Registration Date
54.	Actel Corporation	Testability architecture and techniques for programmable interconnect architecture	5223792	06/29/93
55.	Actel Corporation	Testability architecture and techniques for programmable interconnect architecture	5208530	05/04/93
56.	Actel Corporation	Logic module with configurable combinational and sequential blocks	5198705	03/30/93
57.	Actel Corporation	Methods for preventing disturbance of antifuses during programming	5194759	03/16/93
58.	Actel Corporation	Programmable interconnect architecture	5191241	03/02/93
59.	Actel Corporation	Reconfigurable programmable interconnect architecture	5187393	02/16/93
60.	Actel Corporation	Method and apparatus for universal program controlled bus architecture	7830173	11/09/10
61.	Actel Corporation	Non-volatile programmable memory cell and array for programmable logic array	7838944	11/23/10
62.	Actel Corporation	Push-pull FPGA cell	7839681	11/23/10
63.	Actel Corporation	PROGRAMMABLE SYSTEM ON A CHIP FOR POWER-SUPPLY VOLTAGE AND CURRENT MONITORING AND CONTROL	7859302	12/28/10
64.	Actel Corporation	PLD Providing Soft Wakeup Logic	7884640	02/08/11
65.	Actel Corporation	Flash Based FPGA with Secure Reprogramming	7885122	02/08/11
66.	Actel Corporation	Single Event Transient Mitigation and Measurement In Integrated Circuits	7884636	02/08/11
67.	Actel Corporation	Field Programmable Gate Array and Microcontroller System on a Chip	7886130	02/08/11
68.	Actel Corporation	Programmable Logic Device Adapted to Enter a Low-Power Mode	7886261	02/08/11
69.	Actel Corporation	Non-Volatile Two-Transistor Programmable Logic Cell and Array Layout	7898018	03/01/11
70.	Actel Corporation	Power-Up and Power-Down Circuit for System-on-a-Chip Integrated Circuit	7911226	03/22/11
71.	Actel Corporation	Isolated-Nitride-Region Non-Volatile Memory Cell and Fabrication Method	7910436	03/22/11
72.	Actel Corporation	Reduced-Edge Radiation-Tolerant Non-Volatile Transistor Memory Cells	7906805	03/15/11
73.	Actel Corporation	Method and Apparatus for Universal Program Controlled Bus Architecture	7915918	03/29/11
74.	Actel Corporation	Non-Volatile Two-Transistor Programmable Logic Cell and Array Layout	7915,665	03/29/11
75.	Actel Corporation	CIRCUITS AND METHODS FOR TESTING FPGA ROUTING SWITCHES	7919977	04/05/11
76.	Actel Corporation	Field Programmable Gate Array Including a Non-Volatile User Memory and Method for Programming	7919979	04/05/11
77.	Actel Corporation	Programmable Logic Device with a Microcontroller-Based Control System	7924051	04/12/11
78.	Actel Corporation	Field Programmable Gate Array Architecture Having CLOS Network-Based Input Interconnect	7924052	04/12/11
79.	Actel Corporation	Clustered Field Programmable Gate Array Architecture	7924053	04/12/11
80.	Actel Corporation	Push-Pull Memory Cell Configured for Simultaneous Programming on N-channel and P-channel Non-Volatile Transistors	7929345	04/19/11
81.	Actel Corporation	Staggered I/O Groups for Integrated Circuits	7932744	04/26/11
82.	Actel Corporation	Inverting Flip-Flop for Use in Field Programmable Gate Arrays	7932745	04/26/11
83.	Actel Corporation	Programmable System on a Chip	7937601	05/03/11

	Registered Owner(s)	Patent Description	Registration Number	Registration Date
84.	Actel Corporation	Error-Detecting and Correcting FPGA Architecture	7937647	05/03/11
85.	Actel Corporation	Delayed Locked Loop for an FPGA Architecture	7941685	05/10/11
86.	Actel Corporation	(N+1) Input Flip-Flop Packing with Logic in FPGA Architectures	7944238	05/17/11
87.	Actel Corporation	Non-Volatile Two-Transistor Programmable Logic Cell and Array Layout	7956404	06/07/11
88.	Actel Corporation	Enhanced field programmable gate array	7977970	07/12/11
89.	Actel Corporation	Flexible carry scheme for field programmable gate arrays	7872497	01/18/11
90.	Actel Corporation	Block connector splitting in logic block of a field programmable gate array	6285212	09/04/01
91.	Actel Corporation	Method of reducing test time for NVM cell-based FPGA	6272655	08/07/01
92.	Actel Corporation	Nonvolatile reprogrammable interconnect cell with FN tunneling device for programming and erase	6252273	06/26/11
93.	Actel Corporation	SRAM bus architecture and interconnect to an FPGA	6564273	withdrawn
94.	Actel Corporation	Nonvolatile reprogrammable interconnect cell with programmable buried bitline	6072720	06/06/00
95.	Actel Corporation	Method for erasing nonvolatile memory cells in a field programmable gate array	6125059	09/26/00

Patent Applications:

	Registrant (or Last Registered Owner)	Patent Description	Application Number (Incl. Pub No.)	File Date
1.	Microsemi Corporation	Integrated backlight control system	12/815,441 (20100327761)	06/15/10
2.	Microsemi Corporation	Low voltage drop closed loop unidirectional electronic valve	12/815,496 (20110006232)	06/15/10
3.	Microsemi Corporation	Circuit and method for temperature and process independent transimpedance amplifier arrangement	12/861,919 (20110062311)	08/24/10
4.	Microsemi Corporation	Distributed architecture voltage controlled backlight driver	12/895,875 (20110080117)	10/01/10
5.	Microsemi Corporation	Non-dissipative start up circuit	12/960,504 (20110141775)	12/05/10
6.	Microsemi Corporation	Converter with crossover frequency responsive to switching frequency	12/960,505 (20110148381)	12/05/10
7.	Microsemi Corporation	Dimming input suitable for multiple dimming signal types	13/017,128 (20110187283)	01/31/11
8.	Microsemi Corporation	Battery charging and discharging by using a by-directional transistor	12/838,254 (20100277125)	07/16/10
9.	Microsemi Corporation	Balancing transformers for multi-lamp operation	13/084,229 (20110181204)	04/11/11
10.	Microsemi Corporation	Silicon carbide semiconductor	12/658,328 (20110193097)	02/09/10
11.	Microsemi Corporation	High voltage high package pressure semiconductor package	12/658,576 (20110193098)	02/09/10
12.	Microsemi Corporation	Inclusion of assessment data in millimeter wave concealed object detection systems	12/338,780 (20090184861)	12/18/08
13.	Microsemi Corporation	Software methodology for autonomous concealed object detection and threat assessment	12/338,807 (20090297039)	12/18/08
14.	Microsemi Corporation	Millimeter Wave Concealed Object Detection System Using Portal Deployment	12/867,807 (20110102597)	12/29/10
15.	Microsemi Corporation	Multi channel radiometer imaging method and system	11/835,886 20090041292	08/08/07

	Registrant (or Last Registered Owner)	Patent Description	Application Number (Incl. Pub No.)	File Date
16.	Microsemi Corporation	Mirror scanning system	12/043,246 (20090225382)	03/06/08
17.	Microsemi Corporation	System for deployment of a millimeter wave concealed object detection system	12/110,520 (20080266165)	04/28/08
18.	Microsemi Corporation	Passive outdoor millimeter waive illuminator	12/118,960 (20090015457)	05/12/08
19.	Microsemi Corporation	Method and system for dynamically altering the analysis methodology of millimeter wave imagery in response to the range and direction of motion of a subject	12/131,539 (20110037139)	10/26/10
20.	Microsemi Corporation	Structural system for optimizing performance of a millimeter wave concealed object detection system	12/136,000 (20080303708)	06/09/08
21.	Microsemi Corporation	System and method for overlaying computer generated highlights in a display of millimeter wave imagery	12/142,982 (20090060272)	06/20/08
22.	Microsemi Corporation	Variable range millimeter wave method and system	12/575,604 (20110084868)	10/08/09
23.	Microsemi Corporation[9]	Information currently unavailable	12/893,171	Information currently unavailable
24.	Microsemi Corporation[10]	Information currently unavailable	12/846,240	Information currently unavailable
25.	Microsemi Corporation[11]	Information currently unavailable	13/019,722	Information currently unavailable
26.	Microsemi Corporation[12]	Information currently unavailable	29/385,710	Information currently unavailable
27.	Microsemi Corporation[13]	Information currently unavailable	29/379,049	Information currently unavailable
28.	Microsemi Corporation[14]	Information currently unavailable	29/385,707	Information currently unavailable
29.	Microsemi Corporation	SCHOTTKY BARRIER DIODE (SBD) AND ITS OFF-SHOOT MERGED PN/SCHOTTKY DIODE OR JUNCTION BARRIER SCHOTTKY (JBS) DIODE	12/912,539 (20110037139)	10/26/10
30.	Microsemi Corporation	RF PACKAGE	12/938,974 (20110116237)	11/03/10
31.	Microsemi Corporation	MULTI-LAYER THICK-FILM RF PACKAGE	12/941,401 (20110117705)	11/08/10
32.	Microsemi Corporation	SILICON CARBIDE DUAL-MESA STATIC INDUCTION TRANSISTOR	12/870,699 (20110049532)	08/27/10
33.	Microsemi Corporation	FIXING APPARATUS AND AN IMAGE FORMATION APPARATUS	12/030,907 (20080292347)	02/14/08
34.	ASIC Advantage, Inc.[15]	Semiconductor package with embedded magnetic component and method of manufacture	12/383,468 (20090237899)	03/23/09
35.	ASIC Advantage, Inc.[16]	PHASE-CUT DIMMING CIRCUIT	12/404,979 (20090243582)	03/16/09

[9] The missing information is current unavailable as the applications may still be in the secrecy period.
[10] The missing information is current unavailable as the applications may still be in the secrecy period.
[11] The missing information is current unavailable as the applications may still be in the secrecy period.
[12] The missing information is current unavailable as the applications may still be in the secrecy period.
[13] The missing information is current unavailable as the applications may still be in the secrecy period.
[14] The missing information is current unavailable as the applications may still be in the secrecy period.
[15] This patent application was acquired by Microsemi Corp. - Analog Mixed Signal Group.

	Registrant (or Last Registered Owner)	Patent Description	Application Number (Incl. Pub No.)	File Date
36.	ASIC Advantage, Inc.17	PULSE TRANSFORMER DRIVER	12/416,363 (20090243683)	04/01/09
37.	ASIC Advantage, Inc.18	POWER SUPPLY WITH STANDBY POWER	12/472,539 (20090295228)	05/27/09
38.	ASIC Advantage, Inc.19	Bus voltage optimizer for switched power converter	12/536,596 (20100033150)	08/06/09
39.	ASIC Advantage, Inc.20	LIGHT EMITTING DIODE FAULT MONITORING	12/545,437 (20100049454)	08/21/09
40.	ASIC Advantage, Inc.21	Battery charge and discharge controller	12/719,204 (20100225277)	03/08/10
41.	ASIC Advantage, Inc.22	High-speed comparator	13/012,645 (20110204923)	01/24/11
42.	ASIC Advantage, Inc.23	Boundary mode coupled inductor boost power converter	12/824,301 (20100328971)	06/28/10
43.	ASIC Advantage, Inc.24	Multiple independently regulated parameters using a single magnetic circuit element	12/850,120 (20110032731)	08/04/10
44.	Microsemi Corporation	RF switchable balun	12/903,387 (20110128088)	10/13/10
45.	Microsemi Corporation25	Method and Apparatus for Secure Data Storage System	12/181,533	07/29/08
46.	Microsemi Corporation26	Method and apparatus to control display brightness with ambient light correction	12/336990	12/17/08
47.	Microsemi Corporation27	Process for fabricating a homogenously mixed powder/ pelletized compound for use in light emitting devices	11/079323	03/14/05

(cont'd from previous page)

[16] This patent application was acquired by Microsemi Corp. - Analog Mixed Signal Group.
[17] This patent application was acquired by Microsemi Corp. - Analog Mixed Signal Group.
[18] This patent application was acquired by Microsemi Corp. - Analog Mixed Signal Group.
[19] This patent application was acquired by Microsemi Corp. - Analog Mixed Signal Group.
[20] This patent application was acquired by Microsemi Corp. - Analog Mixed Signal Group.
[21] This patent application was acquired by Microsemi Corp. - Analog Mixed Signal Group.
[22] This patent application was acquired by Microsemi Corp. - Analog Mixed Signal Group.
[23] This patent application was acquired by Microsemi Corp. - Analog Mixed Signal Group.
[24] This patent application was acquired by Microsemi Corp. - Analog Mixed Signal Group.
[25] This patent is of immaterial value and may be abandoned.
[26] This patent is of immaterial value and may be abandoned.
[27] This patent is of immaterial value and may be abandoned.