11/08/2011

B/O FORM PTO 1595 Patent and Trademark RECORDATION FOI PATENTS ONLY



103636094

U.S. PTO 13/317846 10/31/2011 U.S. Department of Commerce

1 Name of Conveying Party(ies):	2 Name and Address of Receiving Party(ies):
(1) NORIAKI MAEDA (2) YOSHIHIRO SHINOZAKI (3) MASANAO YAMAOKA (4) YASUHISA SHIMAZAKI (5) MASANORI ISODA AND (6) KOJI NII	Name: Renesas Technology Corp. Address: 4-1, Marunouchi 2-chome, Chiyoda-ku City: Tokyo Japan
Additional names of conveying parties attached: Yes No	
3 Nature of Conveyance:	
4. (a) Patent Application Number(s):	4 (b) Patent Numbers
If this document is being filed together with a new application, the execution date of the application is: Additional Numbers Attached.	
 Name and Address of Party to whom Correspondence Concerning this Document Should be Mailed: Name: Juan Carlos A. Marquez 	6. Total Number of Applications and Patents Involved:
Address: STITES & HARBISON, PLLC 1199 North Fairfax Street Suite 900 Alexandria, VA 22314	7. Total Fee: \$40.00 (37 C.F.R. § 3.41) Payment by credit card. Form PTO-2038 is attached. Authorized to be charged to deposit account.
	8. Deposit Account Number: 12-0555 ATTACH DUPLICATE COPY OF THIS PAGE IF PAYING BY DEPOSIT ACCOUNT
DO NOT USE THIS SPACE	ATTACTEC
9. Statement and Signature: To the best of my knowledge and belief, the foregoing is true original document. Juan Carlos A. Marquez, Registration No. 34,072	and correct and any attached copy is a true copy of the October 31, 201
Juan Carros Ar tran queen regional	

PATENT

REEL: 027298 FRAME: 0196

ASSIGNMENT (譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by Renesas Technology Corp., a corporation organized under the laws of Japan, located at 4-1, Marunouchi 2-chome, Chiyoda-ku, Tokyo, Japan, receipt of which is hereby acknowledged I do hereby sell and assign to said Renesas Technology Corp., its successors and assigns, all my right, title and interest, in and for the United States of America, in and to

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

invented by me (If only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted,

to be held and enjoyed by said Renesas Technology Corp.,

its successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

Date Signed

(聚名目)

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said Renesas Technology Corp.,

Signed on the date(s) indicated aside our signatures:

INVENTOR(S)

(※明本コルラニルサイン)

(光明石ブルイン	-A942)	(FIDA)
1) <u>horiaki Maeda</u>	Noriaki MAEDA	30/March /2006
2) YoshiheroShimojaki	Yoshihiro SHINOZAKI	30/March / 2005
3) Mewaner Yours Loa	Masanao YAMAOKA	30 /March / 2005
4) Yasuhin Shimozalii	Yasuhisa SHIMAZAKI	30/March/2005
6) Masanorl Isoda	Masanori ISODA	30/March/2005
6) Koji Zivi	Koji NII	12/April/2005
7)		
8)		·

PATENT
RECORDED: 10/31/2011 REEL: 027298 FRAME: 0197