

PATENT ASSIGNMENT

Electronic Version v1.1
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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
LSTAR Technologies LLC	06/23/2011
RECEIVING PARTY DATA	
Name:	Empire Technology Development LLC
Street Address:	2711 Centerville Road
Internal Address:	Suite 400
City:	Wilmington
State/Country:	DELAWARE
Postal Code:	19808
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	12429580
CORRESPONDENCE DATA	
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<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
Correspondent Name:	Hope Baldauff Hartman
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Address Line 2:	Suite 1010
Address Line 4:	Atlanta, GEORGIA 30309
ATTORNEY DOCKET NUMBER:	60069.0001US01
NAME OF SUBMITTER:	Steven Koon Hon Wong

Total Attachments: 6
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ASSIGNMENT OF PATENT RIGHTS

For valuable consideration, the receipt and sufficiency of which is hereby acknowledged, **LSTAR TECHNOLOGIES LLC** ("Assignor"), effective as of June 23, 2011, hereby sells, assigns, transfers, and Conveys unto Empire Technology Development LLC, a Delaware limited liability company, with an address at 2711 Centerville Road, Suite 400, Wilmington, DE 19808 ("Assignee"), all rights, title, and interests that exist today and may exist in the future in and to any and all of the following items (1) through (8) below:

1. the inventions disclosed in the solution report/invention disclosure with the titles indicated below and all inventions claimed and/or described in the Application (collectively, the "Invention");
2. the patent applications listed in the table below (the "Application");

Patent Application No.	Country	Filing Date	Patent Application Title	Invention Disclosure/Solution Report Title
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12/429,580	US	04/24/2009	Interrupt Optimization for Multiprocessors	External Interrupt Optimization Device for Single-chip multiprocessors.
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Patent Application No.	Country	Filing Date	Patent Application Title	Invention Disclosure/Solution Report Title
201010124820.3	CN	02/26/2010	Interrupt Optimization for Multiprocessors	External Interrupt Optimization Device for Single-chip multiprocessors.

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3. all rights with respect to the Invention, including all United States patents or other governmental grants or issuances that may be granted with respect to the Invention or from any direct or indirect divisionals, continuations, continuations-in-part, or other patent applications claiming priority rights from the Application ("**Potential Patents**");
4. all reissues, reexaminations, extensions, or registrations of the Potential Patents;
5. all non-United States patents, patent applications, and counterparts relating to any or all of the Invention, the Application, and the Potential Patents, including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances ("**Foreign Rights**"), and including the right to file foreign applications directly in the name of Assignee, its successors and assigns;
6. all rights to claim priority rights deriving from the Application;
7. all causes of action and remedies related to any or all of the Application, the Invention, the Potential Patents, and the Foreign Rights (including, without limitation, the right to sue for past, present, or future infringement, misappropriation or violation of rights related to any of the foregoing and the right to collect royalties and other payments under or on account of any of the foregoing); and
8. any and all other rights and interests arising out of, in connection with, or in relation to the Application, the Invention, Potential Patents, and the Foreign Rights.

Assignor will not sign any document or do any act conflicting with this Assignment, and, without further compensation, will sign all documents and do such additional acts as Assignee, its successors, legal representatives, and assigns deem necessary or desirable to perfect enjoyment of the Rights, conduct proceedings regarding the Rights (including any litigation or interference proceedings), or

perfect or defend title to the Rights. Assignor requests the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models, or other governmental grants or issuances that may be granted upon any of the Rights in the name of the Assignee, as the assignee to the entire interest therein.

Assignor hereby authorizes and requests Assignee's authorized representative to insert on this Assignment the filing date and Patent Application Numbers in the table above when known.

The terms and conditions of this Assignment will inure to the benefit of Assignee, its successors, legal representatives, and assigns and will be binding upon Assignor, their successors, legal representatives and assigns.

[Signature Page(s) to Follow]

ASSIGNOR:

LSTAR TECHNOLOGIES LLC

By: [Signature]
Print name: Eric Bell
Title: Authorized Person

NOTARIZATION

STATE OF Washington)
) ss.
COUNTY OF King)

On June 23, 2011, before me, Paula Hutchison, Notary Public
in and for said State, personally appeared Eric Bell, personally known to me
(or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to
the within instrument and acknowledged to me that ~~he~~ she executed the same in his/her authorized
capacity, and that by ~~his~~ her signature on the instrument the person, or the entity upon behalf of which
the person acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of Washington
that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.

Signature: [Signature] (Seal)
Print Name: Paula Hutchison

