

# PATENT ASSIGNMENT

Electronic Version v1.1  
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<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
CoFluent Design	08/30/2011
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	Intel Corporation
<b>Street Address:</b>	2200 Mission College Boulevard
<b>City:</b>	Santa Clara
<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	95052
<b>PROPERTY NUMBERS Total: 6</b>	
<b>Property Type</b>	<b>Number</b>
Patent Number:	7885802
Patent Number:	7987083
Patent Number:	7991603
Patent Number:	7925490
Application Number:	13162042
Application Number:	13161197
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(408)720-8383
<b>Phone:</b>	4087208300
<b>Email:</b>	chester_vistan@bstz.com
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Correspondent Name:</b>	Blakely, Sokoloff, Taylor, & Zafman LLP
<b>Address Line 1:</b>	1279 Oakmead Parkway
<b>Address Line 4:</b>	Sunnyvale, CALIFORNIA 94085
<b>ATTORNEY DOCKET NUMBER:</b>	042390.G01111

CH \$240.00 7885802

**501755843**

**PATENT**  
**REEL: 027386 FRAME: 0006**

NAME OF SUBMITTER:

Edwin H. Taylor Reg No. 25,129

**Total Attachments: 18**

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**PATENT ASSIGNMENT**

WHEREAS, CoFluent Design, a "*société anonyme à directoire et conseil de surveillance*" registered under number 448 116 186 R.C.S. Versailles, incorporated under the laws of France, whose registered office is at 24 rue Jean Duplessis 78150 Le Chesnay - France (hereinafter "**Company**"), is the sole and exclusive owner of certain United States and/or non-United States/foreign patents and/or patent applications listed in Exhibit 1 annexed hereto (collectively referred to as the "**Patents and Patent Applications**");

AND WHEREAS, the Company and Intel Corporation, a Delaware corporation, with an office at 2200 Mission College Boulevard, Santa Clara, California 95054 (hereinafter "**Intel**") have entered into an Intellectual Property Transfer and License Agreement dated as of 30 August 2011 (the "**Transfer Agreement**");

AND WHEREAS, Intel is desirous of acquiring the right, title and interest in, to and under the said Patents and Patent Applications (and all foreign counterparts and related foreign patents).

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, the Company does hereby sell, transfer, convey, assign and deliver to Intel all of the Company's worldwide right, title and interest in and to the Patent and Patent Applications and any patents that may issue therefrom, including any foreign counterparts, divisions, continuations, or reissues of such patents, the same to be held by Intel for Intel's own use and enjoyment, and for the use and enjoyment of Intel's successors, assigns and other legal representatives, as fully and entirely as the same would have been held and enjoyed by the Company if this Patent Assignment and sale had not been made; together with all claims for damages by reason of past infringements of the Patent and Patent Applications, along with the right to sue for and collect such damages for the use and benefit of Intel and its successors, assigns and other legal representatives;

AND the Company hereby authorizes and requests the Commissioner of Patents and Trademarks of the United States, and any officer of any country or countries foreign to the United States, whose duty it is to issue patents or other evidence or forms of intellectual property protection or applications as aforesaid, to issue the same to Intel and its successors, assigns and other legal representatives in accordance with the terms of this instrument;

AND the Company does hereby covenant with Intel and the successors and permitted assigns of Intel that, from time to time after the date hereof, the Company will promptly execute and deliver to Intel or shall promptly procure the execution and delivery of any and all such instruments of sale, transfer, conveyance, assignment and delivery, consents, assurances, powers of attorney and other instruments as may reasonably be requested by Intel in order to vest in Intel all of the Company's right, title

and interest in and to the Patents and Patent Applications and carry out the purpose and intent of this Patent Assignment and the Transfer Agreement;

AND notwithstanding any other provisions of this Patent Assignment to the contrary, nothing contained in this Patent Assignment shall in any way supersede, modify, replace, amend, change, rescind, waive, exceed, expand, enlarge, or in any way affect the provisions, including warranties, covenants, agreements, conditions, representations, or in general any of the rights and remedies, or any of the obligations and indemnifications of the Company or Intel set forth in the Transfer Agreement. This Patent Assignment is intended only to effect the transfer of certain intellectual property transferred pursuant to the Transfer Agreement and shall be governed entirely in accordance with the terms and conditions of the Transfer Agreement;

AND the terms and covenants of this Patent Assignment shall inure to the benefit of Intel, its successors and assigns, and shall be binding upon the Company and its successors and assigns. All capitalized terms used herein but not otherwise defined shall have the meanings set forth in the Transfer Agreement.

[signature page follows]

EXECUTION VERSION

IN WITNESS WHEREOF, the Company has executed this Assignment of Patent Rights with effect from 30<sup>th</sup> August 2011.

ASSIGNOR:

COFLUENT DESIGN, a "*société anonyme à  
directoire et conseil de surveillance*"

By:  \_\_\_\_\_

Name: S. NEGRE  
Title: PRESIDENT

ASSIGNEE:

INTEL CORPORATION, a Delaware  
corporation

By: \_\_\_\_\_

Name: \_\_\_\_\_

Title: \_\_\_\_\_

SIGNATURE PAGE TO THE INTEL – COFLUENT DESIGN PATENT ASSIGNMENT

**EXHIBIT 1**  
**TO THE INTEL-COFLUENT PATENT ASSIGNMENT**

**ISSUED U.S. PATENTS**

- |     |   |
|-----|---|
| (1) | <p>U.S. Patent Number: 7,885,802</p> <p>Issue Date: February 8, 2011</p> <p>Cofluent reference #: R10383US</p> <p>Priority date &amp; priority app #: FR0501486 14/02/05</p> <p>Inventor(s): Jean-Paul CALVEZ</p> <p>Title: Method for simulating a complex system including a scheduler hierarchy, and corresponding storage means and computer program product</p> <p>File Type: PCT National Stage</p> <p>Country of Filing: US</p> <p>Serial #: 11/815,829</p> <p>Filing date: September 18, 2008</p> <p>Publication #: US-2008-0228461-A1</p> <p>Status: Issued</p>                          |
| (2) | <p>U.S. Patent Number: 7,987,083</p> <p>Issue Date: July 26, 2011</p> <p>Cofluent reference #: R11870US</p> <p>Priority date &amp; priority app #: FR0605094 08/06/06</p> <p>Inventor(s): Jean-Paul CALVEZ</p> <p>Title: Method for simulating a complex system with construction of at least one model including at least one modelled router, corresponding computer software package and storage means</p> <p>File Type: National Filing</p> <p>Country of Filing: US</p> <p>Serial #: 11/760,252</p> <p>Filing date: 08/06/07</p> <p>Publication #: US-2008-0004851</p> <p>Status: Issued</p> |
| (3) | <p>U.S. Patent Number: 7,991,603</p> <p>Issue Date: 2 August 2011</p> <p>Cofluent reference #: R12337US</p>   |

## EXECUTION VERSION

Priority date & priority app #:	FR0608913 11/10/06
Inventor(s):	Jean-Paul CALVEZ
Title:	Method for simulating a complex system with expansion of instance vectors, corresponding computer program product and storage means
File Type:	National Filing
Country of Filing:	US
Serial #:	11/870,920
Filing date:	11/10/07
Publication #:	US-2008-0091401
Status:	Issued
(4) U.S. Patent Number:	7,925,490
Issue Date:	April 12, 2011
Cofluent reference #:	R13081US
Priority date & priority app #:	FR0702376 30/03/07
Inventor(s):	Jean-Paul CALVEZ
Title:	Method of transactional simulation of a generic communication node model, and the corresponding computer program product and storage means
File Type:	National Filing
Country of Filing:	US
Serial #:	12/059,310
Filing date:	31/03/08
Publication #:	US-2008-0243464-A1
Status:	Issued

## PENDING U.S. APPLICATIONS

(1) U.S. Patent Number:	n/a
Issue Date:	n/a
Cofluent reference #:	R11870US-2
Priority date & priority app #:	FR0605094 08/06/06; US 11/10/07
11/870,920	
Inventor(s):	Jean-Paul CALVEZ
Title:	Method for simulating a complex system with construction of at least one model including at least one modelled router, corresponding computer software package and storage means
File Type:	Continuation

EXECUTION VERSION

Country of Filing: US  
Serial #: 13/162,042  
Filing date: June 16, 2011  
Publication #: n/a  
Status: Pending

(2) U.S. Patent Number: n/a  
Issue Date: n/a  
Confluent reference #: R12337US-2  
Priority date & priority app #: FR0608913 11/10/06; US 31/03/08  
12/059,310  
Inventor(s): Jean-Paul CALVEZ  
Title: Method for simulating a complex  
system with expansion of instance  
vectors, corresponding computer  
program product and storage means  
Continuation  
File Type:  
Country of Filing: US  
Serial #: 13/161,197  
Filing date: June 15, 2011  
Publication #: n/a  
Status: Pending

**ISSUED FRENCH PATENTS**

- |     |                                 |  |
|-----|---------------------------------|--|
| (1) | French Patent Number:           | 2882169  |
|     | Issue Date:                     | May 25, 2007   |
|     | Cofluent reference #:           | R10383FR   |
|     | Priority date & priority app #: | n/a  |
|     | Inventor(s):                    | Jean-Paul CALVEZ   |
|     | Title:                          | Procédé de simulation d'un système complexe incluant une hiérarchie d'ordonnanceurs, produit programme d'ordinateur et moyen de stockage correspondants    |
|     | File Type:                      | National Filing  |
|     | Country of Filing:              | FR   |
|     | Serial #:                       | 0501486  |
|     | Filing date:                    | 14/02/05   |
|     | Publication #:                  | 2882169  |
|     | Status:                         | Issued   |
|     |                                 |  |
| (2) | French Patent Number:           | 2914525  |
|     | Issue Date:                     | May 29, 2009   |
|     | Cofluent reference #:           | R13081FR   |
|     | Priority date & priority app #: | n/a  |
|     | Inventor(s):                    | Jean-Paul CALVEZ   |
|     | Title:                          | Procédé de simulation transactionnelle d'un modèle générique de noeud de communication, produit programme d'ordinateur et moyen de stockage correspondants |
|     | File Type:                      | National Filing  |
|     | Country of Filing:              | FR   |
|     | Serial #:                       | 0702376  |
|     | Filing date:                    | 30/03/07   |
|     | Publication #:                  | 2914525  |
|     | Status:                         | Issued   |

**PENDING FRENCH APPLICATIONS AWAITING GRANT**

- |     |                                 |          |
|-----|---------------------------------|----------|
| (1) | French. Patent Number:          | n/a      |
|     | Issue Date:                     | n/a      |
|     | Cofluent reference #:           | R12337FR |
|     | Priority date & priority app #: | n/a      |

## EXECUTION VERSION

Inventor(s): Jean-Paul CALVEZ  
Title: Procédé de simulation d'un système complexe avec expansion de vecteurs d'instances, produit programme d'ordinateur et moyen de stockage correspondants  
File Type: National Filing  
Country of Filing: FR  
Serial #: 0608913  
Filing date: October 11, 2006  
Publication #: 2907240  
Status: Grant pending

(2) French. Patent Number: n/a  
Issue Date: n/a  
Cofluent reference #: R11870FR  
Priority date & priority app #: n/a  
Inventor(s): Jean-Paul CALVEZ  
Title: Procédé de simulation d'un système complexe avec construction d'au moins un modèle comprenant au moins un routeur modélisé, produit programme d'ordinateur et moyen de stockage correspondants  
File Type: National Filing  
Country of Filing: FR  
Serial #: 0605094  
Filing date: June 8, 2006  
Publication #: 2902211  
Status: Grant pending

## WITHDRAWN EUROPEAN APPLICATION

(1) Patent Number: n/a  
Issue Date: n/a  
Cofluent reference #: unknown  
Priority date & priority app #: FR20050001486 14.02.2005  
Inventor(s): Jean-Paul CALVEZ  
Title: PROCEDE DE SIMULATION D'UN PROCESSEUR UTILISANT UN MODELE COMPRENANT UN ENSEMBLE DE OBJETS

EXECUTION VERSION

File Type:	PCT National Stage
Country of Filing:	EP
Serial #:	06708072 .1
	WO2006EP50725
Filing date:	February 7, 2006
Publication #:	WO2006084845
Status:	Application deemed to be withdrawn for failure to pay filing fee

**PATENT ASSIGNMENT**

WHEREAS, CoFluent Design, a "*société anonyme à directoire et conseil de surveillance*" registered under number 448 116 186 R.C.S. Versailles, incorporated under the laws of France, whose registered office is at 24 rue Jean Duplessis 78150 Le Chesnay - France (hereinafter "**Company**"), is the sole and exclusive owner of certain United States and/or non-United States/foreign patents and/or patent applications listed in Exhibit 1 annexed hereto (collectively referred to as the "**Patents and Patent Applications**");

AND WHEREAS, the Company and Intel Corporation, a Delaware corporation, with an office at 2200 Mission College Boulevard, Santa Clara, California 95054 (hereinafter "**Intel**") have entered into an Intellectual Property Transfer and License Agreement dated as of 30 August 2011 (the "**Transfer Agreement**");

AND WHEREAS, Intel is desirous of acquiring the right, title and interest in, to and under the said Patents and Patent Applications (and all foreign counterparts and related foreign patents).

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, the Company does hereby sell, transfer, convey, assign and deliver to Intel all of the Company's worldwide right, title and interest in and to the Patent and Patent Applications and any patents that may issue therefrom, including any foreign counterparts, divisions, continuations, or reissues of such patents, the same to be held by Intel for Intel's own use and enjoyment, and for the use and enjoyment of Intel's successors, assigns and other legal representatives, as fully and entirely as the same would have been held and enjoyed by the Company if this Patent Assignment and sale had not been made; together with all claims for damages by reason of past infringements of the Patent and Patent Applications, along with the right to sue for and collect such damages for the use and benefit of Intel and its successors, assigns and other legal representatives;

AND the Company hereby authorizes and requests the Commissioner of Patents and Trademarks of the United States, and any officer of any country or countries foreign to the United States, whose duty it is to issue patents or other evidence or forms of intellectual property protection or applications as aforesaid, to issue the same to Intel and its successors, assigns and other legal representatives in accordance with the terms of this instrument;

AND the Company does hereby covenant with Intel and the successors and permitted assigns of Intel that, from time to time after the date hereof, the Company will promptly execute and deliver to Intel or shall promptly procure the execution and delivery of any and all such instruments of sale, transfer, conveyance, assignment and delivery, consents, assurances, powers of attorney and other instruments as may reasonably be requested by Intel in order to vest in Intel all of the Company's right, title

and interest in and to the Patents and Patent Applications and carry out the purpose and intent of this Patent Assignment and the Transfer Agreement;

AND notwithstanding any other provisions of this Patent Assignment to the contrary, nothing contained in this Patent Assignment shall in any way supersede, modify, replace, amend, change, rescind, waive, exceed, expand, enlarge, or in any way affect the provisions, including warranties, covenants, agreements, conditions, representations, or in general any of the rights and remedies, or any of the obligations and indemnifications of the Company or Intel set forth in the Transfer Agreement. This Patent Assignment is intended only to effect the transfer of certain intellectual property transferred pursuant to the Transfer Agreement and shall be governed entirely in accordance with the terms and conditions of the Transfer Agreement;

AND the terms and covenants of this Patent Assignment shall inure to the benefit of Intel, its successors and assigns, and shall be binding upon the Company and its successors and assigns. All capitalized terms used herein but not otherwise defined shall have the meanings set forth in the Transfer Agreement.

[signature page follows]

IN WITNESS WHEREOF, the Company has executed this Assignment of Patent Rights with effect from 30<sup>th</sup> August 2011.

**ASSIGNOR:**

**COFLUENT DESIGN, a “société anonyme à  
directoire et conseil de surveillance”**

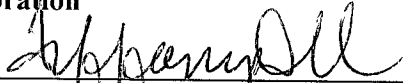
By: \_\_\_\_\_

Name: \_\_\_\_\_

Title: \_\_\_\_\_

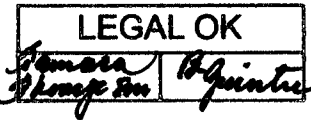
**ASSIGNEE:**

**INTEL CORPORATION, a Delaware  
corporation**

By: 

Name: TIFFANY SILVA

Title: ASSISTANT SECRETARY



SIGNATURE PAGE TO THE INTEL – COFLUENT DESIGN PATENT ASSIGNMENT

**EXHIBIT 1**  
**TO THE INTEL-COFLUENT PATENT ASSIGNMENT**

**ISSUED U.S. PATENTS**

- |     |                                 |  |
|-----|---------------------------------|--|
| (1) | U.S. Patent Number:             | 7,885,802  |
|     | Issue Date:                     | February 8, 2011   |
|     | Cofluent reference #:           | R10383US   |
|     | Priority date & priority app #: | FR0501486 14/02/05   |
|     | Inventor(s):                    | Jean-Paul CALVEZ   |
|     | Title:                          | Method for simulating a complex system including a scheduler hierarchy, and corresponding storage means and computer program product   |
|     | File Type:                      | PCT National Stage   |
|     | Country of Filing:              | US   |
|     | Serial #:                       | 11/815,829   |
|     | Filing date:                    | September 18, 2008   |
|     | Publication #:                  | US-2008-0228461-A1   |
|     | Status:                         | Issued   |
|     |                                 |  |
| (2) | U.S. Patent Number:             | 7,987,083  |
|     | Issue Date:                     | July 26, 2011  |
|     | Cofluent reference #:           | R11870US   |
|     | Priority date & priority app #: | FR0605094 08/06/06   |
|     | Inventor(s):                    | Jean-Paul CALVEZ   |
|     | Title:                          | Method for simulating a complex system with construction of at least one model including at least one modelled router, corresponding computer software package and storage means |
|     | File Type:                      | National Filing  |
|     | Country of Filing:              | US   |
|     | Serial #:                       | 11/760,252   |
|     | Filing date:                    | 08/06/07   |
|     | Publication #:                  | US-2008-0004851  |
|     | Status:                         | Issued   |
|     |                                 |  |
| (3) | U.S. Patent Number:             | 7,991,603  |
|     | Issue Date:                     | 2 August 2011  |
|     | Cofluent reference #:           | R12337US   |

Priority date & priority app #: FR0608913 11/10/06  
 Inventor(s): Jean-Paul CALVEZ  
 Title: Method for simulating a complex system with expansion of instance vectors, corresponding computer program product and storage means  
 File Type: National Filing  
 Country of Filing: US  
 Serial #: 11/870,920  
 Filing date: 11/10/07  
 Publication #: US-2008-0091401  
 Status: Issued

(4) U.S. Patent Number: 7,925,490  
 Issue Date: April 12, 2011  
 Cofluent reference #: R13081US  
 Priority date & priority app #: FR0702376 30/03/07  
 Inventor(s): Jean-Paul CALVEZ  
 Title: Method of transactional simulation of a generic communication node model, and the corresponding computer program product and storage means  
 File Type: National Filing  
 Country of Filing: US  
 Serial #: 12/059,310  
 Filing date: 31/03/08  
 Publication #: US-2008-0243464-A1  
 Status: Issued

#### **PENDING U.S. APPLICATIONS**

(1) U.S. Patent Number: n/a  
 Issue Date: n/a  
 Cofluent reference #: R11870US-2  
 Priority date & priority app #: FR0605094 08/06/06; US 11/10/07  
 11/870,920  
 Inventor(s): Jean-Paul CALVEZ  
 Title: Method for simulating a complex system with construction of at least one model including at least one modelled router, corresponding computer software package and storage means  
 File Type: Continuation

Country of Filing: US  
Serial #: 13/162,042  
Filing date: June 16, 2011  
Publication #: n/a  
Status: Pending

(2) U.S. Patent Number: n/a  
Issue Date: n/a  
Cofluent reference #: R12337US-2  
Priority date & priority app #: FR0608913 11/10/06; US 31/03/08  
12/059,310  
Inventor(s): Jean-Paul CALVEZ  
Title: Method for simulating a complex  
system with expansion of instance  
vectors, corresponding computer  
program product and storage means  
Continuation  
File Type: US  
Country of Filing: US  
Serial #: 13/161,197  
Filing date: June 15, 2011  
Publication #: n/a  
Status: Pending

**ISSUED FRENCH PATENTS**

- |     |  |  |
|-----|--|--|
| (1) | French Patent Number:<br>Issue Date:<br>Coherent reference #:<br>Priority date & priority app #:<br>Inventor(s):<br>Title:<br><br>File Type:<br>Country of Filing:<br>Serial #:<br>Filing date:<br>Publication #:<br>Status: | 2882169<br>May 25, 2007<br>R10383FR<br>n/a<br>Jean-Paul CALVEZ<br>Procédé de simulation d'un système<br>complexe incluant une hiérarchie<br>d'ordonnanceurs, produit programme<br>d'ordinateur et moyen de stockage<br>correspondants<br>National Filing<br>FR<br>0501486<br>14/02/05<br>2882169<br>Issued       |
| (2) | French Patent Number:<br>Issue Date:<br>Coherent reference #:<br>Priority date & priority app #:<br>Inventor(s):<br>Title:<br><br>File Type:<br>Country of Filing:<br>Serial #:<br>Filing date:<br>Publication #:<br>Status: | 2914525<br>May 29, 2009<br>R13081FR<br>n/a<br>Jean-Paul CALVEZ<br>Procédé de simulation<br>transactionnelle d'un modèle<br>générique de noeud de<br>communication, produit programme<br>d'ordinateur et moyen de stockage<br>correspondants<br>National Filing<br>FR<br>0702376<br>30/03/07<br>2914525<br>Issued |

**PENDING FRENCH APPLICATIONS AWAITING GRANT**

- |     |   |                               |
|-----|---|-------------------------------|
| (1) | French. Patent Number:<br>Issue Date:<br>Coherent reference #:<br>Priority date & priority app #: | n/a<br>n/a<br>R12337FR<br>n/a |
|-----|---|-------------------------------|

Inventor(s):	Jean-Paul CALVEZ
Title:	Procédé de simulation d'un système complexe avec expansion de vecteurs d'instances, produit programme d'ordinateur et moyen de stockage correspondants
File Type:	National Filing
Country of Filing:	FR
Serial #:	0608913
Filing date:	October 11, 2006
Publication #:	2907240
Status:	Grant pending

(2)	French. Patent Number:	n/a
	Issue Date:	n/a
	Cofluent reference #:	R11870FR
	Priority date & priority app #:	n/a
	Inventor(s):	Jean-Paul CALVEZ
	Title:	Procédé de simulation d'un système complexe avec construction d'au moins un modèle comprenant au moins un routeur modélisé, produit programme d'ordinateur et moyen de stockage correspondants
	File Type:	National Filing
	Country of Filing:	FR
	Serial #:	0605094
	Filing date:	June 8, 2006
	Publication #:	2902211
	Status:	Grant pending

### **WITHDRAWN EUROPEAN APPLICATION**

(1)	Patent Number:	n/a
	Issue Date:	n/a
	Cofluent reference #:	unknown
	Priority date & priority app #:	FR20050001486 14.02.2005
	Inventor(s):	Jean-Paul CALVEZ
	Title:	PROCEDE DE SIMULATION D'UN PROCESSEUR UTILISANT UN MODELE COMPRENANT UN ENSEMBLE DE OBJETS

EXECUTION VERSION

File Type:	PCT National Stage
Country of Filing:	EP
Serial #:	06708072 .1
	WO2006EP50725
Filing date:	February 7, 2006
Publication #:	WO2006084845
Status:	Application deemed to be withdrawn for failure to pay filing fee