

## PATENT ASSIGNMENT

Electronic Version v1.1  
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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
INFINEON TECHNOLOGIES AG	09/05/2006
RECEIVING PARTY DATA	
Name:	MOSAID Technologies Incorporated
Street Address:	11 Hines Road
City:	Kanata
State/Country:	ONTARIO
Postal Code:	K2K 2X1
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	5761082
CORRESPONDENCE DATA	
Fax Number:	(613)591-8148
Phone:	613-599-9539
Email:	ipadmin@mosaid.com
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
Correspondent Name:	MOSAID Technologies Incorporated
Address Line 1:	11 Hines Road
Address Line 2:	Suite 203
Address Line 4:	Ottawa, ONTARIO K2K 2X1
ATTORNEY DOCKET NUMBER:	5012-01US
NAME OF SUBMITTER:	Ronan O'Byrne
Total Attachments: 3 source=Executed_Assignment_Infineon_to_Mosaid_5Sept2006#page1.tif source=Executed_Assignment_Infineon_to_Mosaid_5Sept2006#page2.tif source=Executed_Assignment_Infineon_to_Mosaid_5Sept2006#page3.tif	

PATENT

## ASSIGNMENT OF PATENTS AND PATENT APPLICATIONS

Whereas, Infineon Technologies AG, a German corporation, having a place of business in St.-Martin-Strasse 53, 81669 Muenchen, Germany (hereafter "Assignor") is the sole owner of the entire right, title and interest in and to the Patents described in Schedule A attached hereto and made a part hereof (hereinafter "Patents"); and

Whereas, MOSAID Technologies Incorporated, a Canadian corporation, having a place of business in 11 Hines Road, K2K 2X1 Kanata, Ontario, Canada (hereafter "Assignee") desires to acquire the entire right, title and interest in and to the Patents.

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, the Assignor has sold, assigned and transferred, and by these presents does hereby sell, assign and transfer Assignor's entire right, title and interest in and to the Patents and all reissues and extensions thereof, including all claims, if any, which may have arisen for infringement of the patents prior to the date of this assignment, all said rights to be held and enjoyed by the Assignee for its own use and for the use of its successors, assigns or other legal representatives, to the full end of the term for which the Patents have been or will be granted, extended or reissued, as fully and entirely as the same would have been held and enjoyed by the Assignor if this assignment and sale had not been made

Assignor further agrees that Assignor will, without demanding any further consideration therefore, at the request but at the expense of Assignee, do all commercially reasonable lawful and just acts, including the execution and acknowledgment of instruments, that are or become necessary for obtaining, sustaining, or reissuing the Patents, and for maintaining and perfecting Assignee's right to the Patents.


IN WITNESS WHEREOF, Assignor has caused these presents to be signed by its duly authorized officers below named effective as of the date written below.

Assignor:

Infineon Technologies AG

  
Dr. Dieter Joseph  
Senior Principal

Date: Munich, 5th September 2006

  
Joe Villella  
Vice President

Assignee:

MOSAID Technologies Incorporated

Date: \_\_\_\_\_

# SCHEDULE A

## UNITED STATES PATENTS

Patent Number	Date of Grant	Filing No	Filing Date	Title
US 4859875	22.08.1989	07/236,076	24.08.1988	Optocoupler
US 5047355	10-Sep-91	07/406,826	12-Sep-89	A Semiconductor Diode and Method for Making it
US 5210699	11.05.1993	07/759,190	10.09.1991	Process for Extracting Logic From Transistor and Resistor Data Representations of Circuits
US 5311407	10.05.1994	07/876,640	30.04.1992	Printed Circuit Board For Mounting Semiconductors And Other Electronic Components
US 5344794	06.09.1994	08/128,424	31.03.1993	A Pedestal Lead Frame for Supporting A Semiconductor Chip
US 5357674	25.10.1994	08/101,637	04.08.1993	Method Of Manufacturing A Printed Circuit Board
US 5446295	29.08.1995	08/110,608	23.08.1993	Silicon Controlled Rectifier with A Variable Base-Shunte Resistance
US 5506152	09.04.1996	08/299,717	23.08.1993	Method Of Making Silicon Controlled Rectifier With A Variable Baseshunt Resistance
US 5506425	09.04.1996	08/355,452	13.12.1994	Semiconductor Device And Lead Frame Combination
US 5723363	03.03.1998	08/537,062	29.09.1995	A Method Of Manufacturing A Semiconductor Device
US 5761082	02.06.1998	08/799,493	12.02.1997	Method for manufacturing an integrated circuit
US 5777482	07.07.1998	08/675,590	03.07.1996	Circuit arrangement and method for measuring a difference in capacitance between a first and a second capacitance
US 5855924	05.01.1999	08/579,535	27.12.1995	Closed-Mold For LED Alphanumeric Displays
US 5965929	12.10.1999	08/623,905	27.03.1996	BIPOLARER SILIZIUMTRANSISTOR
US 6100858	08.08.2000	08/940,703	30.09.1997	Alphanumeric Display With 21-Dot Matrix Format
US 6400194	04.06.2002	09/642,332	21.08.2000	Synchronous feedback digital circuit having a minimized switching power loss
US 6526428	25.02.2003	10/036,246	22.10.2001	Method and apparatus for determining interpolated intermediate values of a sampled signal

*Patents of Germany, France, Japan, Korea:*

Patent Number	Date of Grant	Filing No	Filing Date	Title
DE 3881788	16.06.1993	88113996.8	26.08.1988	Optocoupler
DE 69320090	05.08.1998	93105688.1	06.04.1993	Printed Circuit Board For Mounting Semiconductors And Other Electronic Components
FR 304951	16.06.1993	88113996.8	26.08.1988	Optocoupler
FR 567814	05.08.1998	93105688.1	06.04.1993	Printed Circuit Board For Mounting Semiconductors And Other Electronic Components
JP 1924130	25.04.1995	63-213502	25.08.1988	Optocoupler
JP 3095783	04.08.2000	09-524347	17.12.1996	Closed-Mold For LED Alphanumeric Displays
KR 326487	18.02.2002	704208/95	31.03.1994	A Pedestal Lead Frame for Supporting A Semiconductor Chip