PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Intellectual Ventures DRAM 1 LLC	09/26/2011

RECEIVING PARTY DATA

Name:	Talon Research, LLC	
Street Address:	4455 Camp Bowie Boulevard	
Internal Address:	ess: #74	
City:	Fort Worth	
State/Country:	TEXAS	
Postal Code:	76107	

PROPERTY NUMBERS Total: 21

Property Type	Number
Patent Number:	5469558
Patent Number:	6799246
Patent Number:	5740460
Patent Number:	5801973
Patent Number:	5828907
Patent Number:	5821885
Patent Number:	5798719
Patent Number:	6217234
Patent Number:	5703793
Patent Number:	5835792
Patent Number:	5984512
Patent Number:	5963154
Patent Number:	5995727
Patent Number:	6141721
	DATENT

REEL: 027427 FRAME: 0327

PATENT

Patent Number:	6119213
Patent Number:	5829007
Patent Number:	5768629
Patent Number:	5603012
Patent Number:	5699544
Patent Number:	5761741
Patent Number:	6330666

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NAME OF SUBMITTER: Marc Belloli

Total Attachments: 4

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ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Intellectual Ventures DRAM 1 LLC, a Delaware limited liability company, having an address at 2711 Centerville Road, Suite 400, Wilmington, DE 19808 ("Assignor"), does hereby sell, assign, transfer, and convey unto Talon Research, LLC, a Delaware limited liability company having offices at 4455 Camp Bowie Blvd. #74, Fort Worth, TX 76107 ("Assignee"), all of Assignor's right, title, and interest in and to the following (collectively, the "Assigned Patent Rights"):

(a) the patents listed in the table below (the "Patents");

			<u>Title of Patent and First</u>
Patent No.	Country	Filing Date	<u>Named Inventor</u>
5469558	US	4/18/1994	Dynamically reconfigurable
			memory system with
			programmable controller and
			FIFO buffered data channels
			Donald A. Lieberman
DE69230134	DE	8/14/1992	High-performance dynamic
			memory system
			Donald A. Lieberman
FR0553338	FR	8/14/1992	High-performance dynamic
			memory system
			Donald A. Lieberman
GB0553338	GB	8/14/1992	High-performance dynamic
			memory system
			Donald A. Lieberman
IT0553338	IT	8/14/1992	High-performance dynamic
		ŕ	memory system
			Donald A. Lieberman
NL0553338	NL	8/14/1992	High-performance dynamic
			memory system
			Donald A. Lieberman
JP3599334	JP	8/14/1992	High-performance dynamic
			memory system
			Donald A. Lieberman
6799246	US	12/16/1997	A Memory interface for
		ı	reading/writing data from/to a
			memory
			Adrian Philip Wise
5740460	US	6/7/1995	Arrangement for processing
			packetized data
			Adrian Philip Wise
5801973	US	6/7/1995	Method for Accessing a
			Memory Using a
			Programmable State Machine

Title of Patent and First

Dotord No	Country	Filing Data	Named Inventor
Patent No.	Country	Filing Date	The state of the s
5000007	TIG	6/5/1005	Adrian P. Wise
5828907	US	6/7/1995	Token-based adaptive video
			processing arrangement
			Adrian P. Wise
5821885	US	6/7/1995	Video decompression
			Adrian P. Wise
5798719	US	6/7/1995	Parallel Huffman decoder
			Adrian P, Wise
6217234	US	6/7/1995	Apparatus and method for
			processing data with an
			arithmetic unit
			Anthony Mark Jones
5703793	US	6/7/1995	Video decompression
			Adrian P. Wise
5835792	US	6/7/1995	Token-based adaptive video
			processing arrangement
			Adrian P. Wise
KR10-0270798	KR	7/29/1995	Video decompression
1410-02/0790	TKK	112711773	Adrian P. Wise
5984512	US	6/7/1995	Method for storing video
3984312	US	0/7/1993	information
50/0154	TIC	6/7/1005	Anthony Mark Jones
5963154	US	6/7/1995	Technique for decoding
			variable and fixed length codes
			Adrian P. Wise
MX195907	MX	7/27/1995	Video decompression
			Adrian P. Wise
5995727	US	10/7/1997	Video decompression
			Adrian P. Wise
6141721	US	3/18/1999	Method of asynchronous
			memory access
			Donald William Patterson
6119213	US	6/7/1995	Method for addressing data
			having variable data width
			using a fixed number of bits
			for address and width defining
	·		fields
			William P. Robbins
5829007	US	6/7/1995	Technique for implementing a
JOMPOUT		0.111220	swing buffer in a memory
			array
			Anthony Mark Jones
CD2202076	GB	6/7/1995	Time Synchronisation In A
GB2293076	QB	01111993	Multiplexed Data Stream
			Manufacta Data Su cam

Title o	of Patent	and	First
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Patent No.	Country	Filing Date	Named Inventor
			Adrian Philip Wise
5768629	US	6/7/1995	Token-based adaptive video processing arrangement Adrian P. Wise
GB2287808	GB	2/28/1995	Method and apparatus for interfacing with RAM Adrian Philip Wise
GB2288957	GB	2/28/1995	Start code detector Adrian Philip Wise
5603012	US	3/7/1995	Start code detector Martin W. Sotheran
5699544	US	6/7/1995	Method and apparatus for using a fixed width word for addressing variable width data Adrian P Wise
5761741	US	6/7/1995	Technique for addressing a partial word and concurrently providing a substitution field Adrian P Wise
6330666	US	10/7/1997	Multistandard video decoder and decompression system for processing encoded bit streams including start codes and methods relating thereto Adrian P Wise

- (b) any future reissues, reexaminations, extensions, continuations, continuing prosecution application, requests for continuing examinations, divisions, and registrations of any of the Patents;
- (c) rights to apply in any or all countries of the world for future patents, certificates of invention, utility models, industrial design protections, design patent protections, or other future governmental grants or issuances of any type related to the Patents; and
- (d) causes of action and enforcement rights of any kind under, or on account of, any of the Patents and/or any of the items described in either of the foregoing categories (b) or (c), including, without limitation, all causes of action, enforcement rights and all other rights to seek and obtain any other remedies of any kind for past, current and future infringement; and

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all future patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Assigned Patent Rights in the name of Assignee, as the assignee to the entire interest therein. This Assignment of Patent Rights will inure for the benefit of any permitted successors or assigns of Assignee.

Assignor will, at the reasonable request of Assignee, do all things necessary and proper, to confirm the assignment to Assignee of the Assigned Patent Rights pursuant to this Assignment of Patent Rights, including without limitation, the execution, acknowledgment, and recordation of specific assignments, oaths, declarations, and other documents on a country-by-country basis, to assist Assignee in obtaining and perfecting the Assigned Patent Rights

IN WITNESS WHEREOF this Assignment of Patent Rights is executed on September 2011.

ASSIGNOR:-

INTELL'ECTUAL VENTURES DRAM 1 LLC

∕By: Name;

HUTHURIZED PERSON

PATENT

REEL: 027427 FRAME: 0332