

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT														
NATURE OF CONVEYANCE:	ASSIGNMENT														
CONVEYING PARTY DATA															
<table border="1"> <thead> <tr> <th>Name</th> <th>Execution Date</th> </tr> </thead> <tbody> <tr> <td>Ryan T. HIROSE</td> <td>12/23/2011</td> </tr> <tr> <td>Bogdan GEORGESCU</td> <td>12/27/2011</td> </tr> <tr> <td>Leonard GITLAN</td> <td>12/22/2011</td> </tr> <tr> <td>Ashish AMONKAR</td> <td>12/27/2011</td> </tr> <tr> <td>Gary MOSCALUK</td> <td>12/23/2011</td> </tr> <tr> <td>John TIEDE</td> <td>12/22/2011</td> </tr> </tbody> </table>		Name	Execution Date	Ryan T. HIROSE	12/23/2011	Bogdan GEORGESCU	12/27/2011	Leonard GITLAN	12/22/2011	Ashish AMONKAR	12/27/2011	Gary MOSCALUK	12/23/2011	John TIEDE	12/22/2011
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CORRESPONDENCE DATA															
<p>Fax Number: (408)545-6911</p> <p>Phone: 408-545-6962</p> <p>Email: pbj@cypress.com</p> <p><i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i></p> <p>Correspondent Name: CYPRESS SEMICONDUCTOR CORPORATION</p> <p>Address Line 1: 198 CHAMPION COURT</p> <p>Address Line 4: SAN JOSE, CALIFORNIA 95134-1709</p>															
ATTORNEY DOCKET NUMBER:	CD11092														

CH \$40.00 13340248

NAME OF SUBMITTER:

Jason R. Graff

Total Attachments: 3

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ASSIGNMENT OF PATENT APPLICATION

Attorney Docket No.: CD11092

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I/we, the undersigned, Ryan T. Hirose, Ashish Amonkar, Bogdan Georgescu,
Leonard Gitlan, Gary Moscaluk, and John Tiede,

hereby sell, assign, and transfer to Cypress Semiconductor Corporation

a corporation of Delaware,
(Type of Assignee: e.g., corporation, company, partnership, university, etc.)

having a principal place of business at 198 Champion Court, San Jose, California 95134-1709

("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all foreign countries, in and to any and all inventions or improvements that are disclosed in the application (provisional or non-provisional) for the United States patent which may have a declaration executed by the undersigned prior hereto or concurrently herewith and is entitled

SYSTEMS AND METHODS FOR PROVIDING HIGH VOLTAGE TO
MEMORY DEVICES

said patent application also identified as follows (when known):

[I/we hereby authorize an attorney or agent for said Assignee to insert below the application number and filing date of said patent application when known.]

United States Patent Application Number 13/340,248 filed 2011-12-29,

and in and to said application (provisional or non-provisional) and all provisional applications, non-provisional applications, utility applications, design applications, divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions, and all other patent applications that have been or shall be filed in the United States and all foreign countries on any of said inventions or improvements; and in and to all original patents, reissued patents, reexamination certificates, and extensions, that have been or shall be issued in the United States and all foreign countries on said inventions or improvements; and in and to all rights of priority resulting from the filing of said United States application;

agree that said Assignee may apply for and receive a patent or patents for said inventions or improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all provisional applications, non-provisional applications, utility applications, design applications, divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions, and all other patent applications on any and all said inventions or improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and representatives all facts known to the undersigned relating to said inventions or improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or representatives in securing and maintaining proper patent protection for said inventions or improvements and for vesting title to said inventions or improvements, and all applications for patents and all patents on said inventions or improvements, in said Assignee, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Each Inventor: Please Sign and Date Below:

12/23, 20¹¹
Date

Ryan T. Hirose
Signature

Ryan T. HIROSE

Printed/Typed Name

12/27/, 20¹¹
Date

Bogdan Georgescu
Signature

Bogdan GEORGESCU

Printed/Typed Name

12/22, 20¹¹
Date

Leonard Gitlan
Signature

Leonard GITLAN

Printed/Typed Name

Assignment Document Return Address:

CYPRESS SEMICONDUCTOR CORPORATION
198 Champion Court
San Jose, CA 95134
(408) 545-7194

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Each Inventor: Please Sign and Date Below:

12/27 20¹¹
Date

Ashish A. Amorkar
Signature

Ashish AMONKAR

Printed/Typed Name

12/23 20¹¹
Date

Gary Moscaluk
Signature

Gary MOSCALUK

Printed/Typed Name

Dec 22 20¹¹
Date

John W. Tiede
Signature

John TIEDE

Printed/Typed Name

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