

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	Deed of Transfer
CONVEYING PARTY DATA	
Name	Execution Date
STMicroelectronics NV	07/28/2008
RECEIVING PARTY DATA	
Name:	ST-Ericsson SA
Street Address:	Chemin du Champ-des-Filles 39
City:	Plan-Les-Ouates
State/Country:	SWITZERLAND
Postal Code:	1228
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	12021689
CORRESPONDENCE DATA	
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<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.</i>	
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ATTORNEY DOCKET NUMBER:	S102271223US00
NAME OF SUBMITTER:	Gail Driscoll
Total Attachments: 12 source=12021689#page1.tif source=12021689#page2.tif source=12021689#page3.tif	

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DEED OF TRANSFER FOR ST PATENTS

WHEREAS, STMicroelectronics NV, a public company with limited liability incorporated under the laws of the Netherlands, with corporate seat in Amsterdam, the Netherlands, and address at WTC Schiphol Airport, Schiphol Boulevard 265, 1118 BH Schiphol Airport, Amsterdam, the Netherlands, and

STMicroelectronics S.A., (formerly known as SGS-Thomson Microelectronics S.A.), a corporation organized under the laws of France, having a place of business at 29 Boulevard Romain Rolland, 92120 Montrouge, France,

(hereinafter collectively referred to as "Assignor") has rights in and to the following:

LISTED IN EXHIBIT A

(said patents, patent applications and invention disclosures listed in Exhibit A being hereinafter referred to as "SAID PATENT RIGHTS"); and

WHEREAS, ST Wireless SA, a legal entity incorporated under the laws of Switzerland, and address at 39 Chemin du Champ-des-Filles, 1228 Plan-les-Ouates, Geneva, Switzerland (hereinafter referred to as "Assignee") is desirous of obtaining rights to SAID PATENT RIGHTS;

NOW, THEREFORE, for and in consideration of good and valuable consideration the receipt, sufficiency and adequacy of which are hereby acknowledged, Assignor does hereby sell, convey, transfer and assign to Assignee, its successors, assigns and legal representatives, subject to prior encumbrances, all its right, title and interest in and to SAID PATENT RIGHTS, together with its right to claim priority, together with any and all continuations, continuations-in-part, continuing prosecution applications, requests for continuing examinations, divisions, reissues, reexaminations, extensions, registrations, and foreign counterparts of any item in any of the foregoing, together with its right to sue for and be entitled to any

damages, injunctive relief, and any other remedies of any kind for past, current and future infringement thereof. Assignee hereby accepts such conveyance, transfer and assignment of SAID PATENT RIGHTS.

Signed this 28th day of July, 2008.

STMicroelectronics NV

By: [Signature]

Printed Name: Orsini

Title: General Counsel

Place: Geneva

STMicroelectronics S.A. (formerly known
as SGS-Thomson Microelectronics S.A.)

By: [Signature]

Printed Name: P. CHAETAGNER

Title: Chairman

Place: Grande, France.

ST Wireless SA

By: [Signature]

Printed Name: R. R. ROBERT

Title: Director

Place: Geneva

EXHIBIT A

US Patent	Country	Class	Status	Pub. No.	Pub. Date	Title
3823890	US	330	Granted	3823890	08/25/74	BIASING OF A DIODE
3823890	US	330	Granted	3823890	08/25/74	VARIABLE-CURRENT DIFFERENTIAL INPUT AND OUTPUT AMPLIFIER
3823890	US	330	Granted	3823890	08/25/74	LOW ELECTRICAL CONSUMPTION VOLTAGE REGULATOR
3823890	US	330	Granted	3823890	08/25/74	LOW-POWER OPERATIONAL AMPLIFIER
3823890	US	330	Granted	3823890	08/25/74	TRANSISTOR-BASED STAGE AND DEVICE FOR COMMUNICATION BY WIRELESS CHANNEL EQUIPPED WITH SUCH A STAGE
3823890	US	330	Granted	3823890	08/25/74	CONTROLLED CURRENT SOURCE ASSEMBLY
3823890	US	330	Granted	3823890	08/25/74	INTEGRATED CIRCUIT WITH REDUCED COUPLING VIA THE SUBSTRATE
3823890	US	330	Granted	3823890	08/25/74	VOLTAGE REGULATING DEVICE AND PROCESS
3823890	US	330	Granted	3823890	08/25/74	DIFFERENTIAL SIGNAL MULTIPLIER CIRCUIT
3823890	US	330	Granted	3823890	08/25/74	LOW-POWER POWER SUPPLY DEVICE ADAPTED FOR LOW CURRENT DRAINS AND CELLULAR PHONE EQUIPPED WITH SUCH A DEVICE
3823890	US	330	Granted	3823890	08/25/74	DATA RECEIVER HAVING SEVERAL STAGES AND METHOD OF PROCESSING TRANSMITTED SIGNAL THEREIN
3823890	US	330	Granted	3823890	08/25/74	METHOD OF ADJUSTING THE CUTOFF FREQUENCY OF AN ELECTRONIC FILTERING SYSTEM AND CORRESPONDING SYSTEM
3823890	US	330	Granted	3823890	08/25/74	METHOD AND DEVICE FOR BIASING A TRANSISTOR OF A HIGH FREQUENCY AMPLIFIED STAGE
3823890	US	330	Granted	3823890	08/25/74	DEVICE AND METHOD FOR GENERATING DIGITAL SIGNALS EACH CODING A VALUE OF AN ANALOG SIGNAL
3823890	US	330	Granted	3823890	08/25/74	METHOD OF GENERATING A VOLTAGE WAVE AT THE TERMINALS OF A CAPACITOR AND CORRESPONDING DEVICE
3823890	US	330	Granted	3823890	08/25/74	AMPLIFIER CIRCUIT DEVICE FOR ELECTRICAL OSCILLATOR AND ELECTRICAL OSCILLATOR COMPRISING SUCH A DEVICE
3823890	US	330	Granted	3823890	08/25/74	PROCESS AND DEVICE FOR CONTROLLING THE OPERATION OF A BIPOLAR TRANSISTOR IN CLASS A

SECRET A

Patent No.	Class.	Pub. No.	Pub. Date	Pub. No.	Pub. Date	Title
371452	120	Granted		371452		METHOD AND DEVICE FOR GENERATING A SIGNAL WITH A FREQUENCY EQUAL TO THE PRODUCT OF A REFERENCE FREQUENCY AND A REAL NUMBER
371530	120	Granted	4411339	371530.1	10/47/80	CIRCUIT FOR DIGITAL SIGNAL-LEVEL MODULATOR AND DIGITAL FREQUENCY SYNCHRONIZER INCORPORATING THE SAME
371537	120	Granted		371537		CLOSED-LOOP CONTROL OF AN ANALOG SYSTEM PROVIDING AN OUTPUT SIGNAL WITH A CONTROL SIGNAL
371545	120	Granted		371545.4	10/47/80	METHODS FOR CONTROL OF STANDING CURRENTS IN A DIRECT CONVERSION TYPE OF FREQUENCY TRANSDUCING DEVICE AND CORRESPONDING DEVICE
371550	120	Granted		371550.2	10/47/80	IMPEDANCE MATCHING IN PROJECTION
371557	120	Granted		371557.4	10/47/80	DEVICE FOR TRANSDUCING FREQUENCY SIGNALS
371561	120	Granted	4411339	371561.2	10/47/80	IMPROVED METHOD
371562	120	Granted		371562.1	10/47/80	LOW-POWER AMPLIFIER
371563	120	Granted		371563.1	10/47/80	VERY LOW-POWER COMPARISON DEVICE
371567	120	Granted		371567.2	10/47/80	VERY LOW-POWER COMPARISON DEVICE
371568	120	Granted		371568.1	10/47/80	DATA TRANSMISSION METHOD
371569	120	Granted		371569.1	10/47/80	METHOD FOR DETERMINING THE FREQUENCY STABILITY NOISE FROM A SOURCE AND A DEVICE FOR IMPLEMENTING THE METHOD
371570	120	Granted		371570		LOW-TOKEN LOAD PUMP FOR PHASE-LOCKING LOOP
371572	120	Granted		371572.2	10/47/80	VOLTAGE REGULATOR WITH REDUCED OPEN-LOOP STATIC GAIN
371573	120	Granted		371573.4	10/47/80	VOLTAGE REGULATOR WITH ENHANCED STABILITY
371574	120	Granted	4411339	371574	10/47/80	VOLTAGE REGULATOR WITH AN IMPROVED EFFICIENCY
371575	120	Granted	4411339	371575	10/47/80	VOLTAGE REGULATOR INCORPORATING A STABILIZATION RESISTOR AND A CIRCUIT FOR LIMITING THE OUTPUT CURRENT
371576	120	Granted		371576	10/47/80	PHASE-LOCKING LOOP WITH HIGH FREQUENCY EQUIPMENT OF THE OPERATING RANGE OF THE OSCILLATOR
371577	120	Granted		371577	10/47/80	IMPROVED CIRCUIT FOR OPERATED AT HIGH FREQUENCIES
371578	120	Granted		371578	10/47/80	LOW-POWER VOLTAGE REGULATOR

REPORT A

US Patent Number	Country	Priority Status	Foreign Patent Number	Foreign Patent Number	Title
4829297	US	Granted		4829297	CIRCUIT FOR SWITCHING SWITCHED MODE POWER AND METHODS FOR CONTROLLING A SWITCHING POWER SUPPLY AND CORRESPONDING SWITCHING POWER SUPPLY
4837452	US	Granted		4837452	METHOD AND CIRCUIT FOR CORRECTING THE OFFSET OF AN AMPLIFICATION CIRCUIT
4838649	US	Granted	2828888	4838649	CIRCUITS FOR PROTECTING ELECTRONIC CIRCUITS AGAINST ELECTROSTATIC DISCHARGES AND METHODS OF OPERATING THE SAME
4837726	US	Granted	2828888	4837726	DIFFERENTIAL AMPLIFIER WITH A COMMON MODE VOLTAGE LOOP
4833324	US	Granted	2828888	4833324	TEMPERATURE DETECTION CELL AND METHOD TO DETERMINE THE DETECTION THRESHOLD OF SUCH A CELL
4832326	US	Granted	2828888	4832326	ELECTRONIC CIRCUIT WITH A DIFFERENTIAL PAIR OF TRANSISTORS AND LOGIC GATE CONNECTIONS SUCH A CIRCUIT
4832412	US	Granted		4832412	WIND ADAPTATION DEVICE WITH A METHOD OF INTEGRATED CIRCUIT WITH RESISTIVE NETWORK HAVING RESISTOR NETWORK
4832322	US	Granted	2828888	4832322	METHOD OF CORRECTING THE PHASE DIFFERENCE BETWEEN TWO INPUT SIGNALS OF A PHASE-LOCKED LOOP AND ASSOCIATED DEVICE
4832324	US	Granted		4832324	INTEGRATED CIRCUIT WITH MODULABLE LOAD DROPOUT VOLTAGE REGULATOR
4832324	US	Granted	2828888	4832324	AUTO-ADJUSTMENT OF THE CELL WITHIN A CIRCUIT
4832324	US	Granted	2828888	4832324	AMPLIFIER ARCHITECTURE AND APPLICATION THEREOF TO A BAND-GAP VOLTAGE GENERATOR
4832324	US	Granted	2828888	4832324	STRUCTURE FOR TESTING INTEGRATED CIRCUITS
4832324	US	Granted	2828888	4832324	DIFFERENTIAL ACTIVE FILTER OF THE SECOND ORDER
4832324	US	Granted	2828888	4832324	FUNCTIONAL LOGIC CIRCUIT DETERMINATION OF AN EXCITATION VECTOR IN SELF-EXCITED
4832324	US	Granted	2828888	4832324	DEVICE FOR CHANGING THE ACCESS TO A MEMORY BUS
4832324	US	Granted	2828888	4832324	APPARATUS FOR POSTPROCESSING OF INTERRUPTS BY A MICROPROCESSOR
4832324	US	Granted	2828888	4832324	LOW-VOLTAGE JUNCTION SWITCH HAVING SELECTIVE BULK BIASING PROPERTY
4832324	US	Granted	2828888	4832324	INTERNAL PROCESSING SYSTEM
4832324	US	Granted	2828888	4832324	VOLTAGE REGULATOR



SECRET A

US Patent	Country	Class. Basis	Serial Number	Class Number	Title
3971474	US	Classical		3971474	NETWORK AND METHOD OF NETWORK TRANSFER BETWEEN CIRCUITS THAT EXCHANGE DATA VIA CONNECTIONS
3971475	US	Classical		3971475	SYSTEM AND METHOD FOR THE DYNAMIC CALCULATION OF THE
3971476	US	Classical		3971476	ELECTRICAL CONNECTION BETWEEN TWO ELECTRICALLY CONNECTED IN ELECTRONIC COMPONENT
3971477	US	Classical		3971477	PROTECTION DEVICE AGAINST ELECTROSTATIC DISCHARGES
3971478	US	Classical	3726281	3971478	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971479	US	Classical		3971479	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971480	US	Classical	3726282	3971480	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971481	US	Classical		3971481	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971482	US	Classical	3726283	3971482	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971483	US	Classical		3971483	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971484	US	Classical		3971484	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971485	US	Classical		3971485	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971486	US	Classical		3971486	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971487	US	Classical		3971487	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971488	US	Classical		3971488	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971489	US	Classical		3971489	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971490	US	Classical		3971490	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971491	US	Classical		3971491	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971492	US	Classical		3971492	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971493	US	Classical		3971493	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971494	US	Classical		3971494	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971495	US	Classical		3971495	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971496	US	Classical		3971496	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971497	US	Classical		3971497	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971498	US	Classical		3971498	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971499	US	Classical		3971499	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION
3971500	US	Classical		3971500	VOLTAGE REGULATOR FOR CURRENT SOURCE GENERATION INCLUDING SUCH A GENERATION

Exhibit A (continued)

Invention Disclosure Title
INTEGRATED RC TIME CONSTANT MEASUREMENT
SUPPLY PUSHING COMPENSATION IN SWITCHED CAPACITORS USED FOR FREQUENCY CALIBRATION OF OSCILLATORS
REFERENCE NOISE SOURCE MINIMISATION IN PDM SIGMA-DELTA CONVERSION
INEXPENSIVE CURRENT-LIMIT BASED ON PMOS SENSING
HIGH EFFICIENCY NON INVERTING BUCK BOOST CONVERTER WITH CURRENT MODE CONTROL
GATE ASSISTED NPN BJT FOR BOOTSTRAP CAPACITOR LOADING OF BUCK DC/DC CONVERTERS USING DUAL NMOS IN ADVANCED CMOS TECHNOLOGIES
TOPOLOGY FOR SINGLE-ENDED "CAPLESS" CLASS D AMPLIFIER
AUTO-CALIBRATION SYSTEM OF THE RC TIME CONSTANT FOR A CONTINUOUS-TIME DELTA-SIGMA ADC
INNOVATIVE CLOCK AND POWER CONTROL TECHNIQUE TO REDUCE THE POWER SUPPLY WAKEUP SYSTEM LATENCY OF A DIGITAL BLOCK
ADAPTIVE INTERPOLATION DECISION FEEDBACK CT CANCELLER
INNOVATIVE DEBUG INTERFACE FOR PROCESSORS WITH POWER ON/OFF CAPABILITIES
FAST RECEIVER APPROACH FOR DIGITAL CONTROLLED SMPs
FREQUENCY INTERPOLATION FILTER FOR AN OFDM RECEIVER
SEMI-DIGITAL ON TOP IN A CONTEXT OF ANALOG AND MIXED DESIGNS
INTERNAL HIGH BANDWIDTH LINK BASED ON STANDARD INTERFACE
SUPPLY PUSHING COMPENSATION IN SWITCHED CAPACITORS USED FOR FREQUENCY CALIBRATION OF OSCILLATORS



ST-Ericsson SA

inscrite le 14 juillet 2008

Société anonyme

Raison Sociale	
1	ST-Wireless SA
6	ST-Ericsson SA (ST-Ericsson Ltd)
Siège	
1	Plan-les-Ouates
Adresse	
1	chemin du Champ-des-Filles 39, 1228 Plan-les-Ouates
Dates des Statuts	
1	11.07.2008
2	22.07.2008
3	28.07.2008
6	06.03.2009 (nouv. stat.)
But, Observations	
1	<u>But:</u> dans le domaine des technologies sans fil, recherche, développement, fabrication, achat et vente de matériaux semi-conducteurs, de systèmes électroniques et de matériels analogues, acquisition, administration, mise en valeur et vente de participations de toutes sociétés ou entreprises dont les activités sont similaires, ou de toutes sociétés financières, ou de biens immobiliers, dans le respect des prescriptions de la LFAIE (cf. statuts pour but complet).
6	<u>But:</u> développement, production et/ou vente de logiciels et de circuits intégrés basés sur les semi-conducteurs (IC) et destinés en priorité aux composants de communication cellulaire sans fil ainsi qu'aux services y relatifs (cf. statuts pour but complet).
Organe de publication	
1	Feuille Officielle Suisse du Commerce
1	Communication aux actionnaires: par lettre
Succursales	
4	Zurich

Réf.	Capital-actions		
	Nominal	Libéré	Actions
1	CHF 100'000	CHF 100'000	100 actions de CHF 1'000, nominatives
2	CHF 92'251'000	CHF 92'251'000	92'251 actions de CHF 1'000, nominatives (augmentation ordinaire)
3	CHF 273'135'000	CHF 273'135'000	273'135 actions de CHF 1'000, nominatives (augmentation ordinaire)
Apports en nature, reprises de biens, avantages particuliers			
2	<u>Apport en nature:</u> selon contrat du 22.07.2008, la branche d'activité de la société STMicroelectronics N.V., à Amsterdam, NL, liée aux circuits intégrés, comprenant notamment divers équipements d'ingénierie, les droits transférables découlant de contrats de licence, divers droits de propriété intellectuelle, goodwill et des biens d'équipements provenant de diverses usines à l'étranger pour CHF 412'800'800, en contrepartie duquel sont remises 92'151 actions de CHF 1'000, nominatives, le solde constituant un agio.		

Apports en nature, reprises de biens, avantages particuliers

- 3 Apport en nature:
selon contrat du 28.07.2008, la branche d'activité acquise du groupe hollandais NXP liée aux circuits intégrés comprenant notamment divers équipements d'ingénierie, les droits transférables découlant de contrats de licence, divers droits de propriété intellectuelle, goodwill, ainsi que divers biens (stock de puces électroniques en cours de production et d'assemblage, produits semi-conducteurs semi-finis et finis selon inventaires), en Europe (Allemagne, Suède et Finlande) et en Asie (Thaïlande et Hong-Kong); des biens d'équipement d'un centre de R&D (Recherche & Développement) à Zurich, et deux créances, le tout pour le prix de CHF 619'527'700, en contrepartie duquel sont remises 180'884 actions de CHF 1'000, nominatives, le solde de CHF 438'643'700 constituant un agio.
- 3 Reprise de biens envisagée:
la branche d'activité à Taiwan liée aux circuits intégrés du groupe hollandais NXP comprenant notamment divers équipements d'ingénierie, divers biens (stock de puces électroniques en cours de production et d'assemblage, produits semi-conducteurs semi-finis et finis selon inventaires) ainsi qu'un goodwill pour le prix maximal de CHF 210'000'000.

Réf.			Administration, organe de révision et personnes ayant qualité pour signer		
Inscr	Mod	Rad.	Nom et Prénoms, Origine, Domicile	Fonctions	Mode Signature
1			Dutheil Alain, de France, à Genève	adm. président	signature individuelle
1		5	Ollivier Pierre, de France, à Genève	adm.	signature individuelle
1		5	Richter Robert, de Randogne, à Mollens (VS)	adm.	signature individuelle
5			Champseix Jean-Louis, de France, à Saint-Julien-en-Genevois, F	adm.	signature collective à 2
5			Lucie-Smith Timothy, de Grande-Bretagne, à Genève	adm.	signature collective à 2
5			Puskaric Robert, de Suède, à Lund, SWE	adm.	signature collective à 2
1			PricewaterhouseCoopers SA (CH-660-1784998-4), succursale à Genève	organe de révision	

Réf.	JOURNAL		PUBLICATION FOISC		Réf.	JOURNAL		PUBLICATION FOISC	
	Numéro	Date	Date	PageId		Numéro	Date	Date	PageId
1	9288/660	14.07.2008	18.07.2008	8/4580318	2	9738/660	23.07.2008	29.07.2008	7/4593982
3	10112/660	04.08.2008	08.08.2008	7/4605900	4	10846/660	22.08.2008	28.08.2008	8/4629372
5	4267/660	19.03.2009	25.03.2009	10/4942788	6	4910/660	01.04.2009	07.04.2009	11/4963474

Genève, le 21 octobre 2009

Fin de l'extrait

Il est possible d'obtenir un extrait complet avec mention des éventuelles radiations sur demande auprès du registre.